PESD4USB3UBTTS-Q

Extremely low capacitance unidirectional ESD protection diode array

25 October 2023

Product data sheet

1. General description

This unidirectional ESD protection device is designed to protect high-speed interfaces such as SuperSpeed USB 3.2, HDMI, DisplayPort, external Serial Advanced Technology Attachment (eSATA), Low Voltage Differential Signaling (LVDS), and Gigabit Multimedia Serial Link (GMSL) Serializer/Deserializer (SerDes) against ElectroStatic Discharge (ESD).

The device is encapsulated in a leadless small DFN2510D-10 (SOT1165D) plastic package and provides ESD protection up to 15 kV exceeding IEC 61000-4-2 level 4 and fulfilling ISO 10605.

2. Features and benefits

- System-level ESD protection for USB 2.0 and SuperSpeed USB 3.2, HDMI, DisplayPort, eSATA and LVDS
- V_{RWM} = 3.3 V device
- · Typical line capacitance of only 0.4 pF
- Outstanding system protection: extremely deep snap-back combined with dynamic resistance of 0.2 Ω
- ESD protection level up to ±15 kV (IEC 61000-4-2, level 4; ISO 10605)
- Matched 0.5 mm trace spacing and side-wettable flanks (SWF) for AOI
- Design-friendly 'pass-through' signal routing
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Infotainment systems: USB 2.0, USB 3.2 and HDMI 2.1
- Automotive A/V monitors, display and cameras
- SerDes: GMSL, APIX, FPD-Link and LVDS

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage			-	-	3.3	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	0.4	0.5	pF

[1] Measured on pin 1



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	CH1	channel 1 ESD protection		CH1 CH3 CH2 CH4
2	CH2	channel 2 ESD protection		
3	GND	ground		本本本
4	CH3	channel 3 ESD protection	10 9 8 7 6	GND
5	CH4	channel 4 ESD protection		←
6	n.c.	not connected		\\
7	n.c.	not connected	Transparent top view	* = \$
8	GND	ground	DFN2510D-10 (SOT1165D)	★ = ★
9	n.c.	not connected		
10	n.c.	not connected		<u> </u>
				aaa-016329

6. Ordering information

Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PESD4USB3UBTTS-Q	DFN2510D-10	plastic, leadless thin small outline package with Side-Wettable Flanks (SWF); 10 terminals; 0.5 mm pitch; 2.5 mm x 1 mm x 0.75 mm body	SOT1165D	

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD4USB3UBTTS-Q	Q2D

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{RWM}	reverse standoff voltage			-	3.3	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-6.5	6.5	А
T _{stg}	storage temperature			-65	150	°C
T _{amb}	ambient temperature			-55	150	°C
ESD maximun	n ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-15	15	kV
	voltage	ISO 10605; contact discharge; R = 330 Ω ; C = 150 pF	[2]	-15	15	kV
		ISO 10605; contact discharge; R = 330 Ω ; C = 330 pF	[2]	-13	13	kV

- [1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC 61000-4-5.
- [2] Device stressed with ten non-repetitive ESD pulses.

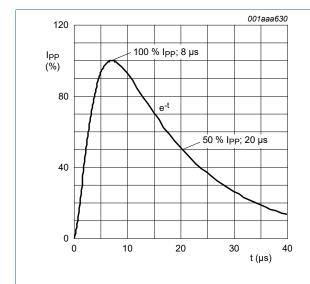


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

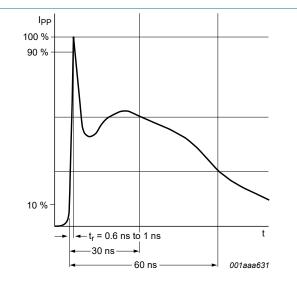


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

Product data sheet

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		5.5	9	11.5	V
V _{CL}	clamping voltage	I _{TLP} = 8 A; t _p = 100 ns; T _{amb} = 25 °C	[1]	-	2.8	-	V
		I_{TLP} = 16 A; t_p = 100 ns; T_{amb} = 25 °C	[1]	-	4.2	-	V
		$I_{PPM} = 6.5 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[2]	-	2.9	-	V
I _{RM}	reverse leakage current	V _{RWM} = 3.3 V; T _{amb} = 25 °C		-	1	100	nA
R _{dyn}	dynamic resistance	$I_R = 10 \text{ A}; t_p = 100 \text{ ns}; T_{amb} = 25 ^{\circ}\text{C}$	[1]	-	0.2	-	Ω
		$I_R = -10 \text{ A}; t_p = 100 \text{ ns}; T_{amb} = 25 \text{ °C}$	[1]	-	0.2	-	Ω
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[3]	-	0.4	0.5	pF

- [1] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008 on pin 2
- [2] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [3] Measured on pin 1

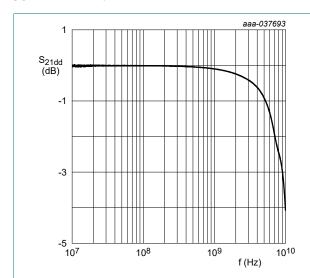


Fig. 3. Insertion loss; typical values

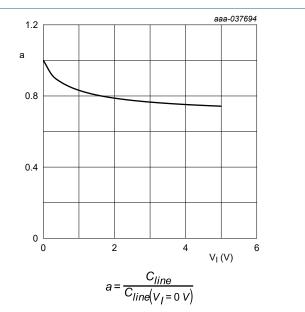
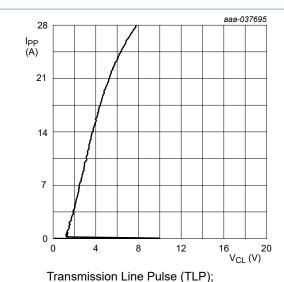
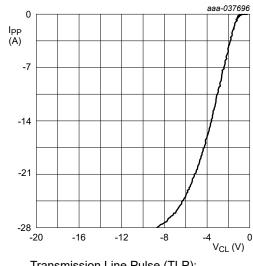


Fig. 4. Relative capacitance as a function of input voltage; typical values



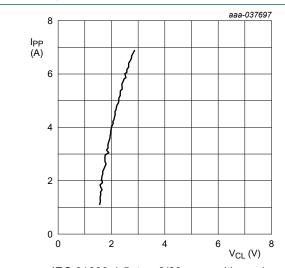
 $t_p = 100 \text{ ns}; t_r = 1 \text{ ns}; \text{ pin 2}$

Fig. 5. Dynamic resistance with positive clamping; typical values



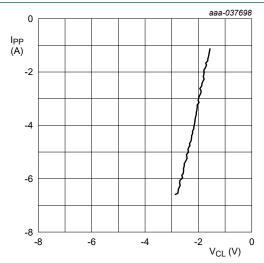
Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$; $t_r = 1 \text{ ns}$; pin 2

Fig. 6. Dynamic resistance with negative clamping; typical values



IEC 61000-4-5; t_p = 8/20 μ s; positive pulse Fig. 7. Dynamic resistance with positive clamping;

typical values



IEC 61000-4-5; t_p = 8/20 μ s; negative pulse

Fig. 8. Dynamic resistance with negative clamping; typical values

10. Application information

The device is designed to provide high-level ESD protection for high-speed serial data buses such as HDMI, DisplayPort, automotive video-links, eSATA and LVDS data lines.

Note: When designing the PCB, give careful consideration to impedance matching and signal coupling. Do not connect the signal lines to unlimited current sources like, for example, a battery.

Dynamic resistance

The device uses an advanced clamping structure showing a negative dynamic resistance.

This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid keeping the ESD protection device in snap-back state after exceeding breakdown voltage (due to an ESD pulse for instance).

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

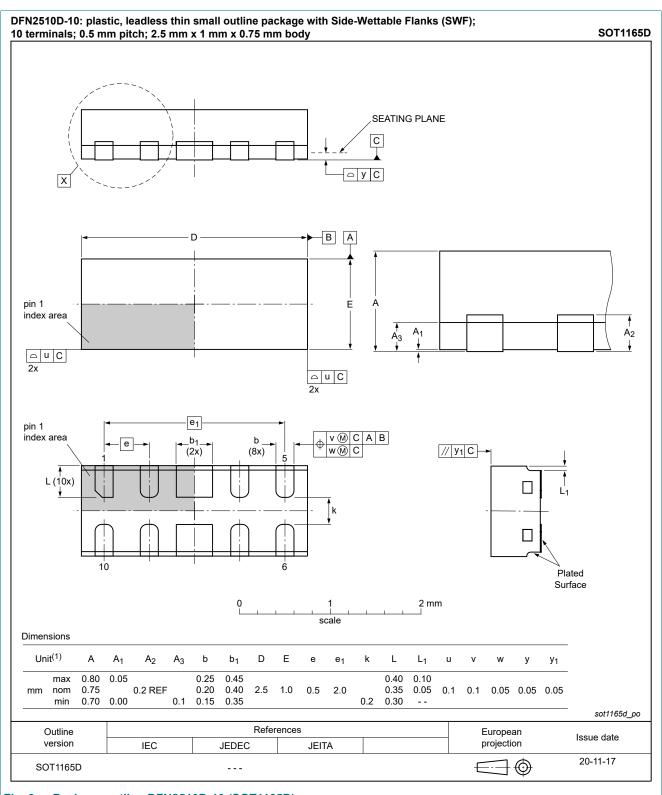
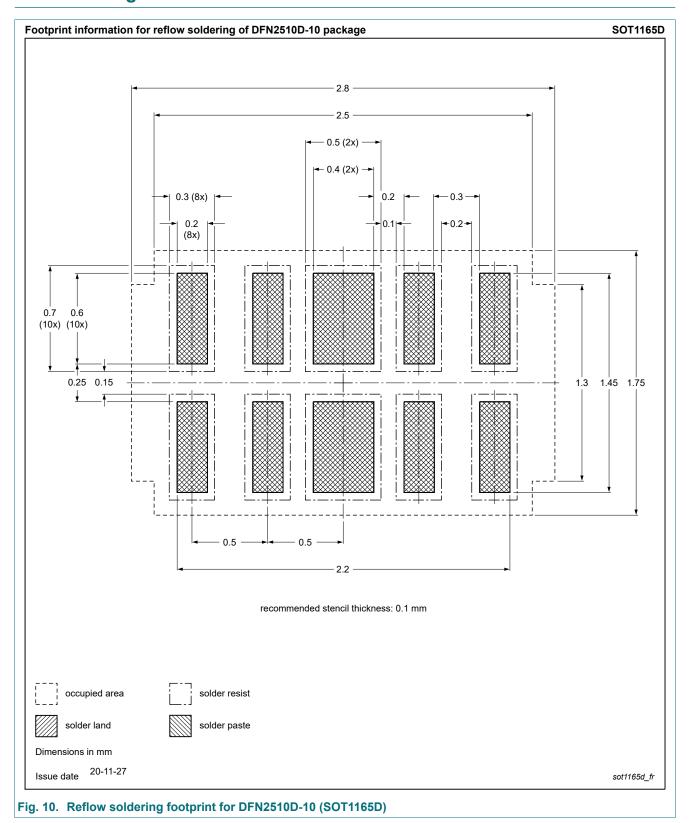


Fig. 9. Package outline DFN2510D-10 (SOT1165D)

13. Soldering



14. Revision history

Table 7. Revision history

Data sheet ID Rele	lease date	Data sheet status	Change notice	Supersedes
PESD4USB3UBTTS-Q 202 v.1	231025	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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