1. General description

ESD protection device in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side wettable flanks, designed to protect two automotive in-vehicle network bus lines from the damage caused by ElectroStatic Discharge (ESD) and other transients.

2. Features and benefits

- Reverse stand-off voltage: V_{RWM} = 24 V
- Low clamping voltage: V_{CL}= 33 V at I_{PP} = 1 A
- ESD protection up to 23 kV (IEC 61000-4-2)
- Low capacitance: C_d = 6 pF
- ESD protection up to 23 kV (ISO 10605; C = 150 pF; R = 330 Ω)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

ESD protection for in-vehicle network lines in automotive environments

- CAN-FD
- CAN
- FlexRay
- SENT

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
I _{PPM}	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	-	2.6	Α
V _{CL}	clamping voltage	$I_{PP} = 1 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[3] [2]	-	33	42	V

- [1] According to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	3	V4 14 51
3	СС	common cathode		K1 CC CC K2 D1 CC
			Transparent top view DFN1412D-3 (SOT8009)	006aaa155

6. Ordering information

Table 3. Ordering information

Type number			
	Name	Description	Version
PESD2CANFD24VQC-Q		plastic, leadless ultra small outline package with sidewettable flanks (SWF); 3 terminals; 0.8 mm pitch; 1.4 mm x 1.2 mm x 0.48 mm body	SOT8009

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD2CANFD24VQC-Q	6D

Product data sheet

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	2.6	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximi	um ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	23	kV
	voltage	ISO 10605; contact discharge; C = 330 pF, R = 330 Ω	[2] [3]	-	20	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 Ω	[2] [3]	-	23	kV

- [1] According to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

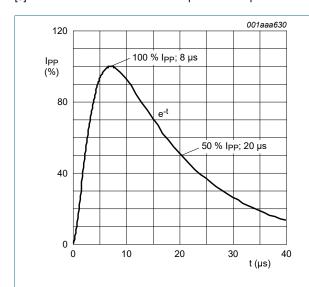


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

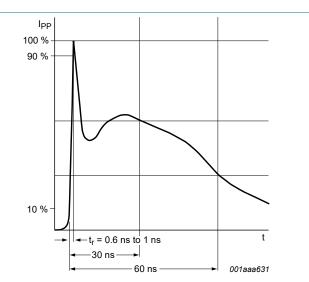


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
V_{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	25.5	-	35.5	V
I _{RM}	reverse leakage current	V _{RWM} = 24 V; T _{amb} = 25 °C	[1]	-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 2.5 V; T _{amb} = 25 °C	[1]	-	5.2	6	pF
		f = 1 MHz; V _R = -2.5 V; T _{amb} = 25 °C	[1]	-	5.2	6	pF
$\Delta C_d/C_d$	diode capacitance	f = 1 MHz; V _R = 2.5 V; T _{amb} = 25 °C	[2]	-	0.5	-	%
matching	matching	f = 1 MHz; V _R = -2.5 V; T _{amb} = 25 °C	[2]	-	0.5	-	%
V _{CL}	clamping voltage	I _{PP} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[3] [1]	-	33	42	V
R _{dyn}	dynamic resistance	$I_R = 10 \text{ A}; t_p = 100 \text{ ns}; T_{amb} = 25 ^{\circ}\text{C}$	[4] [1]	-	0.7	-	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- ΔC_d is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.
- Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

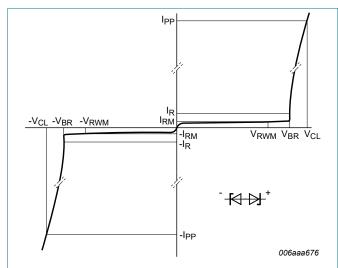
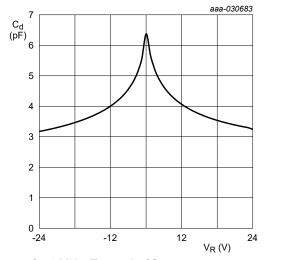
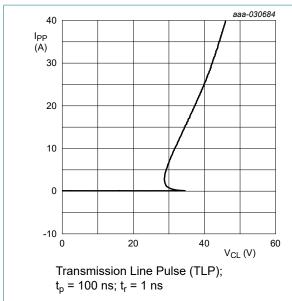


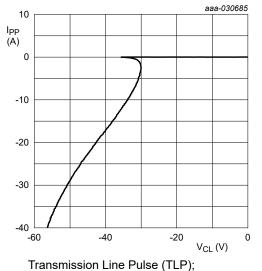
Fig. 3. V-I characteristics for a bidirectional ESD protection diode



 $f = 1 MHz; T_{amb} = 25 °C$

Fig. 4. Diode capacitance as a function of reverse voltage; typical values

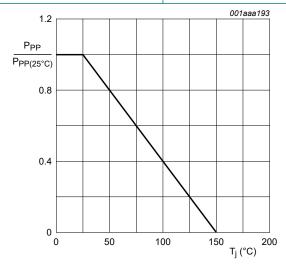




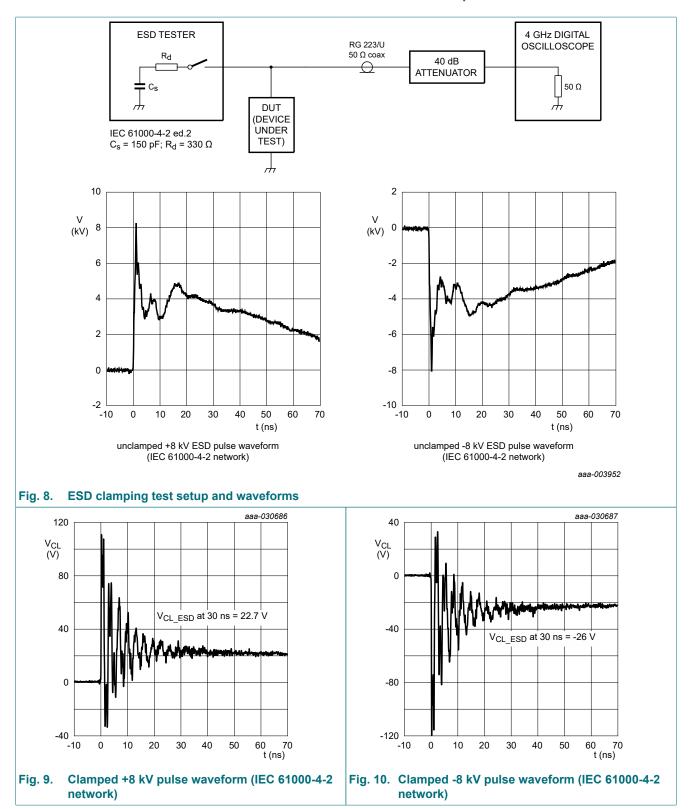
 $t_p = 100 \text{ ns}; t_r = 1 \text{ ns}$

Positive clamping voltage (TLP); typical values Fig. 5.

Fig. 6. Negative clamping voltage (TLP); typical values



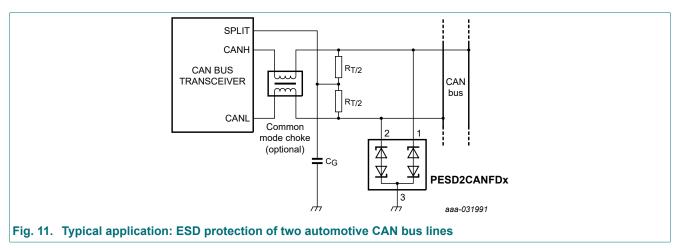
Relative variation of peak pulse power as a function of junction temperature; typical values Fig. 7.



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10. Application information

The device is designed for the protection of two automotive in-vehicle bus lines, e.g. CAN (FD), from the damage caused by ESD and surge pulses.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

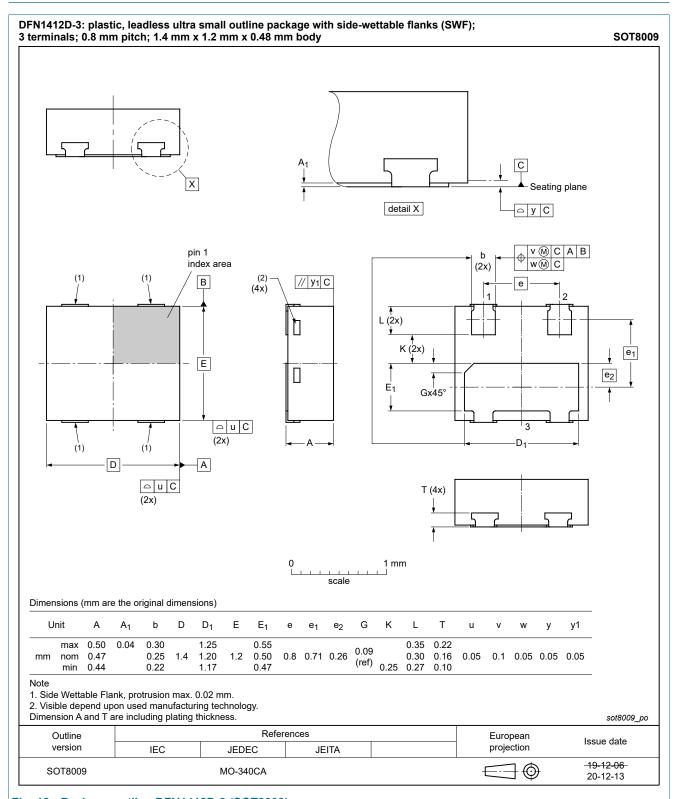
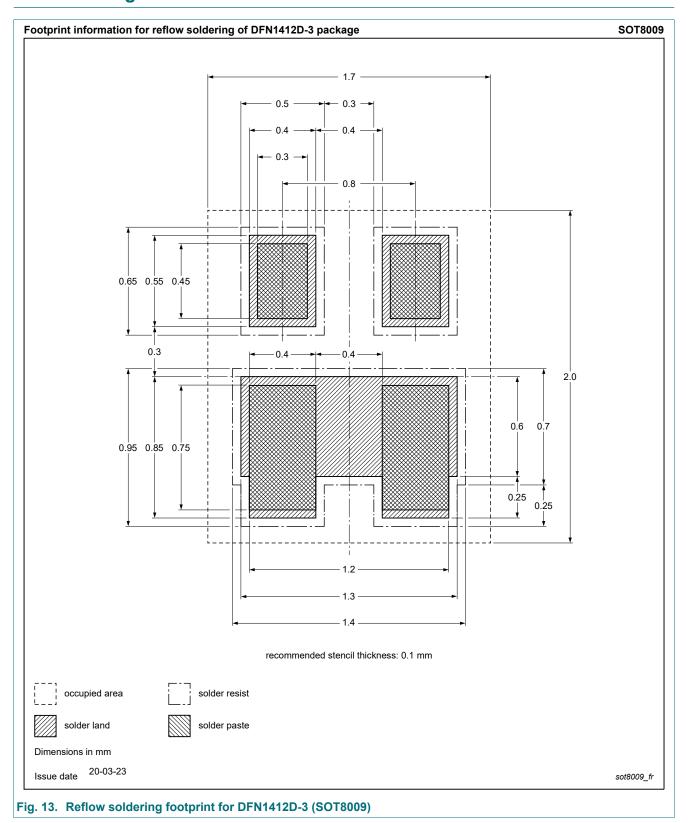


Fig. 12. Package outline DFN1412D-3 (SOT8009)

13. Soldering



14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD2CANFD24VQC-	20220803	Product data sheet	-	-
Q v.1				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Contents

1. 0	General description	1
2. F	Features and benefits	1
3. <i>A</i>	Applications	1
4. C	Quick reference data	1
5. F	Pinning information	2
6. C	Ordering information	2
7. N	Marking	2
8. L	_imiting values	3
9. (Characteristics	4
10.	Application information	7
	Test information	
12.	Package outline	8
	Soldering	
	Revision history	
	Legal information	

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