



PBSS4350Z-Q

50 V low V_{CEsat} NPN transistor

19 September 2023

Product data sheet

1. General description

NPN low V_{CEsat} transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5350Z-Q

2. Features and benefits

- Low collector-emitter saturation voltage
- High collector current capability: I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- Higher efficiency leading to less heat generation
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - Linear voltage regulation (LDO)
- Peripheral drivers
 - Driver in low supply voltage applications, for example lamps, LEDs
 - Inductive load driver, for example relays, buzzers, motors

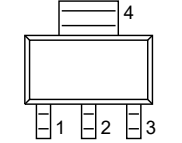
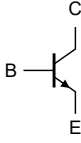
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	50	V
I_C	collector current		-	-	3	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	5	A
h_{FE}	DC current gain	$V_{CE} = 2$ V; $I_C = 500$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	200	-	-	
R_{CEsat}	collector-emitter saturation resistance	$I_C = 2$ A; $I_B = 200$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	110	145	m Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-73 (SOT223)	 sym123
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4350Z-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4350Z-Q	PB4350

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	60	V
V_{CEO}	collector-emitter voltage	open base		-	50	V
V_{EBO}	emitter-base voltage	open collector		-	6	V
I_C	collector current			-	3	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	5	A
I_{BM}	peak base current			-	1	A
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	1.35	W
			[2]	-	2	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
 [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	92	K/W
			[2]	-	-	62.5	K/W

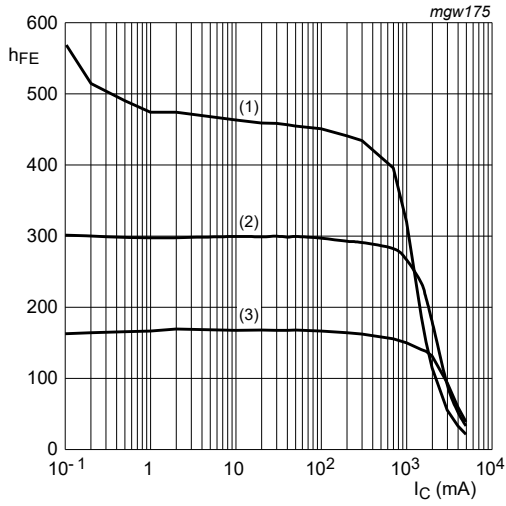
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

10. Characteristics

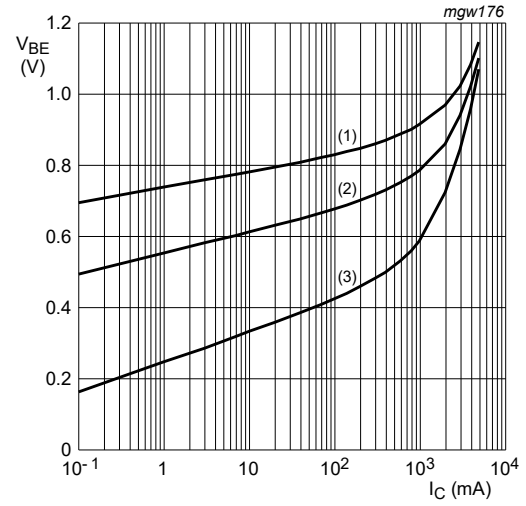
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = 50 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	100	nA
		$V_{CB} = 50 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ }^\circ\text{C}$		-	-	50	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	100	nA
h_{FE}	DC current gain	$V_{CE} = 2 \text{ V}; I_C = 500 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		200	-	-	
		$V_{CE} = 2 \text{ V}; I_C = 1 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		200	-	-	
		$V_{CE} = 2 \text{ V}; I_C = 2 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		100	-	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	90	mV
		$I_C = 1 \text{ A}; I_B = 50 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	170	mV
		$I_C = 2 \text{ A}; I_B = 200 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	290	mV
R_{CEsat}	collector-emitter saturation resistance			-	110	145	m Ω
V_{BEsat}	base-emitter saturation voltage			-	-	1.2	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}; I_C = 1 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	1.1	V
f_T	transition frequency	$V_{CE} = 5 \text{ V}; I_C = 100 \text{ mA}; f = 100 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		100	-	-	MHz
C_c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$		-	-	30	pF



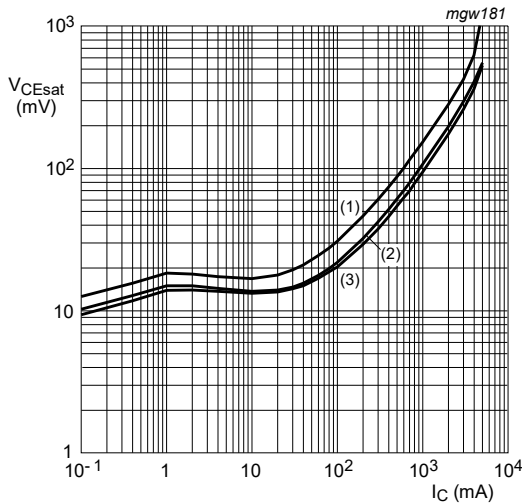
$V_{CE} = 2\text{ V}$
 (1) $T_{amb} = 150\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -55\text{ °C}$

Fig. 1. DC current gain; typical values



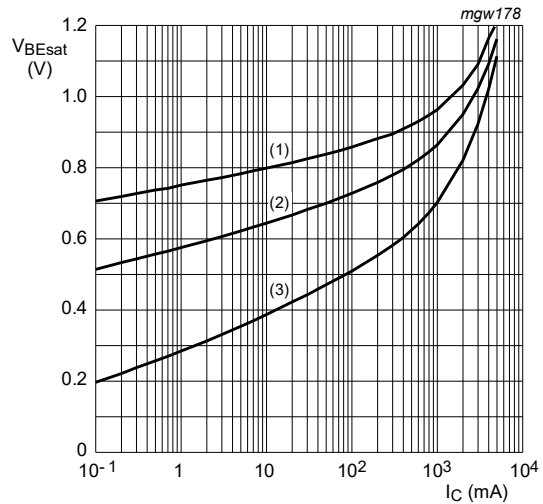
$V_{CE} = 2\text{ V}$
 (1) $T_{amb} = -55\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = 150\text{ °C}$

Fig. 2. Base-emitter voltage as a function of collector-current; typical values



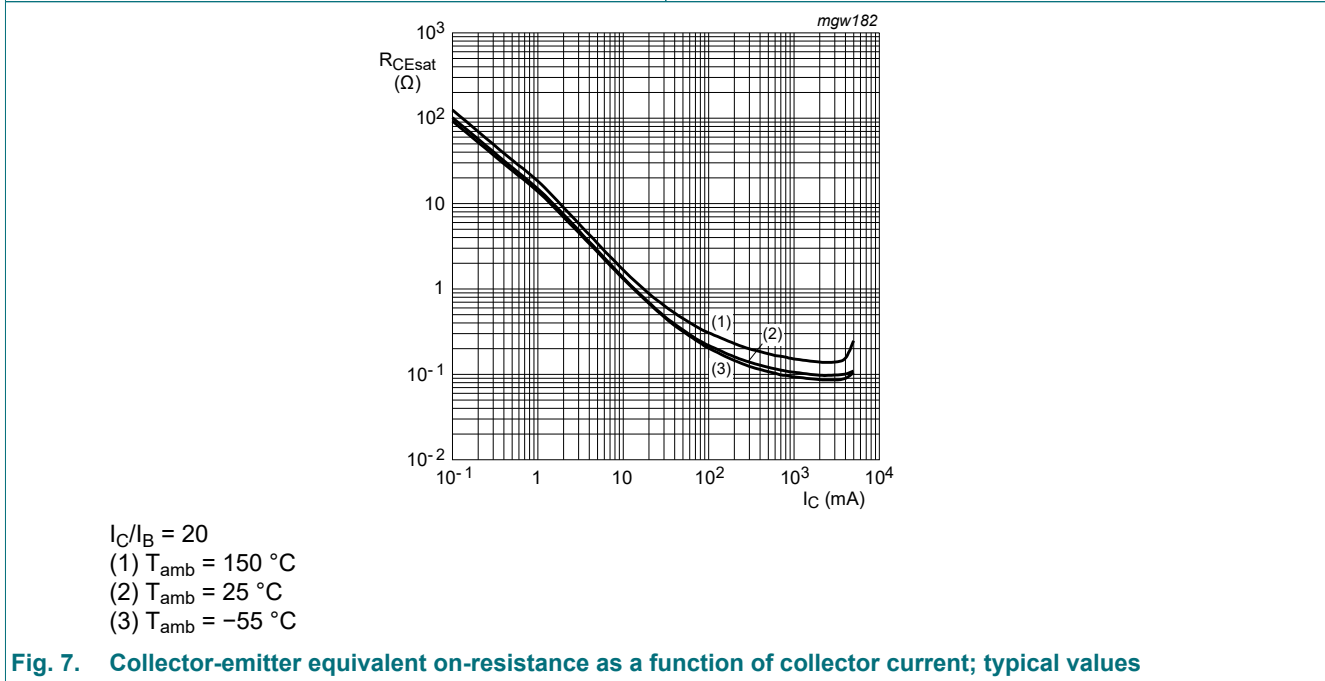
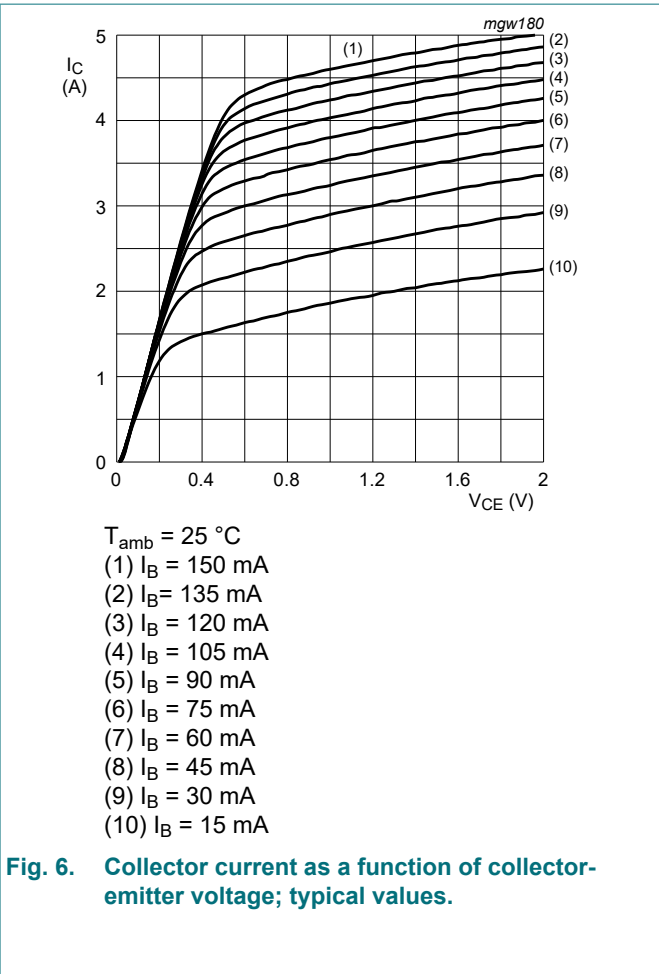
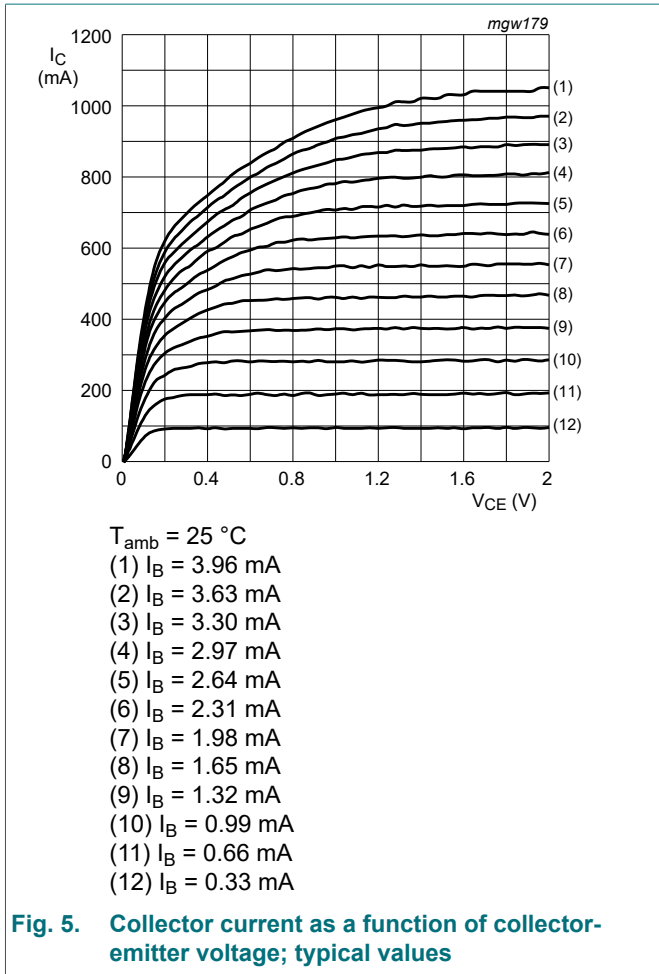
$I_C/I_B = 20$
 (1) $T_{amb} = 150\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -55\text{ °C}$

Fig. 3. Collector-emitter saturation as a function of collector current; typical values.



$I_C/I_B = 20$
 (1) $T_{amb} = 150\text{ °C}$
 (2) $T_{amb} = 25\text{ °C}$
 (3) $T_{amb} = -55\text{ °C}$

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values



11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

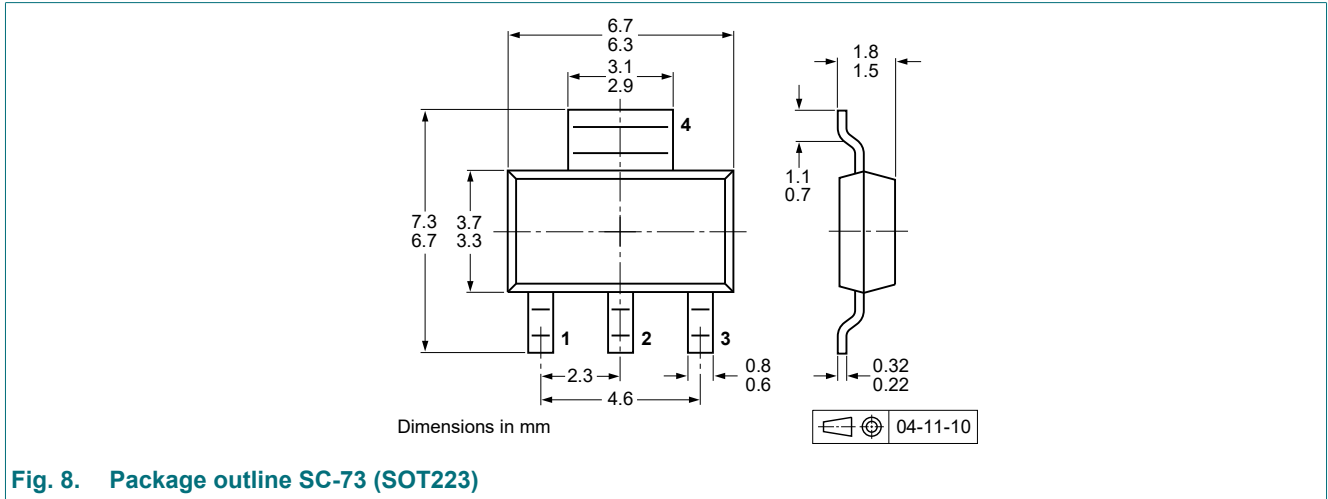


Fig. 8. Package outline SC-73 (SOT223)

13. Soldering

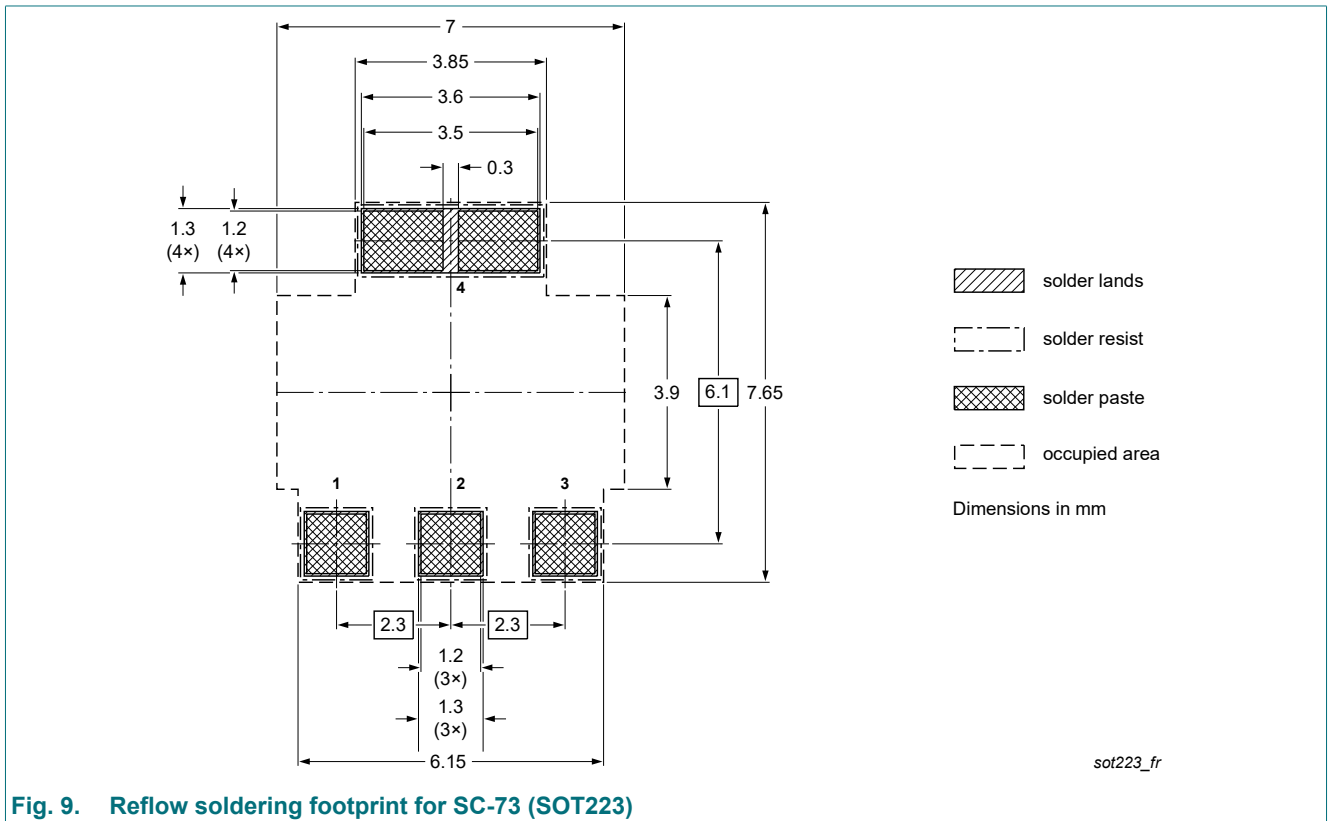


Fig. 9. Reflow soldering footprint for SC-73 (SOT223)

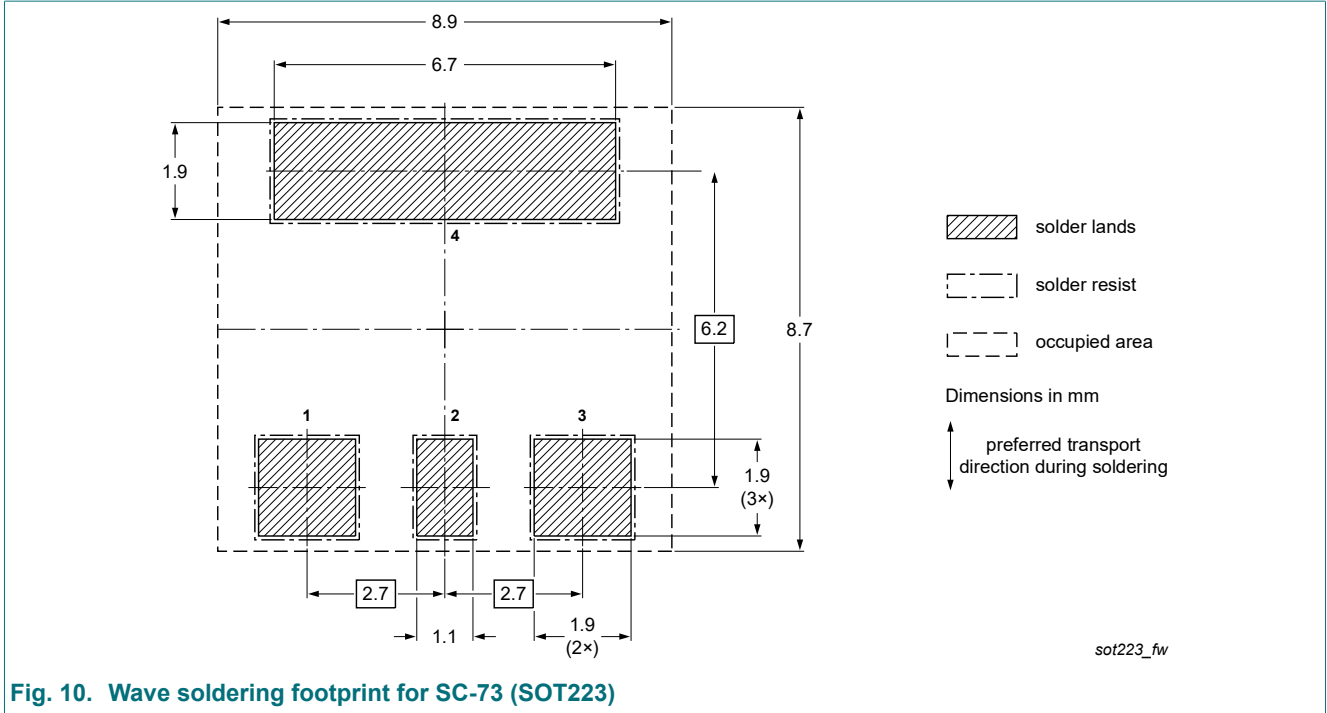


Fig. 10. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4350Z-Q v.1	20230919	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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