

# BC850B NPN general purpose transistor 25 April 2023

**Product data sheet** 

### 1. General description

NPN transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package. PNP complement: BC860B

### 2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 45 V)
- AEC-Q101 qualified

## 3. Applications

· General purpose switching and amplification

## 4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; I <sub>C</sub> = 10 µA; T <sub>j</sub> = 25 °C		-	240	-	

# 5. Pinning information

Table 2.	<b>Pinning infor</b>	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	E	emitter		J
3	С	collector		вК
				E
			1 2 SOT23	sym123
			30123	

## 6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
<u>BC850B</u>		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>		

### 7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
BC850B	2F%

[1] % = placeholder for manufacturing site code

### 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current			-	200	mA
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

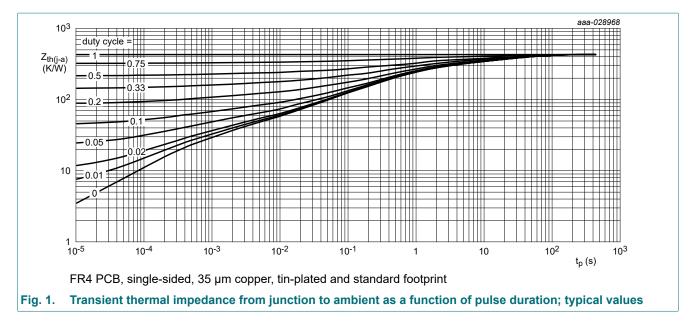
## 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

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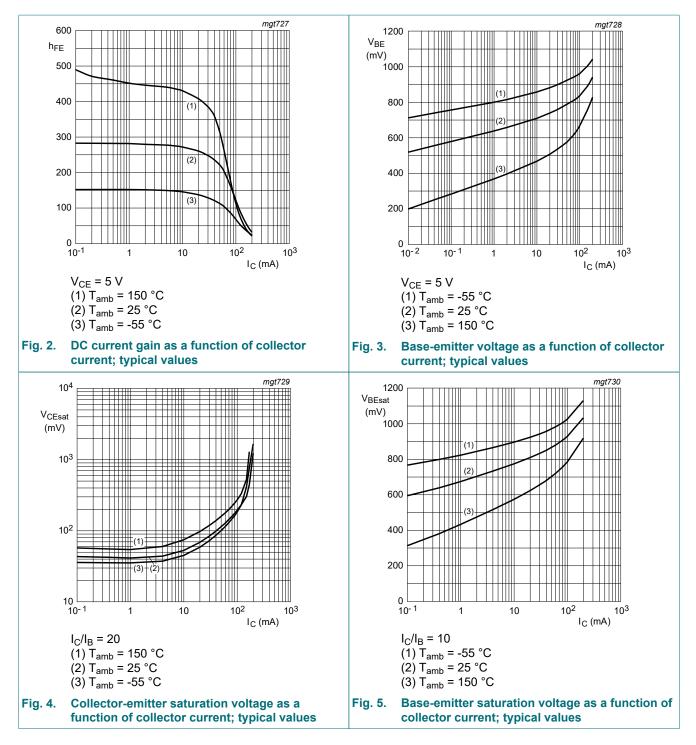
### **10. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 25 °C		-	-	15	nA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>j</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 μA; T <sub>j</sub> = 25 °C		-	240	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>j</sub> = 25 °C		200	290	450	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>j</sub> = 25 °C		-	90	250	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; T <sub>j</sub> = 25 °C		-	200	600	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>j</sub> = 25 °C	[1]	-	700	-	mV
		I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; T <sub>j</sub> = 25 °C	[1]	-	900	-	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>j</sub> = 25 °C	[2]	580	660	700	mV
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; T <sub>j</sub> = 25 °C	[2]	-	-	770	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>j</sub> = 25 °C		-	2.5	-	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = 500 mV; I <sub>C</sub> = 0 A; i <sub>c</sub> = 0 A; f = 1 MHz; T <sub>j</sub> = 25 °C		-	11	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>j</sub> = 25 °C		100	-	-	MHz
NF	noise figure	$V_{CE} = 5 \text{ V}; I_C = 200 \ \mu\text{A}; R_S = 2 \ \text{k}\Omega;$ B = 200 Hz; f = 10 Hz to 15.7 kHz; T <sub>j</sub> = 25 °C		-	-	4	dB
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 200 μA; R <sub>S</sub> = 2 kΩ; f = 1 kHz; B = 200 Hz		-	-	4	dB

[1]  $V_{BEsat}$  decreases by about 1.7 mV/K with increasing temperature.

[2]  $V_{BE}$  decreases by about 2 mV/K with increasing temperature.

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### **11. Test information**

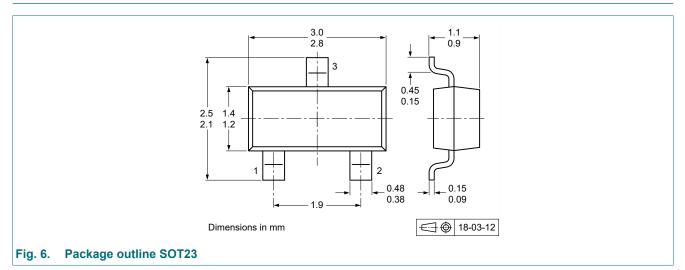
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

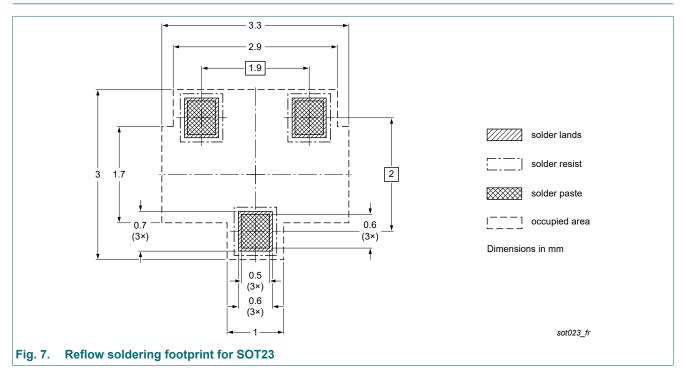
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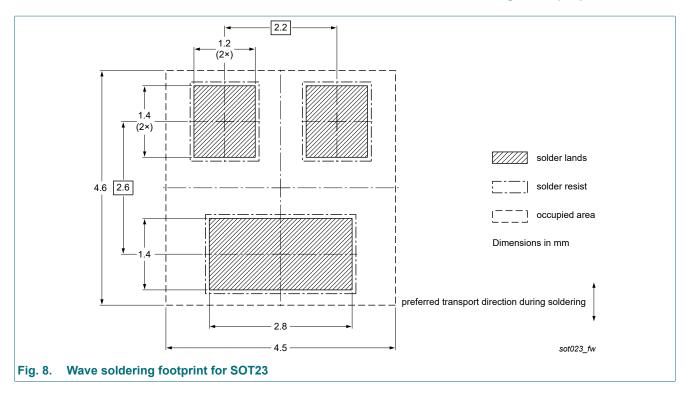
# 12. Package outline



### 13. Soldering



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# 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC850C v.3	20230425	Product data sheet	-	BC849_BC850 v.2
Modifications:	<ul><li>Nexperia.</li><li>Legal texts have b</li></ul>	data sheet has been rede been adapted to the new c t splitted to single type data	ompany name where	
	20040116	Product data sheet	_	BC849 BC850 v.1
BC849_BC850 v.2	20040110		-	D0049_D0000 V.1

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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