**Product data sheet** 

### 1. General description

NPN switching transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 30 V)
- AEC-Q101 qualified

# 3. Applications

· Switching and linear amplification

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	30	V
I <sub>C</sub>	collector current		-	-	600	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 10 V; $I_{C}$ = 150 mA; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02; $T_{j}$ = 25 °C	100	-	300	
		$V_{CE}$ = 10 V; $I_{C}$ = 500 mA; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02; $T_{j}$ = 25 °C	30	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	Е	emitter		в
3	С	collector	1 2 SOT23	E sym021



**NPN** switching transistor

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PMBT2222		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

# 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PMBT2222	%1B

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	30	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	600	mA
I <sub>CM</sub>	peak collector current			-	800	mA
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

2/11

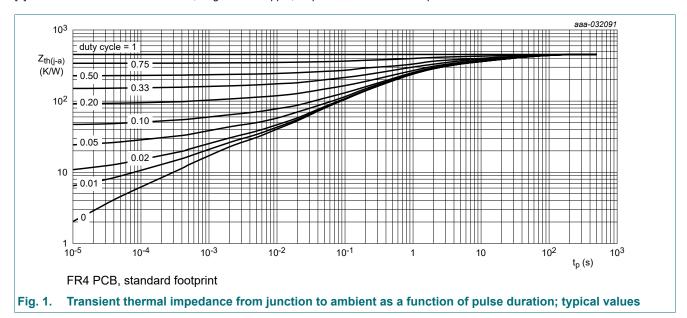
**NPN** switching transistor

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



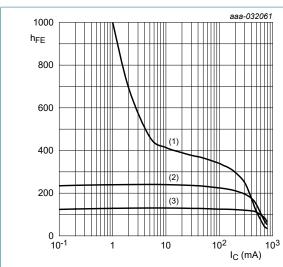
# **NPN** switching transistor

# 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
ODO	collector-base cut-off	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 25 °C	-	-	10	nA
	current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 125 °C	-	-	10	μΑ
I <sub>ЕВО</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{j} = 25 \text{ °C}$	-	-	10	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 10 V; $I_{C}$ = 0.1 mA; $T_{j}$ = 25 °C	35	-	-	
		V <sub>CE</sub> = 10 V; I <sub>C</sub> = 1 mA; T <sub>j</sub> = 25 °C	50	-	-	
		V <sub>CE</sub> = 10 V; I <sub>C</sub> = 10 mA; T <sub>j</sub> = 25 °C	75	-	-	
		V <sub>CE</sub> = 10 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = -55 °C	35	-	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 150 mA; $t_{p} \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_{j}$ = 25 °C	100	-	300	
		$V_{CE}$ = 1 V; $I_{C}$ = 150 mA; $t_{p} \le 300 \mu s$ ; δ ≤ 0.02; $T_{j}$ = 25 °C	50	-	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 500 mA; $t_{p} \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_{j}$ = 25 °C	30	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 150 mA; I <sub>B</sub> = 15 mA; T <sub>j</sub> = 25 °C	-	-	400	mV
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; T <sub>j</sub> = 25 °C	-	-	1.6	V
V <sub>BEsat</sub> base-emitter voltage	base-emitter saturation voltage	$I_C$ = 150 mA; $I_B$ = 15 mA; $t_p \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_j$ = 25 °C	-	-	1.3	V
		$I_C$ = 500 mA; $I_B$ = 50 mA; $t_p \le 300 \ \mu s$ ; $\delta \le 0.02$ ; $T_j$ = 25 °C	-	-	2.6	V
t <sub>d</sub>	delay time	I <sub>C</sub> = 150 mA; I <sub>Bon</sub> = 15 mA;	-	-	15	ns
r	rise time	$I_{Boff}$ = -15 mA; $V_{CC}$ = 10 V; $T_j$ = 25 °C	-	-	20	ns
t <sub>on</sub>	turn-on time		-	-	35	ns
s	storage time		-	-	200	ns
f	fall time		-	-	60	ns
off	turn-off time		-	-	250	ns
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{j} = 25 \text{ °C}$	-	-	8	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 500 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{j} = 25 ^{\circ}\text{C}$	-	-	30	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 20 V; $I_{C}$ = 20 mA; f = 100 MHz; $T_{j}$ = 25 °C	250	-	-	MHz
NF	noise figure	$V_{CE}$ = 5 V; $I_{C}$ = 100 μA; $R_{S}$ = 1 kΩ; $f$ = 1 kHz; $T_{i}$ = 25 °C	-	-	4	dB

### **NPN** switching transistor

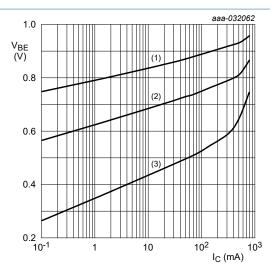


$$V_{CE} = 10 V$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 2. DC current gain as a function of collector current; typical values

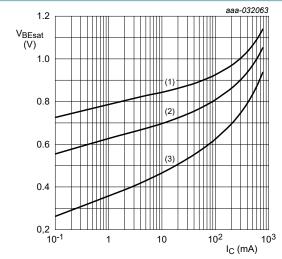


$$V_{CF} = 10 \text{ V}$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 3. Base-emitter voltage as a function of collector current; typical values



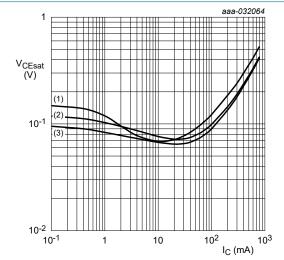
$$I_{\rm C}/I_{\rm B}=10$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

$$(3) T_{amb} = 150 °C$$

Fig. 4. Base-emitter saturation voltage as a function of Fig. 5. collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

$$(1) T_{amb} = 150 °C$$

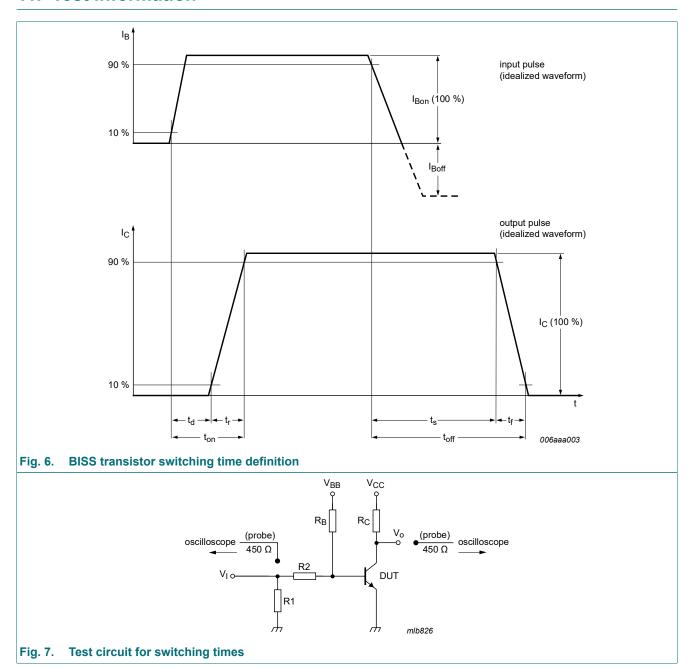
(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Collector-emitter saturation voltage as a function of collector current; typical values

**NPN** switching transistor

### 11. Test information

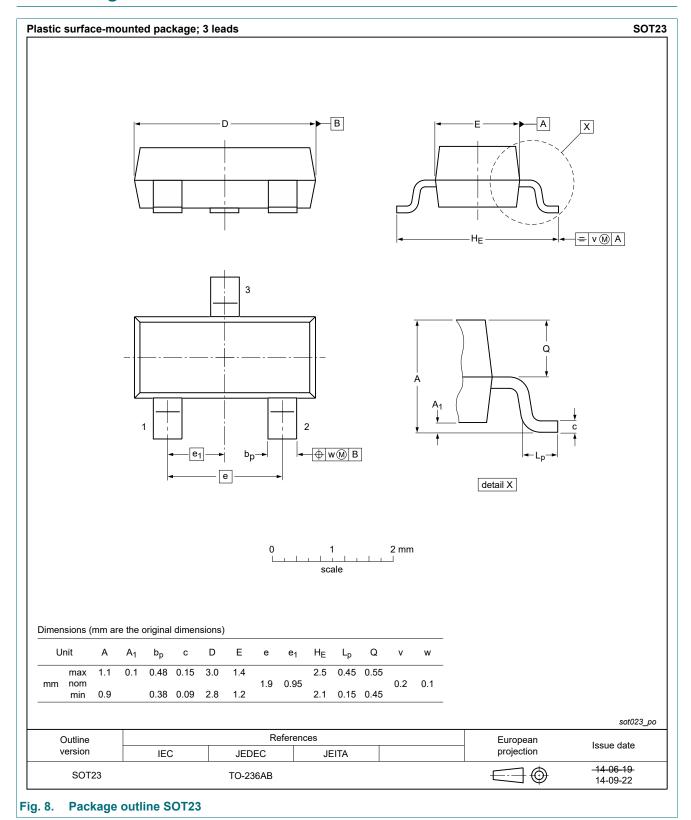


### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

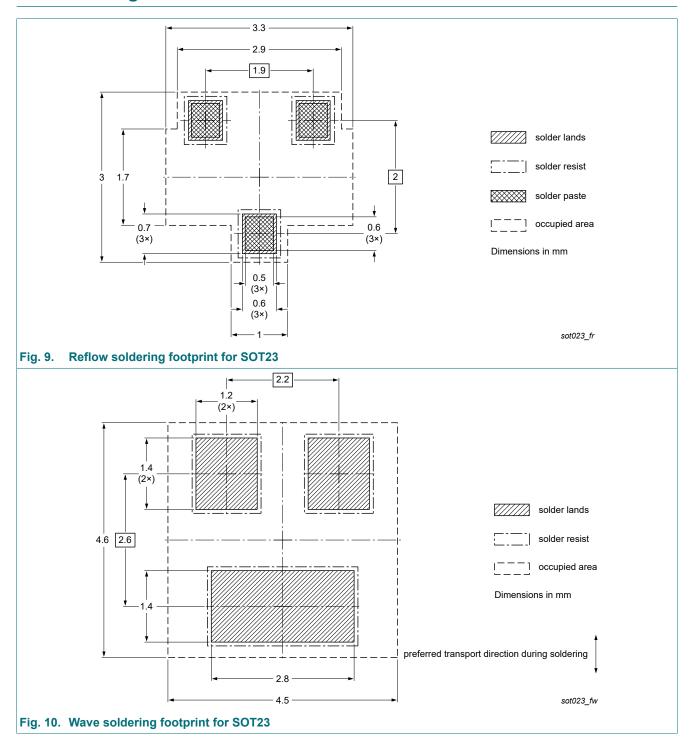
### **NPN** switching transistor

# 12. Package outline



### NPN switching transistor

# 13. Soldering



# **NPN** switching transistor

# 14. Revision history

### **Table 8. Revision history**

Table 6. INEVISION MISTORY				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT2222 v.7	20200805	Product data sheet	-	PMBT2222_2222A v.6
Modifications:	<ul><li>Thermal cha</li><li>Characteristi</li><li>Section "Solo</li></ul>	plitted into single type data racteristics: Figure 1 added cs: Figures 2 - 4 added dering" added king" removed		
PMBT2222_2222A v.6	20101112	Product data sheet	-	PMBT2222_2222A v.5
PMBT2222_2222A v.5	20040122	Product specification	-	PMBT2222_2222A v.4
PMBT2222_2222A v.4	19990427	Product specification	-	PMBT2222 v.3
PMBT2222 v.3	19970909	Product specification	-	-

9 / 11

### **NPN** switching transistor

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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### **NPN** switching transistor

# **Contents**

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	2
9.	Thermal characteristics	3
10.	. Characteristics	4
11.	Test information	6
12.	Package outline	7
13.	Soldering	8
14.	. Revision history	9
15.	Legal information	10

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