Product data sheet

1. General description

NPN switching transistor in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

PNP complement: PMST3906

2. Features and benefits

- Collector current capability I_C = 200 mA
- Collector-emitter voltage V_{CEO} =40 V
- AEC-Q101 qualified

3. Applications

· General amplification and switching

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|---------------------------|--|-----|-----|-----|------|
| V _{CEO} | collector-emitter voltage | open base | - | - | 40 | V |
| I _C | collector current | | - | - | 200 | mA |
| h _{FE} | DC current gain | V_{CE} = 1 V; I_{C} = 10 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 100 | - | 300 | |

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|-----------------------|----------------|
| 1 | В | base | <u></u> 3 | |
| 2 | Е | emitter | | C |
| 3 | С | collector | | B — E |
| | | | 1 2 SC-70 (SOT323) | sym123 |



NPN switching transistor

6. Ordering information

Table 3. Ordering information

| Type number | Package | | | | | |
|-------------|---------|--|---------|--|--|--|
| | Name | Description | Version | | | |
| PMST3904 | SC-70 | plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body | SOT323 | | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| PMST3904 | %1A |

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|---------------------------|--------------------------|-----|-----|-----|------|
| V_{CBO} | collector-base voltage | open emitter | | - | 60 | V |
| V _{CEO} | collector-emitter voltage | open base | | - | 40 | V |
| V_{EBO} | emitter-base voltage | open collector | | - | 6 | V |
| I _C | collector current | | | - | 200 | mA |
| I _{CM} | peak collector current | | | - | 200 | mA |
| I _{BM} | peak base current | | | - | 100 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 200 | mW |
| Tj | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -65 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|---------------|---|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | - | 625 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

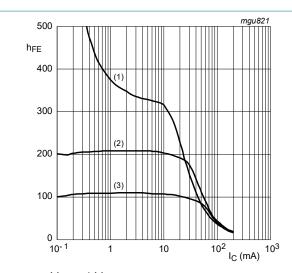
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10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|---------------------------------|---|-----|-----|-----|----------|
| I _{СВО} | collector-base cut-off current | V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C | - | - | 50 | nA |
| I _{EBO} | emitter-base cut-off current | V _{EB} = 6 V; I _C = 0 A; T _{amb} = 25 °C | - | - | 50 | nA |
| h _{FE} | DC current gain | V_{CE} = 1 V; I_{C} = 0.1 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 60 | - | - | |
| | | V_{CE} = 1 V; I_{C} = 1 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 80 | - | - | |
| | | V_{CE} = 1 V; I_{C} = 10 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 100 | - | 300 | |
| | | V_{CE} = 1 V; I_{C} = 50 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 60 | - | - | |
| | | V_{CE} = 1 V; I_{C} = 100 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C | 30 | - | - | |
| V _{CEsat} | collector-emitter | I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C | - | - | 200 | mV |
| | saturation voltage | $I_C = 50 \text{ mA}; I_B = 5 \text{ mA}; T_{amb} = 25 \text{ °C}$ | - | - | 300 | mV |
| V _{BEsat} | base-emitter saturation voltage | I _C = 10 mA; I _B = 1 mA; T _{amb} = 25 °C | 650 | - | 850 | mV |
| | | I _C = 50 mA; I _B = 5 mA; T _{amb} = 25 °C | - | - | 950 | mV |
| C _c | collector capacitance | $V_{CB} = 5 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$ | - | - | 4 | pF |
| C _e | emitter capacitance | $V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$ | - | - | 8 | pF |
| f _T | transition frequency | V _{CE} = 10 V; I _C = 20 mA; f = 100 MHz; T _{amb} = 25 °C | 300 | - | - | MHz |
| NF | noise figure | V_{CE} = 5 V; I_{C} = 100 μA; R_{S} = 1 kΩ; f = 10 Hz to 15.7 kHz; T_{amb} = 25 °C | - | - | 5 | dB |
| Switching t | imes (between 10% and 90 | % levels) | | | | <u>'</u> |
| t _d | delay time | I _C = 10 mA; I _{Bon} = 1 mA; I _{Boff} = -1 mA; | - | - | 35 | ns |
| t _r | rise time | T _{amb} = 25 °C | - | - | 35 | ns |
| t _s | storage time | | - | - | 200 | ns |
| t _f | fall time | | - | - | 50 | ns |

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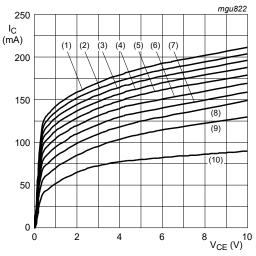


V_{CE} = 1 V (1) T_{amb} = 150 °C

(2) T_{amb} = 25 °C

(3) $T_{amb} = -55 \, ^{\circ}C$

Fig. 1. DC current gain as a function of collector current; typical values



 T_{amb} = 25 °C (1) I_B = 5.0 mA

 $(2) I_B = 4.5 \text{ mA}$

 $(3) I_B = 4.0 \text{ mA}$

 $(4) I_B = 3.5 \text{ mA}$

 $(5) I_B = 3.0 \text{ mA}$

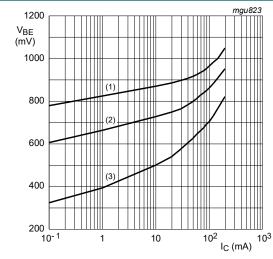
(6) $I_B = 2.5 \text{ mA}$ $(7) I_B = 2.0 \text{ mA}$

 $(8) I_B = 1.5 \text{ mA}$

(9) $I_B = 1.0 \text{ mA}$

 $(10) I_B = 0.5 mA$

Fig. 2. Collector current as a function of collectoremitter voltage; typical values

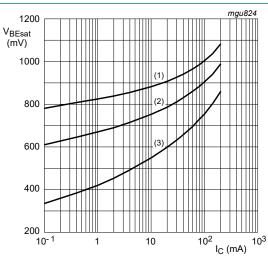


 $V_{CE} = 1 V$

 $(1) T_{amb} = -55 °C$

(2) T_{amb} = 25 °C (3) T_{amb} = 150 °C

Fig. 3. Base-emitter voltage as a function of collector current; typical values



 $I_C/I_B = 10$

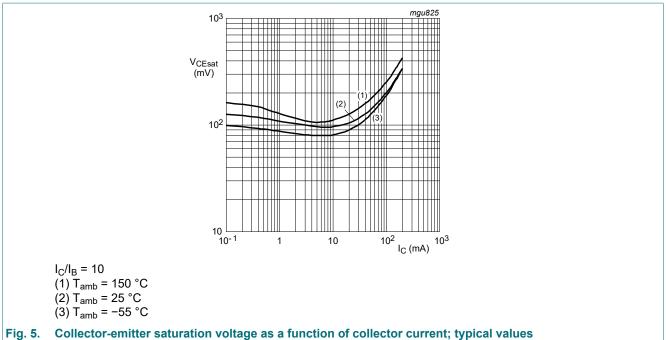
(1) $T_{amb} = -55 \, ^{\circ}C$

 $(2) T_{amb} = 25 °C$

(3) $T_{amb} = 150 \, ^{\circ}C$

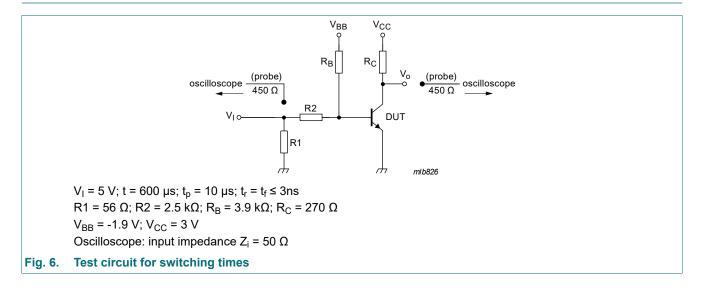
Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values

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11. Test information

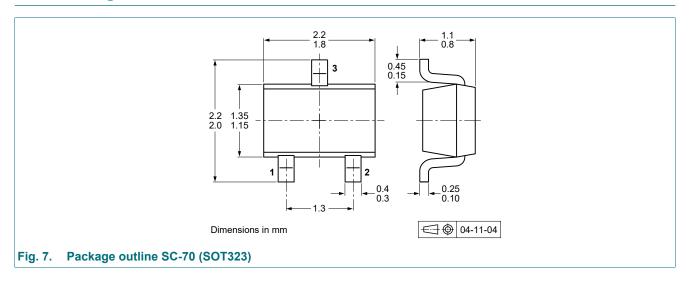


Quality information

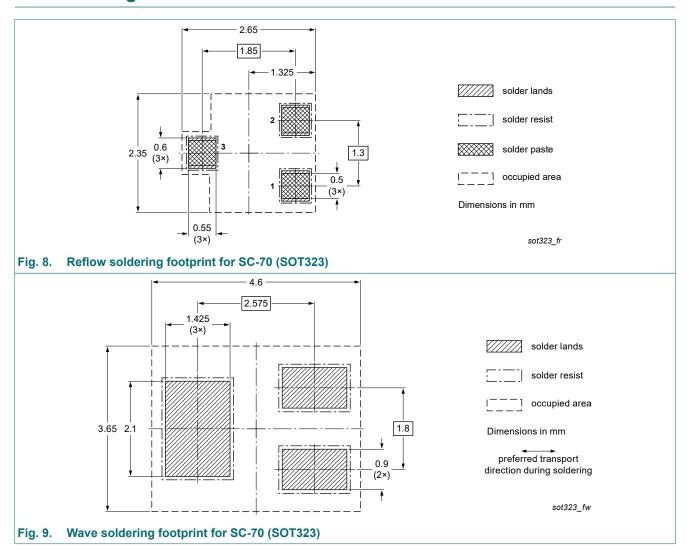
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

| Table of Itevicion Initio | , | | | | | | |
|---------------------------|---|--------------------|---------------|--------------|--|--|--|
| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
| PMST3904 v.3 | 20240228 | Product data sheet | - | PMST3904 v.2 | | | |
| Modifications: | The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. | | | | | | |
| PMST3904 v.2 | 20040421 | Product data sheet | - | PMST3904 v.1 | | | |
| PMST3904 v.1 | 19990422 | Product data sheet | - | - | | | |

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15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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