DISCRETE SEMICONDUCTORS

DATA SHEET

PDTC144T series NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

Product data sheet Supersedes data of 2004 Apr 06 2004 Aug 17



NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

FEATURES

- Built-in bias resistors
- · Simplified circuit design
- Reduction of component count
- · Reduced pick and place costs.

APPLICATIONS

- General purpose switching and amplification
- · Inverter and interface circuits
- Circuit driver.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	TYP.	MAX.	UNIT
V_{CEO}	collector-emitter voltage	_	50	V
Io	output current (DC)	_	100	mA
R1	bias resistor	47	_	kΩ
R2	open	_	_	_

DESCRIPTION

NPN resistor-equipped transistor (see "Simplified outline, symbol and pinning" for package details).

PRODUCT OVERVIEW

TYPE NUMBER	PACK	AGE	MARKING CODE	DND COMPLEMENT	
ITPE NUMBER	PHILIPS EIAJ		MARKING CODE	PNP COMPLEMENT	
PDTC144TE	SOT416	SC-75	43	PDTA144TE	
PDTC144TEF	SOT490	SC-89	33	PDTA144TEF	
PDTC144TK	SOT346	SC-59	53	PDTA144TK	
PDTC144TM	SOT883	SC-101	E4	PDTA144TM	
PDTC144TS	SOT54 (TO-92)	SC-43	TC144T	PDTA144TS	
PDTC144TT	SOT23	-	*41 ⁽¹⁾	PDTA144TT	
PDTC144TU	SOT323	SC-70	*41 ⁽¹⁾	PDTA144TU	

Note

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^{1. * =} p: Made in Hong Kong.

^{* =} t: Made in Malaysia.

^{* =} W: Made in China.

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SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	CIMPLIFIED OUTLINE AND CYMPOL		PINNING
TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PIN	DESCRIPTION
PDTC144TS	1 R1 2 R1 3 MAM361	1 2 3	base collector emitter
PDTC144TE PDTC144TEF PDTC144TK PDTC144TT PDTC144TU	3 1 R1 3 1 Top view MDB270	1 2 3	base emitter collector
PDTC144TM	2 R1 3 Bottom view MHCS07	1 2 3	base emitter collector

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

ORDERING INFORMATION

TYPE NUMBER		PACKAGE	
TYPE NUMBER	NAME	DESCRIPTION	VERSION
PDTC144TE	_	plastic surface mounted package; 3 leads	SOT416
PDTC144TEF	_	plastic surface mounted package; 3 leads	SOT490
PDTC144TK	_	plastic surface mounted package; 3 leads	SOT346
PDTC144TM	_	leadless ultra small plastic package; 3 solder lands; body $1.0 \times 0.6 \times 0.5 \text{ mm}$	SOT883
PDTC144TS	_	plastic single-ended leaded (through hole) package; 3 leads	SOT54
PDTC144TT	_	plastic surface mounted package; 3 leads	SOT23
PDTC144TU	_	plastic surface mounted package; 3 leads	SOT323

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	50	V
V _{CEO}	collector-emitter voltage	open base	_	50	V
V_{EBO}	emitter-base voltage	open collector	-	5	V
Io	output current (DC)		_	100	mA
I _{CM}	peak collector current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
	SOT54	note 1	_	500	mW
	SOT23	note 1	_	250	mW
	SOT346	note 1	_	250	mW
	SOT323	note 1	_	200	mW
	SOT490	notes 1 and 2	_	250	mW
	SOT883	notes 2 and 3	_	250	mW
	SOT416	note 1	_	150	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Notes

- 1. Refer to standard mounting conditions.
- 2. Reflow soldering is the only recommended soldering method.
- 3. Refer to SOT883 standard mounting conditions; FR4 with 60 μm copper strip line.

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PDTC144T series

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		
	SOT54	note 1	250	K/W
	SOT23	note 1	500	K/W
	SOT346	note 1	500	K/W
	SOT323	note 1	625	K/W
	SOT490	notes 1 and 2	500	K/W
	SOT883	notes 2 and 3	500	K/W
	SOT416	note 1	833	K/W

Notes

- 1. Refer to standard mounting conditions.
- 2. Reflow soldering is the only recommended soldering method.
- 3. Refer to SOT883 standard mounting conditions; FR4 with 60 μm copper strip line.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A	_	_	100	nA
I _{CEO}	collector-emitter cut-off current	$V_{CE} = 30 \text{ V}; I_{B} = 0 \text{ A}$	_	_	1	μΑ
		$V_{CE} = 30 \text{ V}; I_{B} = 0 \text{ A}; T_{j} = 150 ^{\circ}\text{C}$	_	_	50	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	_	_	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 1 mA	100	_	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	_	_	150	mV
R1	input resistor		33	47	61	kΩ
Сс	collector capacitance	$I_E = i_e = 0 \text{ A}; V_{CB} = 10 \text{ V};$ f = 1 MHz	_	_	2.5	pF

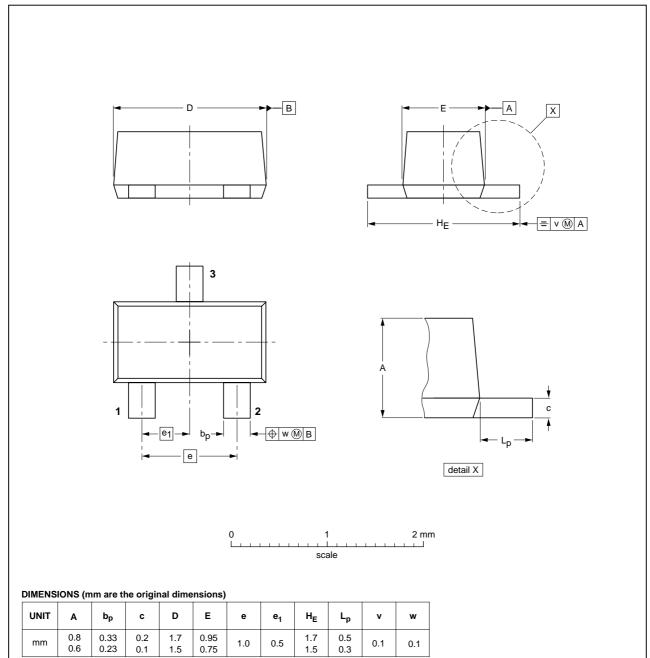
NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

PACKAGE OUTLINES

Plastic surface-mounted package; 3 leads

SOT490



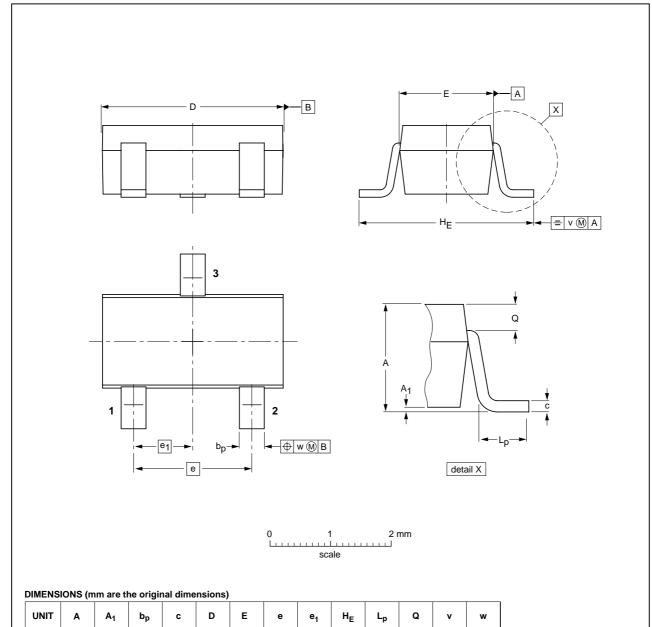
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VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT490			SC-89		05-07-28 06-03-16	

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

Plastic surface-mounted package; 3 leads

SOT346



OUTLINE				EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT346		TO-236	SC-59A			-04-11-11 06-03-16

1.9

0.6

0.33

0.2

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0.1

0.013

0.50

0.35

0.26

1.3

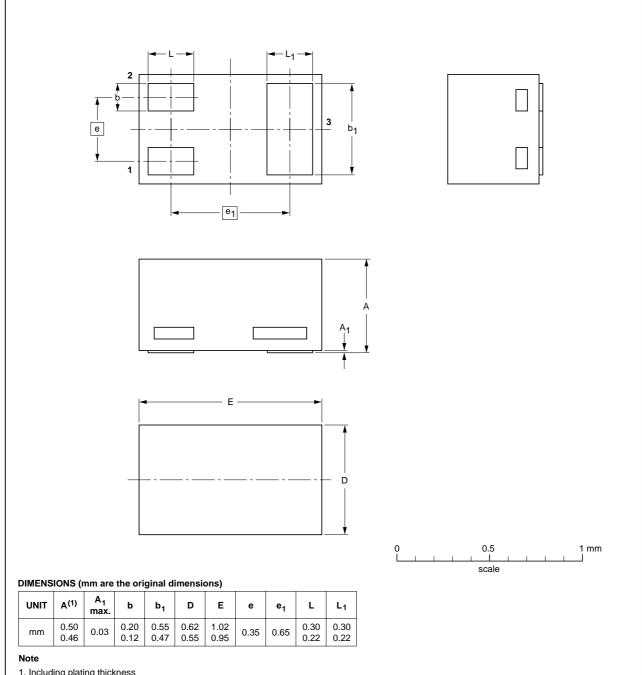
1.0

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

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Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.5 mm

SOT883



1. Including plating thickness

OUTLINE		REFERENCES			EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT883			SC-101			03-02-05 03-04-03	

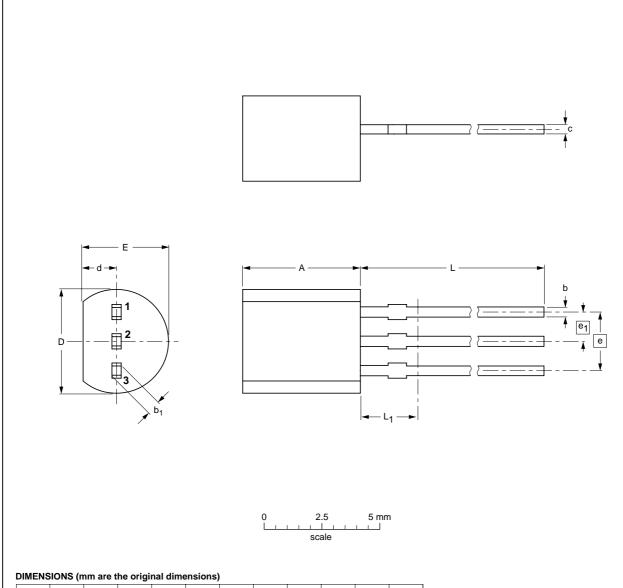
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NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max.
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

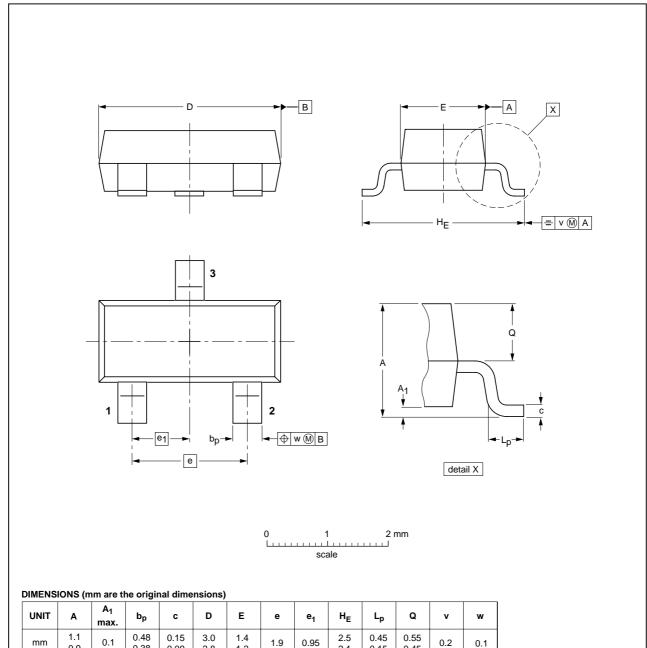
OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43A		04-06-28 04-11-16	

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE		EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04- 06-03-16

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0.38

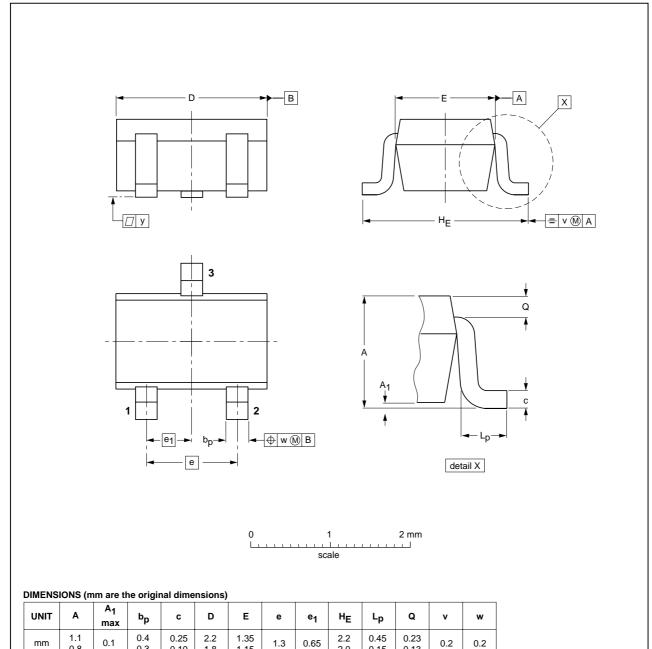
0.9

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

Plastic surface-mounted package; 3 leads

SOT323



OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT323			SC-70			-04-11-04 06-03-16

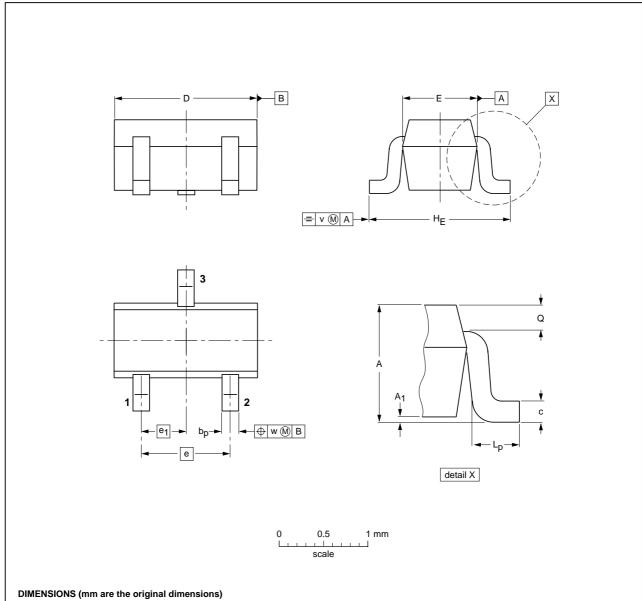
0.3

NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

Plastic surface-mounted package; 3 leads

SOT416



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U	NIT	Α	A ₁ max	bp	С	D	E	е	e ₁	HE	Lp	ø	v	w
n	nm	0.95 0.60	0.1	0.30 0.15	0.25 0.10	1.8 1.4	0.9 0.7	1	0.5	1.75 1.45	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT416			SC-75			04-11-04 06-03-16

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NPN resistor-equipped transistors; R1 = 47 k Ω , R2 = open

PDTC144T series

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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- 2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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Contact information

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