## 1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD323F (SC-90) small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 0.2 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage
- Small and flat lead SMD plastic package
- AEC-Q101 qualified

## 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- · Reverse polarity protection
- · Ultra high-speed switching
- · Low power consumption applications

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>amb</sub> $\leq$ 130 °C	[1]	-	-	0.2	А
		$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> $\leq$ 145 °C		-	-	0.2	Α
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	40	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 200 mA; T <sub>j</sub> = 25 °C		-	520	600	mV
I <sub>R</sub>	reverse current	$V_R = 40 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	0.7	10	μΑ

<sup>[1]</sup> Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	1 2	к <del>_[{-]</del> -А
2	Α	anode		sym001
			SC-90 (SOD323F)	Symoor



40 V, 200 mA low VF Schottky barrier rectifier

## 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PMEG4002EJ	SC-90	plastic, surface-mounted package; 2 leads; 1.7 mm x 1.25 mm x 0.7 mm body	SOD323F

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG4002EJ	1N

## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	40	V
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; $T_{amb} \le$ 130 °C	[1]	-	0.2	А
		$\delta$ = 0.5; f = 20 kHz; square wave; $T_{sp} \le$ 145 °C		-	0.2	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	2.6	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C	[2]	-	2.75	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2] [3]	-	385	mW
			[2] [4]	-	695	mW
			[1] [2]	-	1.045	W
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

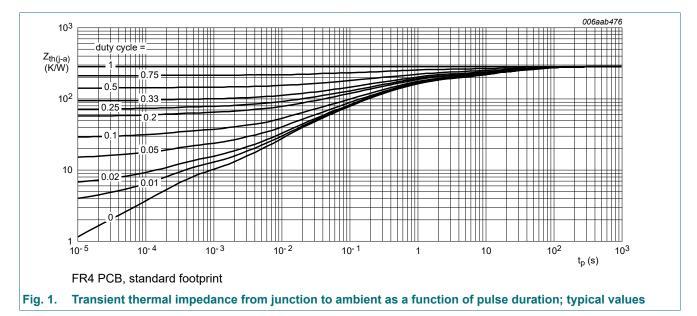
#### 40 V, 200 mA low VF Schottky barrier rectifier

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1] [2] [3]	-	-	325	K/W
			[1] [2] [4]	-	-	180	K/W
			[1] [2] [5]	-	-	120	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[6]	-	-	25	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [6] Soldering point of cathode tab.



### 40 V, 200 mA low VF Schottky barrier rectifier

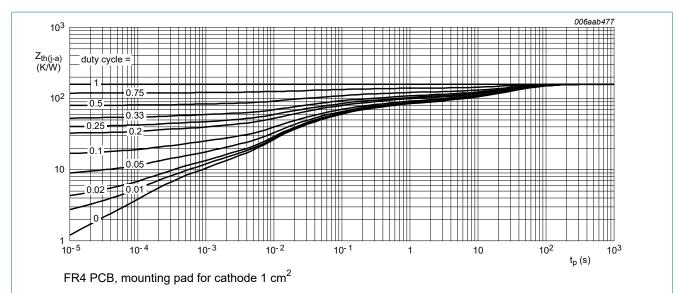


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

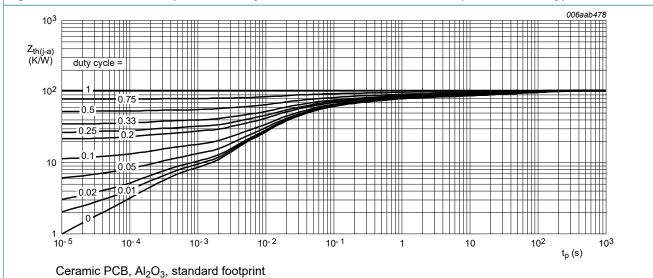


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 40 V, 200 mA low VF Schottky barrier rectifier

## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 mA; T <sub>j</sub> = 25 °C	[1]	-	190	220	mV
		I <sub>F</sub> = 1 mA; T <sub>j</sub> = 25 °C		-	250	290	mV
		I <sub>F</sub> = 10 mA; T <sub>j</sub> = 25 °C		-	320	360	mV
		I <sub>F</sub> = 100 mA; T <sub>j</sub> = 25 °C		-	440	500	mV
		I <sub>F</sub> = 200 mA; T <sub>j</sub> = 25 °C		-	520	600	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C		-	0.2	0.3	μA
		V <sub>R</sub> = 25 V; T <sub>j</sub> = 25 °C		-	0.3	0.5	μΑ
		V <sub>R</sub> = 40 V; T <sub>j</sub> = 25 °C		-	0.7	10	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	14	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	6	-	pF
t <sub>rr</sub>	reverse recovery time	when switched from lasub>F = 10 mA to $I_R$ = 10 mA; $R_L$ = 100 W; measured at $I_R$ = 1 mA; $T_j$ = 25 °C		-	5	-	ns

[1]

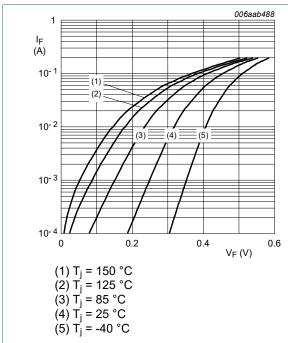


Fig. 4. Forward current as a function of forward voltage; typical values

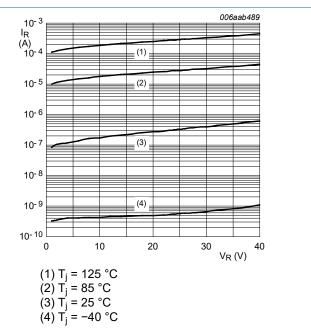


Fig. 5. Reverse current as a function of reverse voltage; typical values

### 40 V, 200 mA low VF Schottky barrier rectifier

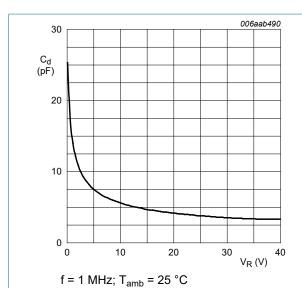
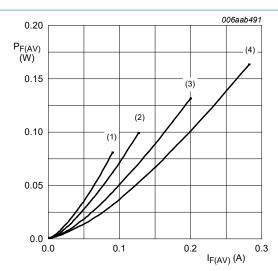
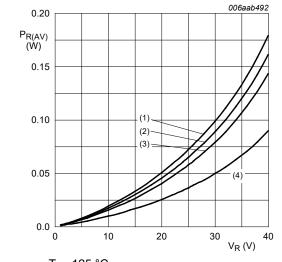


Fig. 6. Diode capacitance as a function of reverse voltage; typical values



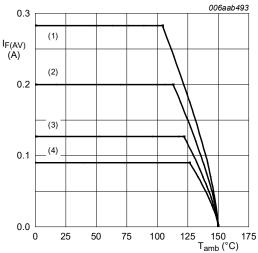
 $T_j = 150 \,^{\circ}\text{C}$   $(1) \, \delta = 0.1$   $(2) \, \delta = 0.2$   $(3) \, \delta = 0.5$   $(4) \, \delta = 0.8$  $(5) \, \delta = 1$ 

Fig. 7. Average forward power dissipation as a function of average forward current; typical values



 $T_j = 125 \,^{\circ}\text{C}$ (1)  $\delta = 1$ (2)  $\delta = 0.9$ (3)  $\delta = 0.8$ (4)  $\delta = 0.5$ 

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

 $T_j = 150 \,^{\circ}\text{C}$ (1)  $\delta = 1$ ; DC

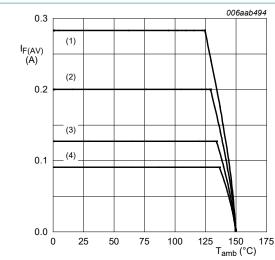
(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values

### 40 V, 200 mA low VF Schottky barrier rectifier



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

 $T_i = 150 \, ^{\circ}C$ 

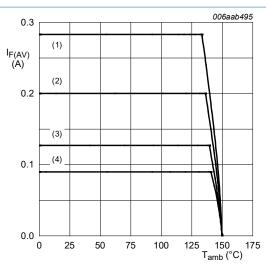
 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 150 °C

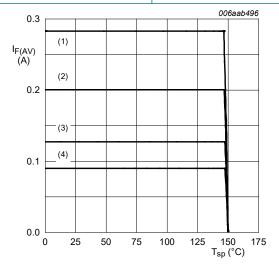
 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



T<sub>i</sub> = 150 °C

 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

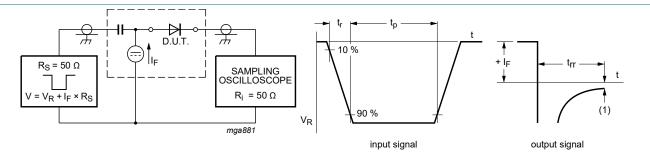
(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 12. Average forward current as a function of solder point temperature; typical values

40 V, 200 mA low VF Schottky barrier rectifier

### 11. Test information



(1)  $I_R = 1 \text{ mA}$ 

Input signal: reverse pulse rise time  $t_r$  = 0.6 ns; reverse voltage pulse duration  $t_p$  = 100 ns; duty cycle  $\delta$  = 0.05 Oscilloscope rise time  $t_r$  = 0.35 ns

Fig. 13. Reverse recovery time: test circuit and waveforms

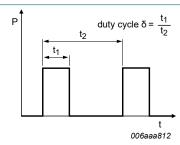


Fig. 14. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current

 $I_{RMS} = I_{F(AV)}$  at DC

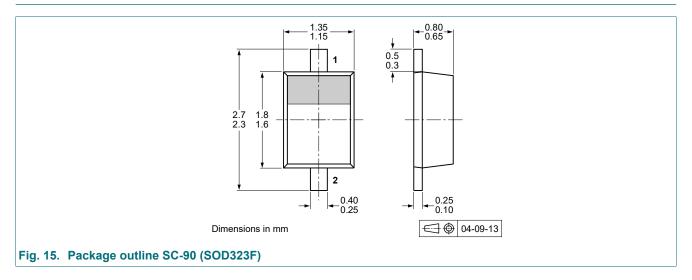
 $I_{RMS} = I_{M} \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current

### **Quality information**

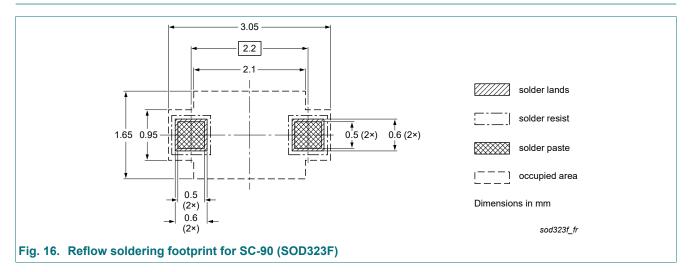
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

40 V, 200 mA low VF Schottky barrier rectifier

# 12. Package outline



## 13. Soldering



### 40 V, 200 mA low VF Schottky barrier rectifier

# 14. Revision history

#### **Table 8. Revision history**

Table 6. Reviolett fliete	• 3			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4002EJ v.2	20230921	Product data sheet	-	-
Modifications:	Nexperia.	ta sheet has been redesion adapted to the new concernation" removed.		, 0
PMEG4002EJ_1	20090515	Product data sheet	-	-

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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### 40 V, 200 mA low VF Schottky barrier rectifier

### **Contents**

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	2
9.	Thermal characteristics	3
10	Characteristics	5
11.	Test information	8
12	Package outline	9
	Soldering	
	Revision history	
	Legal information	

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