

1. General description

ESD protection device in a leadless ultra small DFN1006-2 (SOD882) Surface-Mounted Device (SMD) plastic package, designed to protect automotive in-vehicle network bus lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

2. Features and benefits

- Reverse stand-off voltage: V_{RWM} = 27 V
- Low clamping voltage: V_{CL} = 36 V at I_{PP} = 3 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- ESD protection up to 30 kV (ISO 10605: C = 330 pF, R = 330 Ω)
- Ultra low leakage current: I_{RM} = 1 nA
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

ESD protection for in-vehicle network lines in automotive environments

- CAN
- LIN
- FlexRay
- SENT

4. Quick reference data

Fable 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	27	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	-	3	A
V _{CL}	clamping voltage	I_{PPM} = 3 A; t_p = 8/20 µs; T_{amb} = 25 °C	[1]	-	36	45	V

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	1 2	К1 [] К2
			Transparent top view	006aab041
			DFN1006-2 (SOD882)	

6. Ordering information

Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PESD1IVN27L-Q		plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOD882	

7. Marking

Table 4. Marking codes	
Type number	Marking code
PESD1IVN27L-Q	9G

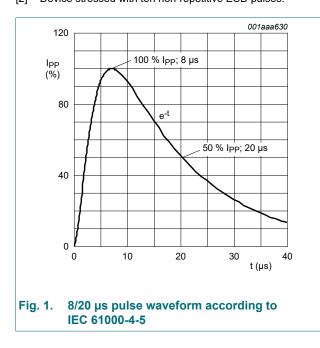
8. Limiting values

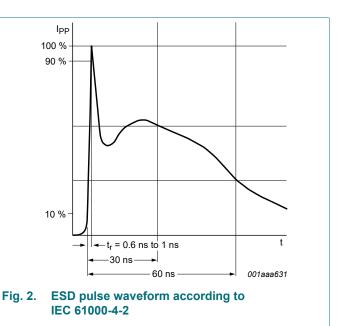
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	3	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	um ratings	•				
V _{ESD}	voltage	IEC 61000-4-2; contact discharge	[2]	-	30	kV
		ISO 10605: contact discharge; C = 330 pF, R = 330 Ω	[2]	-	30	kV
		ISO 10605: contact discharge; C = 150 pF, R = 330 Ω	[2]	-	30	kV

Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
 Device stressed with ten non-repetitive ESD pulses.



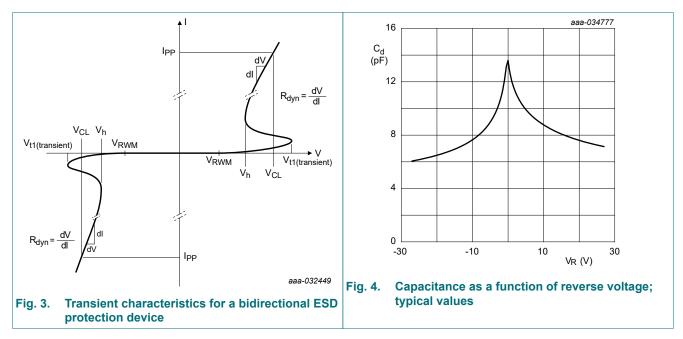


9. Characteristics

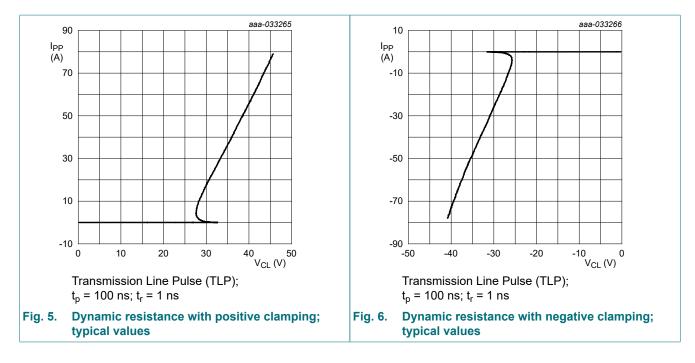
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	27	V
V _{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C		28	33	38	V
I _{RM}	reverse leakage current	V _R = 27 V; T _{amb} = 25 °C		-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	14	17	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1]	-	34	43	V
		I _{PPM} = 3 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1]	-	36	45	V
		I _{PP} = 16 A; t _p = 100 ns; T _{amb} = 25 °C	[2]	-	35	-	V
R _{dyn}	dynamic resistance	I _R = 10 A; t _p = 100 ns; T _{amb} = 25 °C	[2]	-	0.3	-	Ω

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

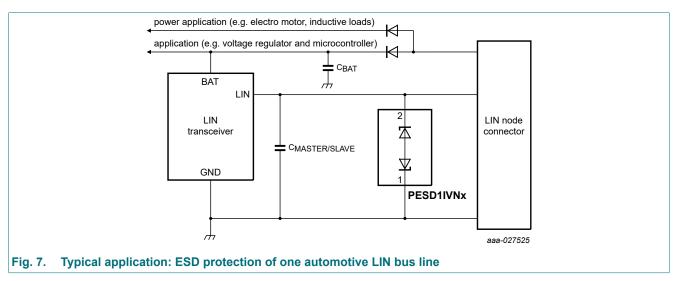


Bidirectional ESD protection diode



10. Application information

The device is designed for the protection of one automotive IVN bus line from the damage caused by ESD and surge pulses.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

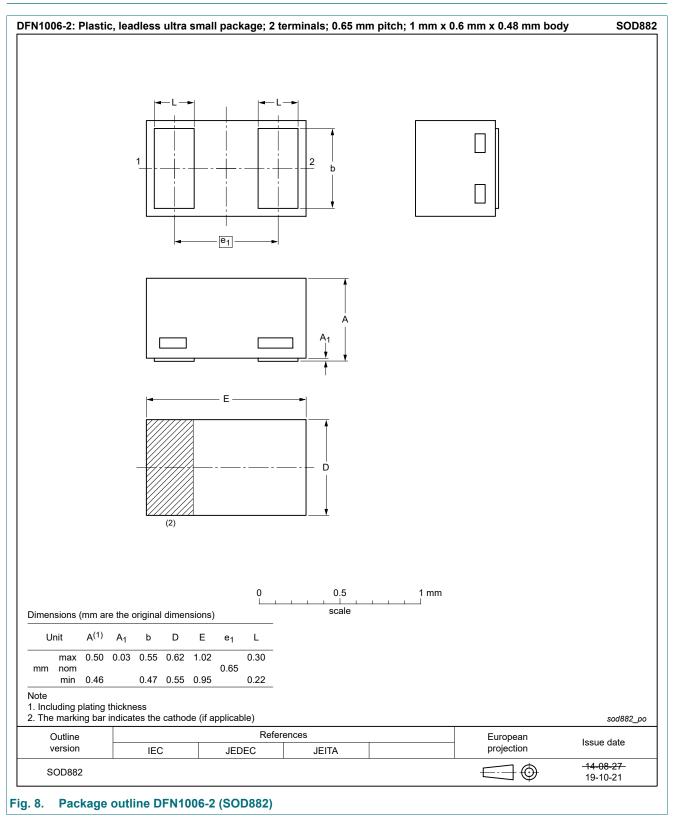
11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

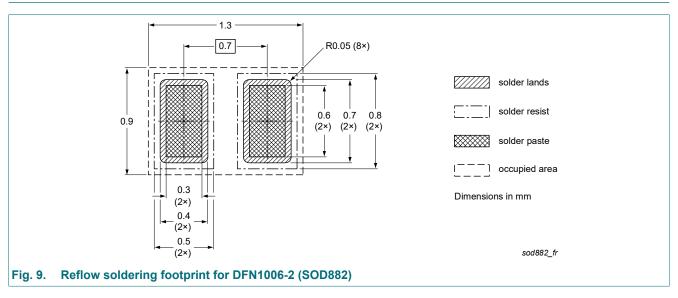
Bidirectional ESD protection diode

12. Package outline



Bidirectional ESD protection diode

13. Soldering



Bidirectional ESD protection diode

14. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD1IVN27L-Q v.1	20230307	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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