

# PMEG100T120ELPE

100 V, 12 A low leakage current Trench MEGA Schottky barrier rectifier

27 September 2021

**Product data sheet** 

### 1. General description

Trench Maximum Efficiency General Application (MEGA) Schottky barrier rectifier encapsulated in a CFP15B (SOT1289B) power and flat lead Surface-Mounted Device (SMD) plastic package.

#### 2. Features and benefits

- Low forward voltage
- Low Q<sub>rr</sub> and low I<sub>RM</sub>
- · Low leakage current
- High power capability due to clip-bonding technology
- Small and flat lead SMD power plastic package

### 3. Applications

- · High efficiency DC-to-DC conversion
- LED lighting
- · Switch mode power supply
- · Freewheeling application
- Reverse polarity protection
- OR-ing

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> $\leq$ 156 °C		-	-	12	А
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	100	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 12 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	740	840	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	1.1	6	μA
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 125 °C	[1]	-	1.5	6	mA
t <sub>rr</sub>	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 ^{\circ}\text{C}$		-	28	-	ns

[1] Very short pulse, in order to maintain a stable junction temperature.



### 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A	anode	5	
2	A	anode		K A
3	K	cathode	2	aaa-009063
			CFP15B (SOT1289B)	

### 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package							
	Name	Description	Version					
PMEG100T120ELPE		plastic, thermal enhanced ultra thin SMD package; 3 leads; 2.13 mm pitch; 5.8 x 4.3 x 0.95 mm body	SOT1289B					

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG100T120ELPE	100T
	L12E

### 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	100	V
I <sub>F</sub>	forward current	δ = 1; T <sub>sp</sub> ≤ 150 °C		-	16.9	А
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 156 °C		-	12	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	230	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

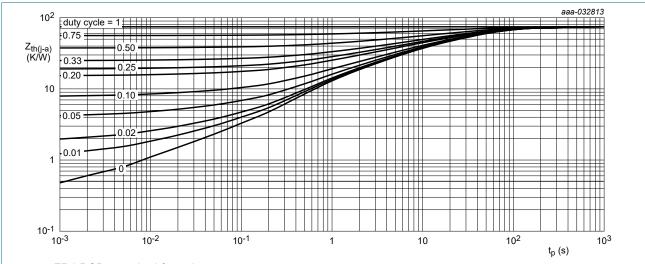
- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

#### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

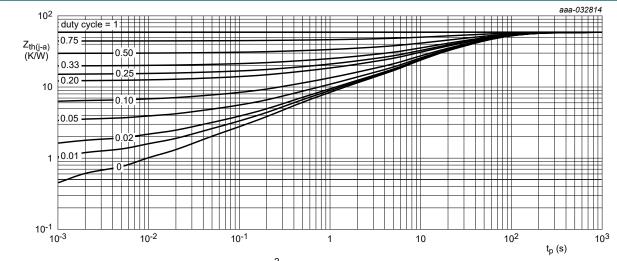
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from	in free air	[1] [2]	-	-	90	K/W
ju	junction to ambient		[1] [3]	-	-	70	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	7	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

### 10. Characteristics

**Table 7. Characteristics** 

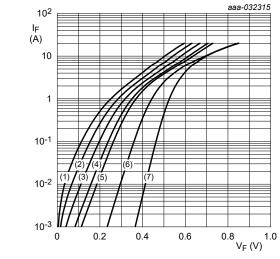
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>j</sub> = 25 °C	[1]	100	-	-	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	445	540	mV
		I <sub>F</sub> = 5 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	580	660	mV
		I <sub>F</sub> = 8 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	660	730	mV
		I <sub>F</sub> = 10 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	695	780	mV
		I <sub>F</sub> = 12 A; pulsed; T <sub>j</sub> = 25 °C	[1]	-	740	840	mV
		I <sub>F</sub> = 12 A; pulsed; T <sub>j</sub> = -40 °C	[1]	-	740	840	mV
		I <sub>F</sub> = 12 A; pulsed; T <sub>j</sub> = 125 °C	[1]	-	610	700	mV
		I <sub>F</sub> = 12 A; pulsed; T <sub>j</sub> = 150 °C	[1]	-	585	660	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 60 V; pulsed; T <sub>j</sub> = 25 °C	[1]	-	0.4	2.2	μΑ
		$V_R$ = 100 V; pulsed; $T_j$ = 25 °C	[1]	-	1.1	6	μΑ
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 125 °C	[1]	-	1.5	6	mA
		V <sub>R</sub> = 100 V; pulsed; T <sub>j</sub> = 150 °C	[1]	-	6	30	mA
C <sub>d</sub>	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$		-	1050	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	300	-	рF
t <sub>rr</sub>	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$		-	28	-	ns
	reverse recovery time ramp recovery	$dI_F/dt = 200 \text{ A/}\mu\text{s}; I_F = 6 \text{ A}; V_R = 26 \text{ V};$ $T_j = 25 \text{ °C}$		-	14	-	ns
I <sub>RM</sub>	peak reverse recovery current			-	1.3	-	Α
Q <sub>rr</sub>	reverse recovery charge			-	14	-	nC
$V_{FRM}$	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$		-	420	-	mV
		1					

<sup>[1]</sup> Very short pulse, in order to maintain a stable junction temperature.

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#### 100 V, 12 A low leakage current Trench MEGA Schottky barrier rectifier

10<sup>-1</sup>



pulsed condition

(1) Tj = 175 °C

(2) Tj = 150 °C

(3) Tj = 125 °C

(4) Tj = 100 °C

(5) Tj = 85 °C (6) Tj = 25 °C

(7) Tj = -40 °C

Fig. 3. Forward current as a function of forward voltage; typical values

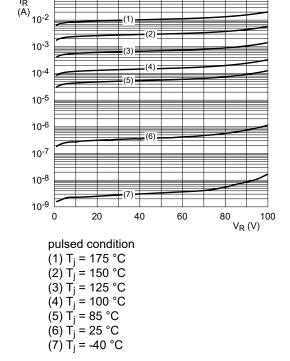


Fig. 4. Reverse current as a function of reverse voltage; typical values

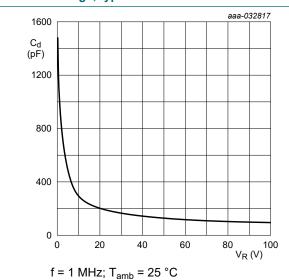
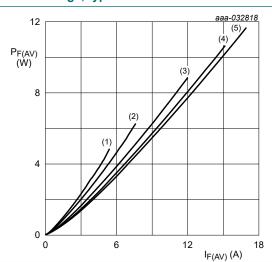


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



 $T_j = 100 \, ^{\circ}C$ 

 $(1) \delta = 0.1$ 

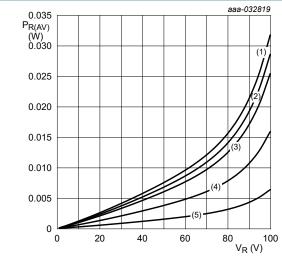
 $(2) \delta = 0.2$ 

 $(3) \delta = 0.5$ 

 $(4) \delta = 0.8$ 

(5)  $\delta = 1$ ; DC

Fig. 6. Average forward power dissipation as a function of average forward current; typical values



T<sub>j</sub> = 100 °C

 $(1) \delta = 1$ ; DC

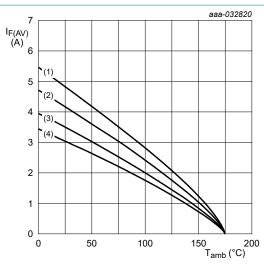
 $(2) \delta = 0.9$ 

 $(3) \delta = 0.8$ 

 $(4) \delta = 0.5$ 

 $(5) \delta = 0.2$ 

Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T<sub>i</sub> = 175 °C

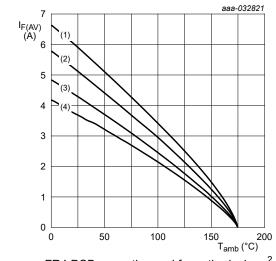
 $(1) \delta = 1; DC$ 

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

T<sub>i</sub> = 175 °C

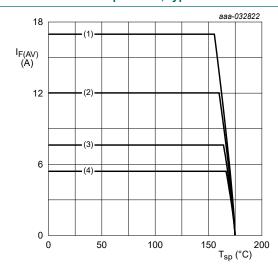
 $(1) \delta = 1$ ; DC

 $(2) \delta = 0.5$ ; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



Tj = 175 °C

(1)  $\delta = 1$ ; DC

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values

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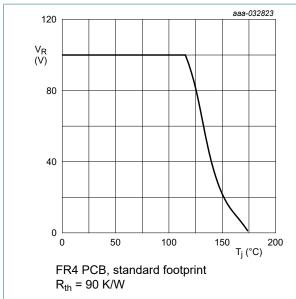
#### 100 V, 12 A low leakage current Trench MEGA Schottky barrier rectifier

120

80

40

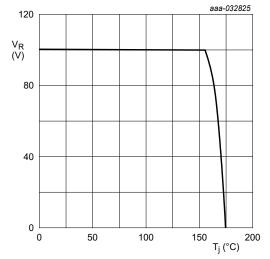
V<sub>R</sub> (V)



50 100 FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>  $R_{th} = 70 \text{ K/W}$ 

of junction temperature; typical values

Fig. 11. Derated maximum reverse voltage as a function | Fig. 12. Derated maximum reverse voltage as a function of junction temperature; typical values



Soldering point of cathode tab  $R_{th} = 7 \text{ K/W}$ 

Fig. 13. Derated maximum reverse voltage as a function of junction temperature; typical values

### 11. Test information

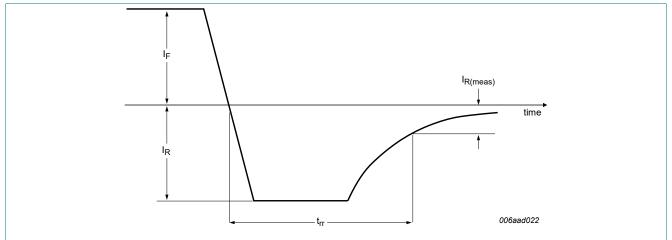


Fig. 14. Reverse recovery definition; step recovery

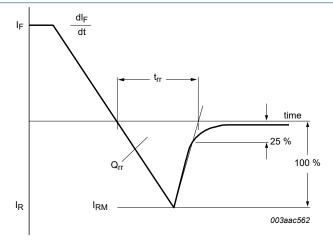


Fig. 15. Reverse recovery definition; ramp recovery

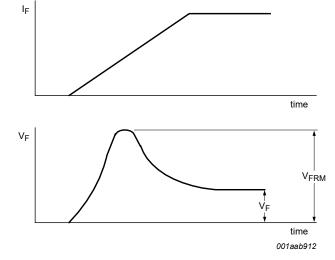
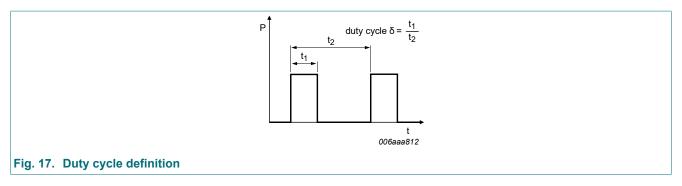
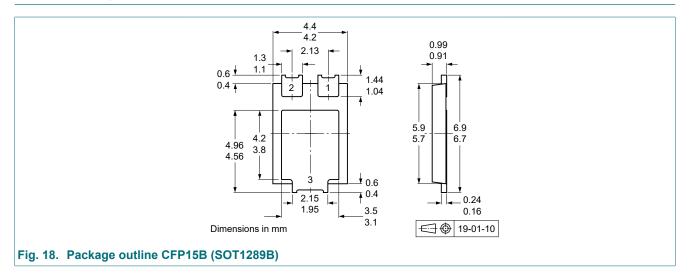


Fig. 16. Forward recovery definition

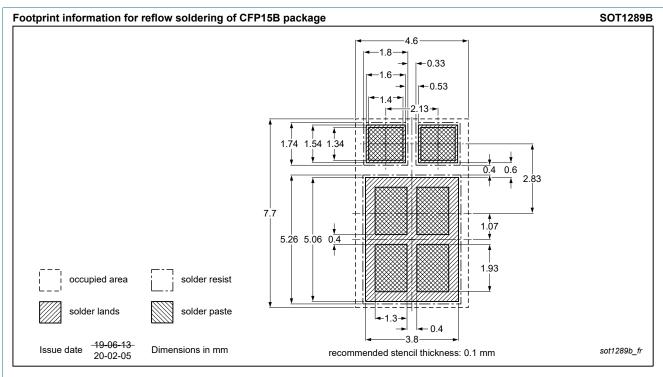


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current

### 12. Package outline



## 13. Soldering



# 14. Revision history

#### **Table 8. Revision history**

		,					
Data sheet ID	Release date	Data sheet status	Change notice Supersedes				
PMEG100T120ELPE v.2	20210927	Product data sheet	- PMEG100T120ELPE v.1				
Modifications:	<ul> <li>Product status changed</li> <li>Characteristics: Values at "forward voltage" partially changed and Fig. 6 adapted</li> </ul>						
PMEG100T120ELPE v.1	20210119	Objective data sheet					

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#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Date of release: 27 September 2021

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