Ultra low capacitance quadruple rail-to-rail ESD protection

14 April 2023 Product data sheet

## 1. General description

Ultra low capacitance quadruple rail-to-rail ElectroStatic Discharge (ESD) protection device in an SOT457 (SC-74) small Surface-Mounted Device (SMD) plastic package.

The device is designed to protect four high-speed data lines or high-frequency signal lines from the damage caused by ESD and other transients.

PRTR5V0U4D integrates four ultra low capacitance rail-to-rail ESD protection channels and one additional ESD protection diode to ensure signal line protection even if no supply voltage is available.

### 2. Features and benefits

- · ESD protection of four high-speed data lines or high-frequency signal lines
- Ultra low input/output to ground capacitance: C<sub>(I/O-GND)</sub> = 1 pF
- ESD protection up to 8 kV
- IEC 61000-4-2, level 4 (ESD)
- Very low clamping voltage due to an integrated additional ESD protection diode
- · Very low reverse current
- Small SMD plastic package

## 3. Applications

- USB 2.0 interfaces
- Digital Video Interface (DVI)
- · High-Definition Multimedia Interface (HDMI)
- Mobile phones
- Digital cameras
- WAN/LAN systems
- · PC, notebooks, printers and other PC peripherals

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
VI	input voltage	T <sub>amb</sub> = 25 °C		0	-	5.5	V
C <sub>(I/O-GND)</sub>	input/output to ground capacitance	$V_{(I/O-GND)} = 0 \text{ V; } V_{CC} = 3 \text{ V; } f = 1 \text{ MHz;}$ $T_{amb} = 25 \text{ °C}$	[1]	-	1	-	pF
C <sub>sup</sub>	supply pin to ground capacitance	$V_{(I/O-GND)} = 0 \text{ V; } V_{CC} = 3 \text{ V; } f = 1 \text{ MHz;} $ $T_{amb} = 25 \text{ °C}$	[2]	-	40	-	pF

- [1] Measured from pins 1, 3, 4 and 6 to pin 2.
- [2] Measured from pin 5 to pin 2.



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I/O1	input/output 1		6 5 4
2	GND	ground		
3	I/O2	input/output 2	<u> </u>	本 本 本 本 本 本 本 本 本 本 本 本 本 本 本 本 本 本 本
4	I/O3	input/output 3		
5	V <sub>CC</sub>	supply voltage	1 2 3	
6	I/O4	input/output 4	SC-74; TSOP6 (SOT457)	1 2 3
				001aag273

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package			
	Name	Description	Version	
PRTR5V0U4D		plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457	

# 7. Marking

#### **Table 4. Marking codes**

Type number	Marking code
PRTR5V0U4D	4D

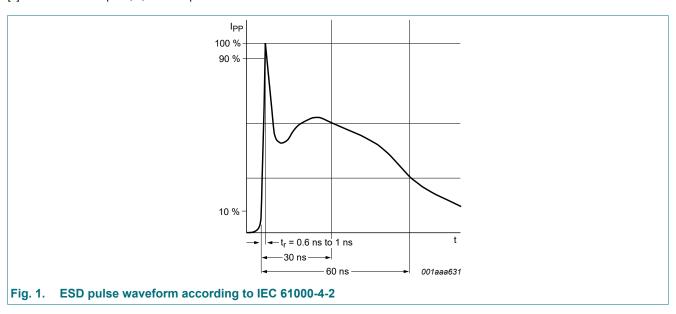
## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximum	ratings					
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge); T <sub>amb</sub> = 25 °C	[1] [2]	-	8	kV
		MIL-STD-883 (human body model); T <sub>amb</sub> = 25 °C		-	8	kV

- [1] Device stressed with ten non-repetitive ESD pulses.
- [2] Measured from pin 1, 3, 4 or 6 to pin 2 or 5.



### 9. Characteristics

#### **Table 6. Characteristics**

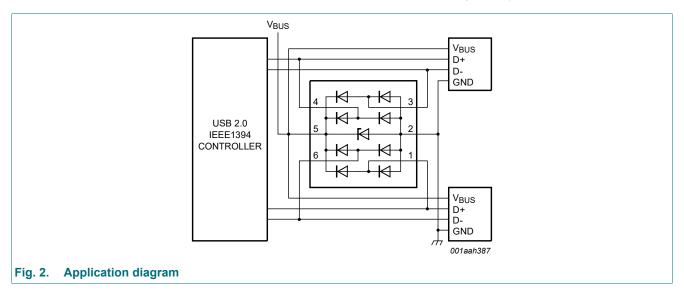
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	T <sub>amb</sub> = 25 °C		-	0.7	-	V
VI	input voltage	T <sub>amb</sub> = 25 °C		0	-	5.5	V
$V_{BR}$	breakdown voltage	I <sub>I</sub> = 1 mA; T <sub>amb</sub> = 25 °C		6	-	9	V
I <sub>RM</sub>	reverse leakage current	V <sub>R</sub> = 3 V; T <sub>amb</sub> = 25 °C	[1]	-	-	100	nA
C <sub>(I/O-GND)</sub>		$V_{(I/O-GND)} = 0 \text{ V; } V_{CC} = 3 \text{ V; } f = 1 \text{ MHz;}$ $T_{amb} = 25 \text{ °C}$	[1]	-	1	-	pF
C <sub>sup</sub>	supply pin to ground capacitance	$V_{(I/O-GND)} = 0 \text{ V}; V_{CC} = 3 \text{ V}; f = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	[2]	-	40	-	pF

- [1] Measured from pins 1, 3, 4 and 6 to pin 2.
- [2] Measured from pin 5 to pin 2.

#### Ultra low capacitance quadruple rail-to-rail ESD protection

# 10. Application information

The device is designed for the protection of for example, two USB 2.0 ports against ESD. Each device is capable to protect both, USB data lines and the  $V_{BUS}$  supply.



# 11. Package outline

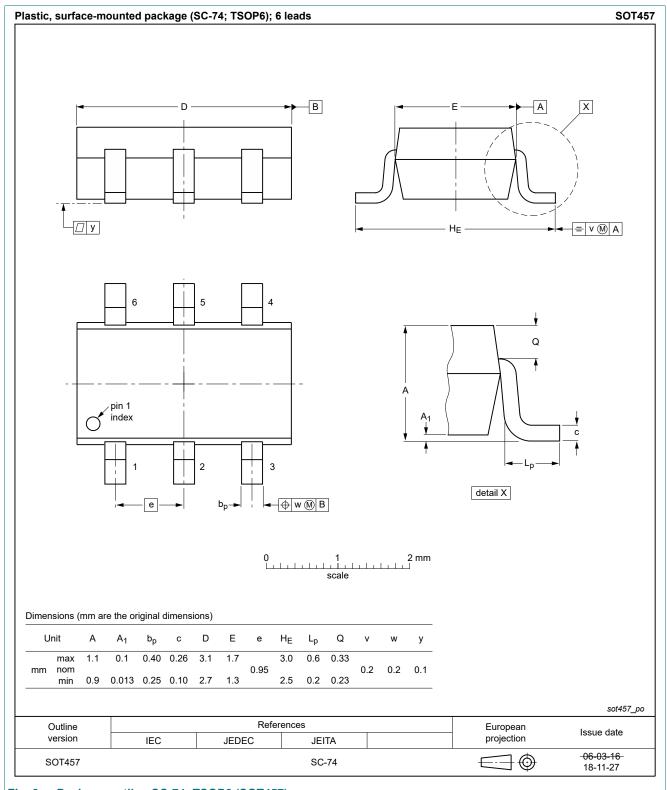
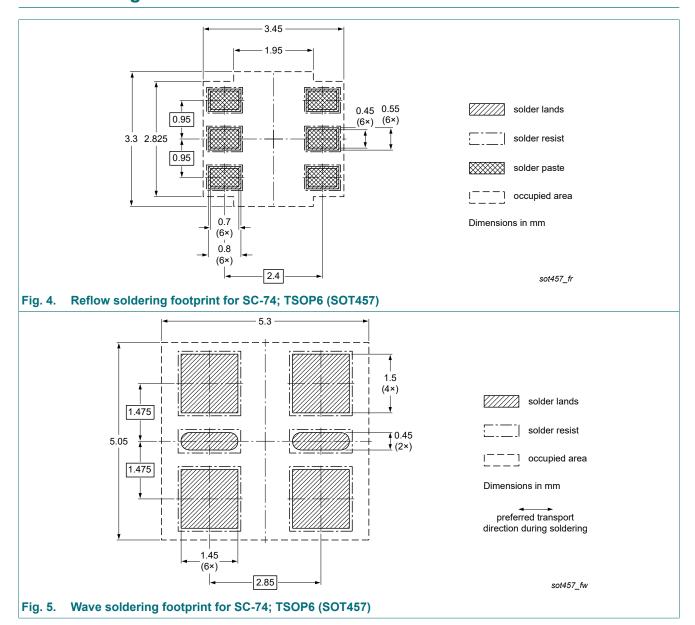


Fig. 3. Package outline SC-74; TSOP6 (SOT457)

#### Ultra low capacitance quadruple rail-to-rail ESD protection

# 12. Soldering



### Ultra low capacitance quadruple rail-to-rail ESD protection

# 13. Revision history

#### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PRTR5V0U4D v.3	20230414	Product data sheet	-	PRTR5V0U4D v.2			
Modifications:	<ul> <li>Product changed to non-automotive qualification. Please refer to nexperia.com for automotical (-Q) product alternative(s).</li> </ul>						
PRTR5V0U4D v.2	20120305	Product data sheet	-	PRTR5V0U4D v.1			
PRTR5V0U4D v.1	20080111	Product data sheet	-	-			

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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