

BC847BVN

45 V, 100 mA NPN/PNP general purpose transistor

27 December 2022

Product data sheet

1. General description

NPN/PNP transistor in a SOT666 ultra small Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm ultra thin package
- Replaces two SC-75/SC-89 packaged transistors on same PCB area
- Reduced required PCB area
- Reduced pick and place costs

3. Applications

- General purpose switching and amplification
- · Switch mode power supply complementary MOSFET driver
- Complementary driver for audio amplifiers

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per transistor	r; for the PNP transistor	with negative polarity				
V _{CEO}	collector-emitter voltage	open base	-	-	45	V
I _C	collector current		-	-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	200	mA
h _{FE}	DC current gain	V_{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	200	-	450	

5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	E1	emitter TR1	6 5 4	C1 B2 E2			
2	B1	base TR1					
3	C2	collector TR2					
4	E2	emitter TR2					
5	B2	base TR2		 E1 B1 C2			
6	C1	collector TR1	SOT666	sym019			



6. Ordering information

Table 3. Ordering information						
Type number						
	Name	Description	Version			
BC847BVN	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>			

7. Marking

Table 4. Marking codes	
Type number	Marking code
BC847BVN	13

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
Per transist	or; for the PNP transistor wit	h negative polarity	1			
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
Per device	I		1			
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

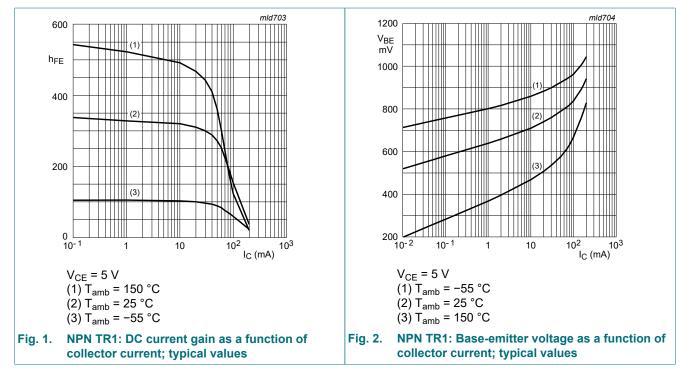
Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							-
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	or; for the PNP transistor v	with negative polarity	I			
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	15	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; \text{ I}_{C} = 2 \text{ mA}; \text{ T}_{amb} = 25 \text{ °C}$	200	-	450	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	-	100	mV
saturation voltage	I_C = 100 mA; I_B = 5 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	300	mV	
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C	-	755	-	mV
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	100	-	-	MHz
NPN transis	stor	· · · · ·	I	_		
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	580	655	700	mV
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	1.5	pF
C _e	emitter capacitance	$V_{EB} = 500 \text{ mV}; I_C = 0 \text{ A}; i_c = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-	11	-	pF
PNP transis	stor			_		
V _{BE}	base-emitter voltage	V _{CE} = -5 V; I _C = -2 mA; T _{amb} = 25 °C	-600	-655	-750	mV
C _c	collector capacitance	V_{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	2.2	pF
C _e	emitter capacitance	V _{EB} = -500 mV; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	10	-	pF

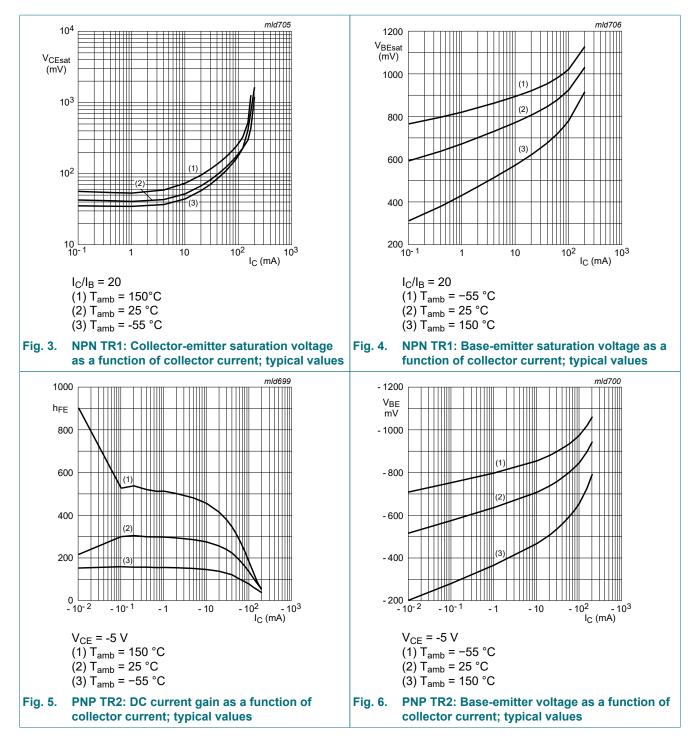


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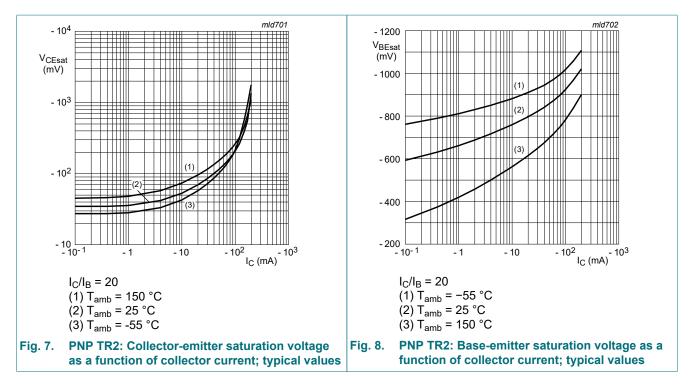
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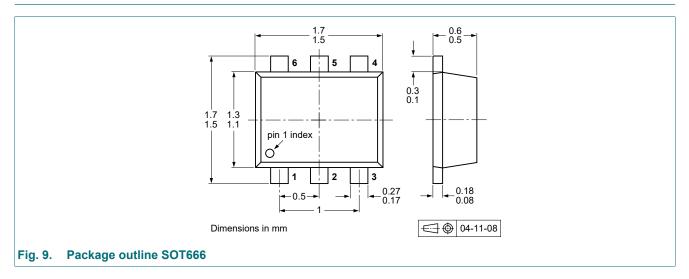
Product data sheet

BC847BVN

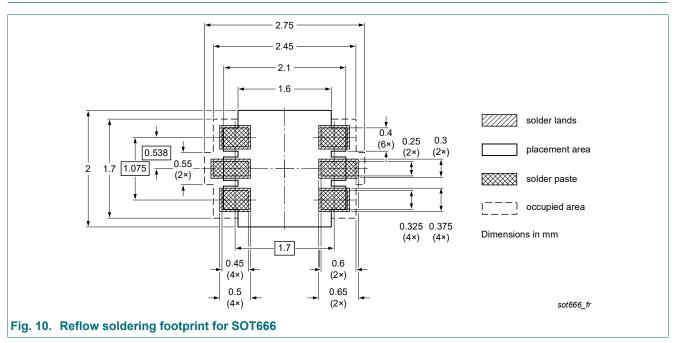
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11. Package outline



12. Soldering



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13. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC847BVN v.4	20221227	Product data sheet	-	BC847BVN v.3
Modifications:	Product change	ed to non-automotive qualific	ation	
BC847BVN v.3	20190520	Product data sheet	-	BC847BVN v.2
BC847BVN v.2	20011107	Product data sheet	-	BC847BVN v.1
BC847BVN v.1	20010830	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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Product data sheet

Contents

1.	General description	1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	1
5.	Pinning information	1
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	. 2
9.	Thermal characteristics	. 2
10.	Characteristics	3
11.	Package outline	. 5
12.	Soldering	. 6
	Revision history	
14.	Legal information	8

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9/9

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