50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

25 April 2023

Product data sheet

## 1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT883 (SC-101) Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA114YM

#### 2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- · Simplifies circuit design
- · Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

## 3. Applications

- · Digital application in automotive and industrial segments
- · Cost-saving alternative for BC847 series in digital applications
- · Controlling IC inputs
- · Switching loads

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	7	10	13	kΩ
R2/R1	bias resistor ratio		[1]	3.7	4.7	5.7	

[1] See "Section 11: Test information" for resistor calculation and test conditions.



50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	GND	ground (emitter)		
3	0	output (collector)		R1
			1 2	GND R2
			Transparent top view  DFN1006-3 (SOT883)	sym007

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PDTC114YM		plastic, leadless ultra small package; 3 terminals; 0.35 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOT883			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PDTC114YM	DU

## 8. Limiting values

### Table 5. Limiting values

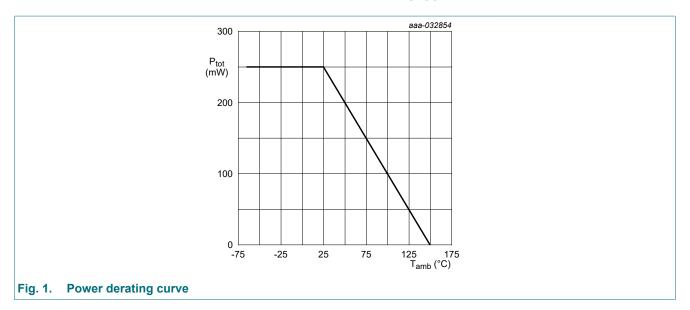
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
VI	input voltage			-6	40	V
Io	output current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Reflow soldering is the only recommended soldering method.
- [2] Device mounted on an FR4 PCB with 70 µm copper strip line, standard footprint.

PDTC114YM

### 50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$

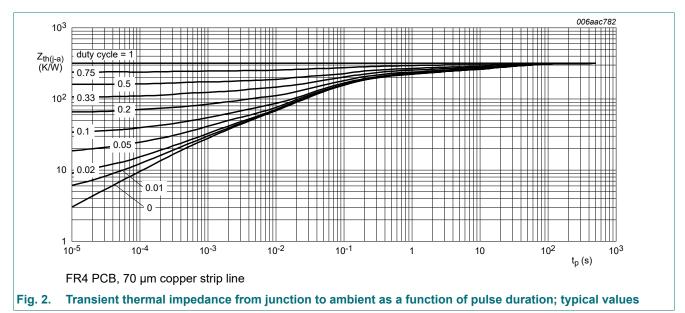


### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uily-a)	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W

- [1] Reflow soldering is the only recommended soldering method.
- [2] Device mounted on an FR4 PCB with 70 µm copper strip line, standard footprint.



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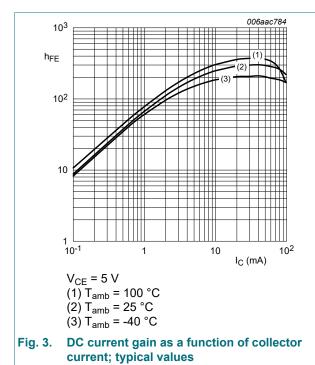
50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

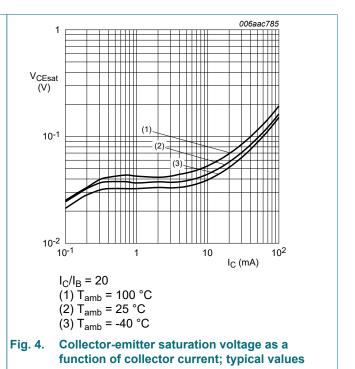
## 10. Characteristics

**Table 7. Characteristics** 

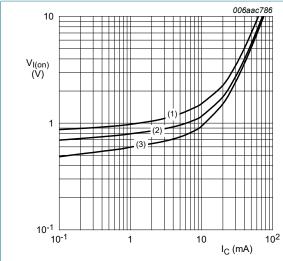
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		50	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
I <sub>CEO</sub>	collector-emitter cut-off	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	150	μA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C		100	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.25 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	100	mV
$V_{I(off)}$	off-state input voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 μA; T <sub>amb</sub> = 25 °C		-	0.7	0.5	V
V <sub>I(on)</sub>	on-state input voltage	$V_{CE} = 0.3 \text{ V}; I_{C} = 1 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		1.4	8.0	-	V
R1	bias resistor 1 (input)		[1]	7	10	13	kΩ
R2/R1	bias resistor ratio		[1]	3.7	4.7	5.7	
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25  ^{\circ}\text{C}$		-	-	2.5	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 5 V; $I_{C}$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	[2]	-	230	-	MHz

- [1] See "Section 11: Test information" for resistor calculation and test conditions.
- [2] Characteristics of built-in transistor.





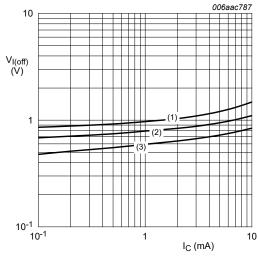
### 50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$



 $V_{CE} = 0.3 V$ 

(1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = 100 °C

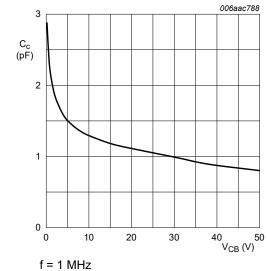
Fig. 5. On-state input voltage as a function of collector | Fig. 6. current; typical values



V<sub>CE</sub> = 5 V (1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C

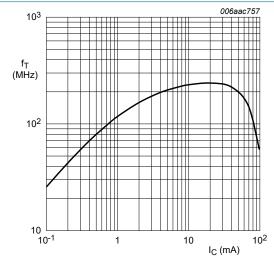
(3)  $T_{amb} = 100 \, ^{\circ}C$ 

Off-state input voltage as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 7. Collector capacitance as a function of collectorbase voltage; typical values



 $V_{CE}$  = 5 V;  $T_{amb}$  = 25 °C

Fig. 8. Transition frequency as a function of collector current; typical values of built-in transistor

50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

## 11. Test information

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

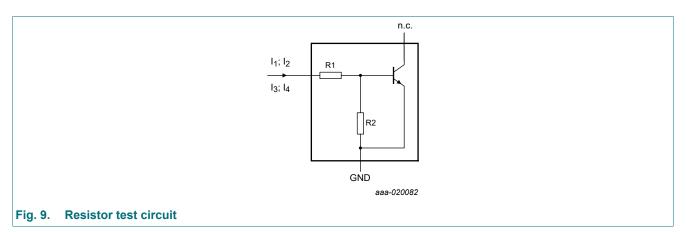
#### **Resistor calculation**

• Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$



## Resistor test conditions

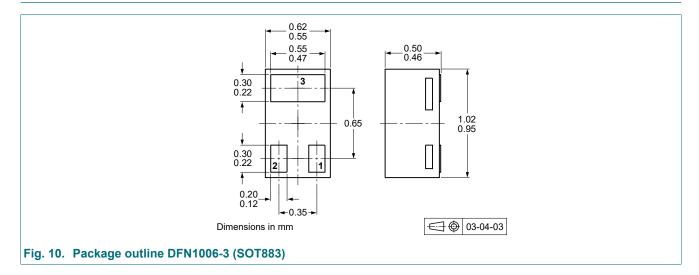
**Table 8. Resistor test conditions** 

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I <sub>1</sub>	l <sub>2</sub>	l <sub>3</sub>	l <sub>4</sub>
PDTC114YM	10	47	90 μΑ	140 μΑ	-55 μΑ	-105 µA

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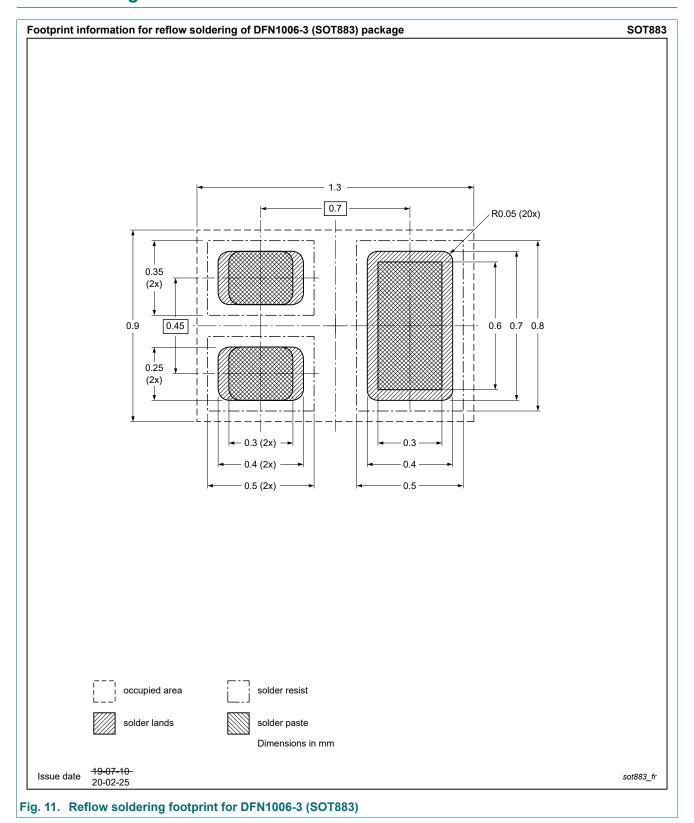
50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

# 12. Package outline



50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

# 13. Soldering



50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$ 

# 14. Revision history

### Table 9. Revision history

Table 9. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PDTC114YM v.8	20230425	Product data sheet	-	PDTC114Y_SER v.7			
Modification:	<ul> <li>Family data sheet splitted to single type data sheets.</li> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Packing information removed.</li> <li>Characteristics: Value corrected for I<sub>CFO</sub> at 25°C.</li> </ul>						
PDTC114Y_SER v.7	20111118	Product data sheet	-	PDTC114Y_SERIES v.6			
PDTC114Y_SERIES v.6	20040817	Product data sheet	-	PDTC114Y_SERIES v.5			
PDTC114Y_SERIES v.5	20040910	Product specification	-	PDTC114Y_SERIES v.4			
PDTC114Y_SERIES v.4	20030414	Product specification	-	-			

## 50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 kΩ, R2 = 47 kΩ

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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### 50 V, 100 mA NPN resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 47 k $\Omega$

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