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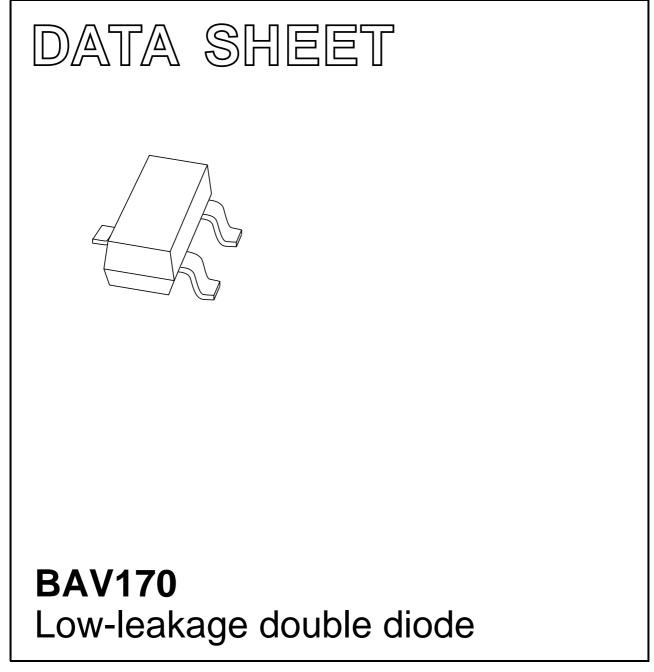
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1999 May 11 2003 Mar 25



FEATURES

 Repetitive peak reverse voltage: max. 85 V

• Low leakage current: typ. 3 pA

• Switching time: typ. 0.8 μs

· Continuous reverse voltage:

Low-leakage double diode

• Repetitive peak forward current: max. 500 mA.

APPLICATION

• Low-leakage current applications in surface mounted circuits.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
BAV170	JX*	

Note

- 1. * = p : Made in Hong Kong.
 - * = t : Made in Malaysia.
 - * = W : Made in China.

DESCRIPTION

Epitaxial, medium-speed switching, double diode in a small SOT23 plastic SMD package. The diodes are in common cathode configuration.

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	IN	INI	UNG.	

PIN	DESCRIPTION
1	anode
2	anode
3	common cathode

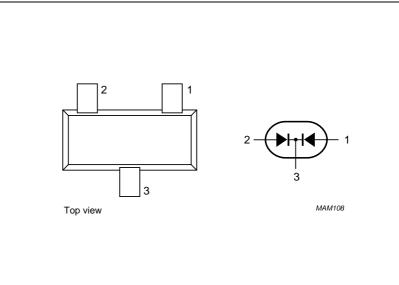


Fig.1 Simplified outline (SOT23) and symbol.

• Plastic SMD package

BAV170

2

BAV170

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
Per diode	Per diode					
V _{RRM}	repetitive peak reverse voltage		_	85	V	
V _R	continuous reverse voltage		-	75	V	
I _F	continuous forward current	single diode loaded; note 1; see Fig.2	-	215	mA	
		double diode loaded; note 1; see Fig.2	-	125	mA	
I _{FRM}	repetitive peak forward current		-	500	mA	
I _{FSM}	non-repetitive peak forward current	square wave; T _j = 25 °C prior to surge; see Fig.4				
		$t_p = 1 \ \mu s$	_	4	А	
		t _p = 1 ms	_	1	А	
		$t_p = 1 s$	_	0.5	А	
P _{tot}	total power dissipation	T _{amb} = 25 °C; note 1	-	250	mW	
T _{stg}	storage temperature		-65	+150	°C	
Tj	junction temperature		_	150	°C	

Note

1. Device mounted on a FR4 printed-circuit board.

ELECTRICAL CHARACTERISTICS

 $T_j = \ 25 \ ^\circ C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
Per diode					
V _F	forward voltage	see Fig.3			
		I _F = 1 mA	_	900	mV
		I _F = 10 mA	_	1000	mV
		I _F = 50 mA	_	1100	mV
		I _F = 150 mA	_	1250	mV
I _R	reverse current	see Fig.5			
		V _R = 75 V	0.003	5	nA
		V _R = 75 V; T _j = 150 °C	3	80	nA
C _d	diode capacitance $f = 1 \text{ MHz}; V_R = 0; \text{ see Fig.6}$		2	-	pF
t _{rr}	reverse recovery time	when switched from $I_F = 10$ mA to $I_R = 10$ mA; $R_L = 100 \Omega$; measured at $I_R = 1$ mA; see Fig.7	0.8	3	μs

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Low-leakage double diode

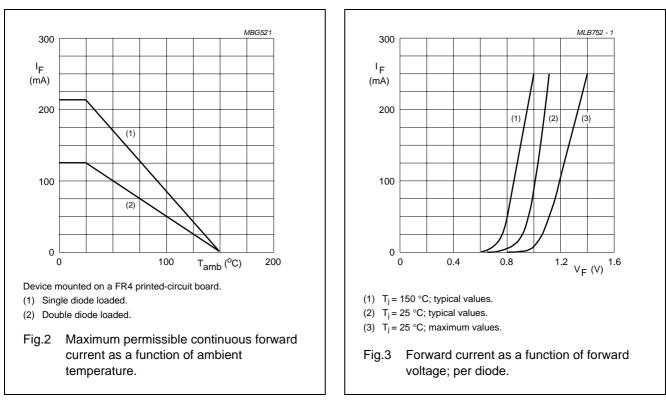
THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-tp}	thermal resistance from junction to tie-point		360	K/W
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

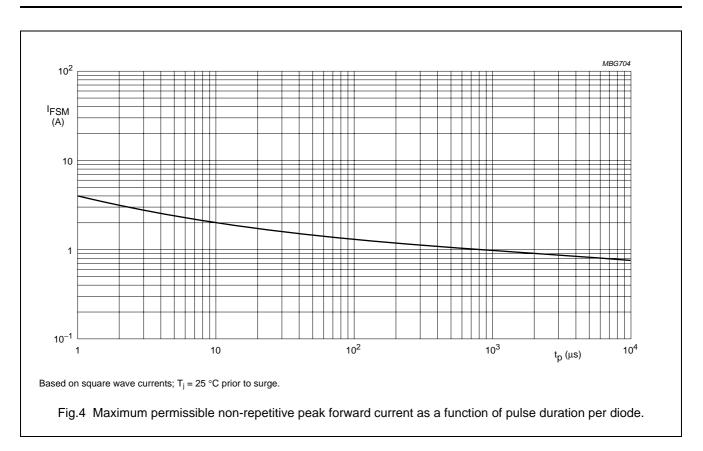
1. Device mounted on a FR4 printed-circuit board.

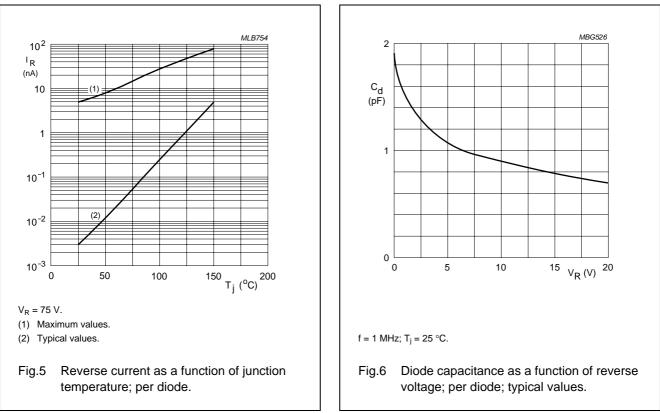
GRAPHICAL DATA



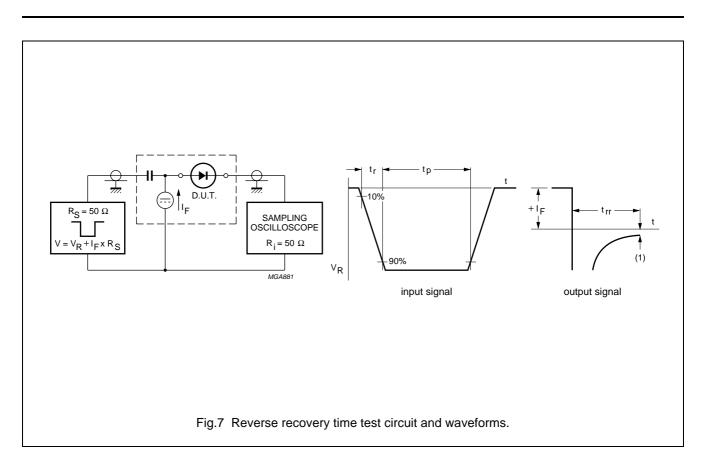
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Low-leakage double diode



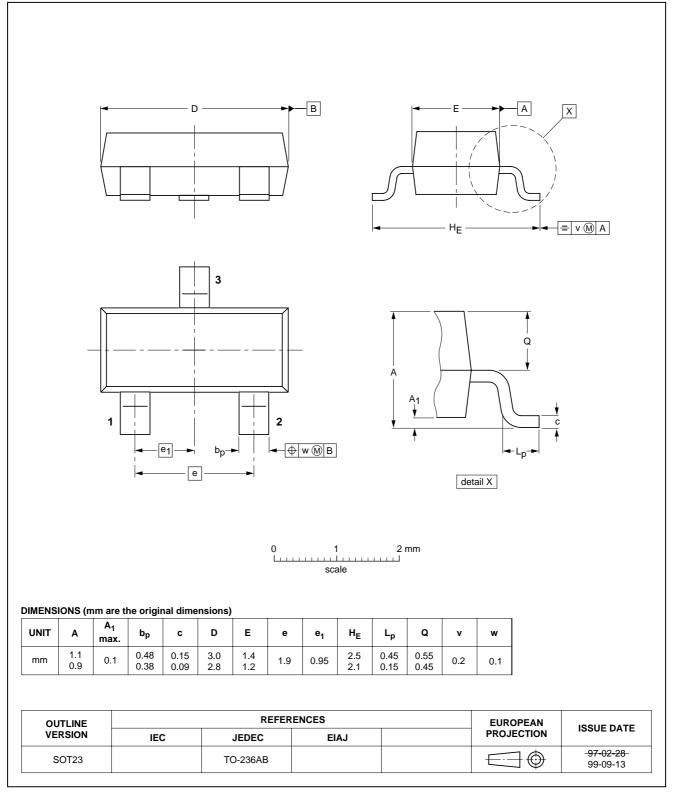


BAV170



PACKAGE OUTLINE





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DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

DATA SHEET STATUS

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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NXP Semiconductors

Customer notification

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Contact information

For additional information please visit: http://www.nxp.com For sales offices addresses send e-mail to: salesaddresses@nxp.com

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