Product data sheet

1. General description

NPN Darlington transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: BCV46

2. Features and benefits

- · Medium current of 500 mA
- Low voltage of 60 V
- · High DC current gain of minimum 2000
- AEC-Q101 qualified

3. Applications

· Preamplifier input amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-	80	V
V _{CES}	collector-emitter voltage	base short-circuited to emitter		-	-	60	V
I _C	collector current			-	-	500	mA
I _{CM}	peak collector current			-	-	800	mA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 100 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	[1]	10000	-	-	

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	B C
2	E	emitter		
3	С	collector		TR1 TR2
			12	E
			SOT23	aaa-029089

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCV47	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BCV47	FG%

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	80	V
V _{CES}	collector-emitter voltage	base short-circuited to emitter		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	10	V
I _C	collector current			-	500	mA
I _{CM}	peak collector current			-	800	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

10. Characteristics

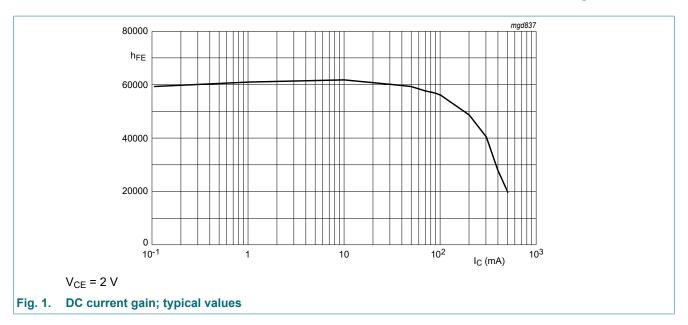
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		80	-	-	V
V _{(BR)CES}	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ °C}$		60	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	$I_C = 0 \text{ A}; I_E = 100 \mu\text{A}; T_{amb} = 25 \text{ °C}$		10	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 60 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = 60 V; V _{BE} = 0 V; T _{amb} = 25 °C		-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 10 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 1 \text{ mA}; T_{amb} = 25 \text{ °C}$	[1]	2000	-	-	
		V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C	[1]	4000	-	-	
		V _{CE} = 5 V; I _C = 100 mA; T _{amb} = 25 °C	[1]	10000	-	-	
		V _{CE} = 5 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	2000	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 100 \text{ mA}; I_B = 1 \text{ mA}; T_{amb} = 25 \text{ °C}$		-	-	1	V
V _{BEsat}	base-emitter saturation voltage	I_C = 100 mA; I_B = 0.1 mA; T_{amb} = 25 °C		-	-	1.5	V
V_{BEon}	base-emitter turn-on voltage	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; T_{amb} = 25 \text{ °C}$		-	-	1.4	V

^[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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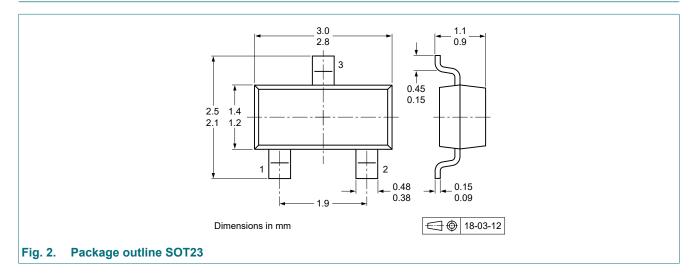


11. Test information

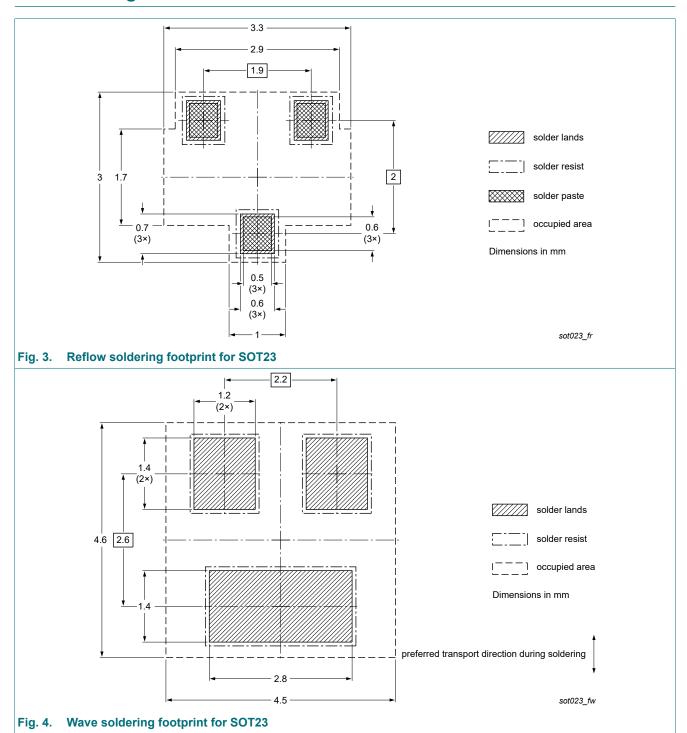
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCV47 v.3	20190821	Product data sheet	-	BCV27_BCV47 v.2
Modifications:	 The format of of Nexperia. Legal texts ha The product is "Test informat 	ive been adapted to the r s AEC-Q101 qualified. Po ion" and Legal Informatio	redesigned to connew company name oint added to sections.	nply with the identity guidelines
BCV27_BCV47 v.2	20040113	Product data sheet	-	BCV27_BCV47 v.1
BCV27_BCV47 v.1	19970408	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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