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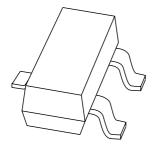
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



## BCX19 NPN general purpose transistor

Product data sheet Supersedes data of 2000 Jul 28 2004 Jan 16



NXP Semiconductors Product data sheet

## NPN general purpose transistor

**BCX19** 

#### **FEATURES**

- High current (500 mA)
- Low voltage (45 V).

#### **APPLICATIONS**

- · General purpose amplification
- Saturated switching and driver applications.

#### **DESCRIPTION**

NPN transistor in a SOT23 plastic package. PNP complement: BCX17.

#### **MARKING**

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
BCX19	U1*

#### Note

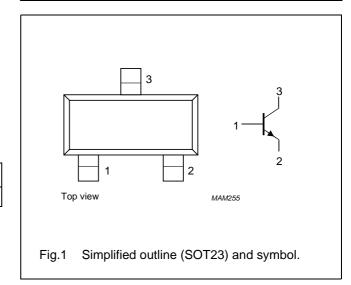
1. \* = p: Made in Hong Kong.

\* = t : Made in Malaysia.

\* = W : Made in China.

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **ORDERING INFORMATION**

TYPE	PACKAGE				
NUMBER	NAME	DESCRIPTION VERSION			
BCX19	_	plastic surface mounted package; 3 leads	SOT23		

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base; I <sub>C</sub> = 10 mA	_	45	V
$V_{EBO}$	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	500	mA
I <sub>CM</sub>	peak collector current		_	1	Α
I <sub>BM</sub>	peak base current		_	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

2004 Jan 16 2

NXP Semiconductors Product data sheet

## NPN general purpose transistor

BCX19

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 20 V	_	_	100	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 20 V; T <sub>j</sub> = 150 °C	_	_	5	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; note 1				
		I <sub>C</sub> = 100 mA	100	_	600	
		I <sub>C</sub> = 300 mA	70	_	_	
		I <sub>C</sub> = 500 mA	40	_	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 2	-	_	620	mV
$V_{BE}$	base-emitter voltage	I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 1 V; notes 1 and 2	_	_	1.2	V
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = I <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	-	5	_	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 5 V; f = 100 MHz	100	-	_	MHz

#### **Notes**

- 1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$
- 2.  $V_{BE}$  decreases by approximately -2 mV/ $^{\circ}$ C with increasing temperature.

2004 Jan 16 3

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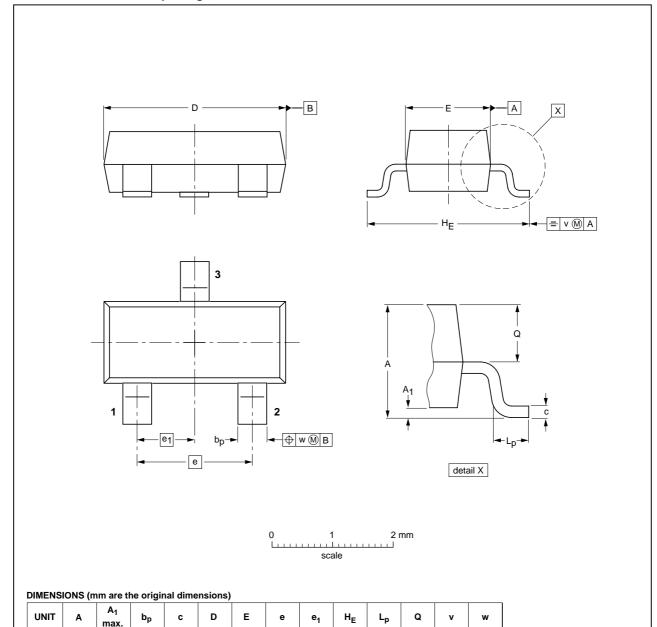
## NPN general purpose transistor

BCX19

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	IOOUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				<del>-04-11-04</del> 06-03-16

0.45

0.55

0.1

2004 Jan 16 4

max

0.38

0.9

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## NPN general purpose transistor

BCX19

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

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2004 Jan 16 5

## **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

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