# ne<mark>x</mark>peria

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Should be replaced with:

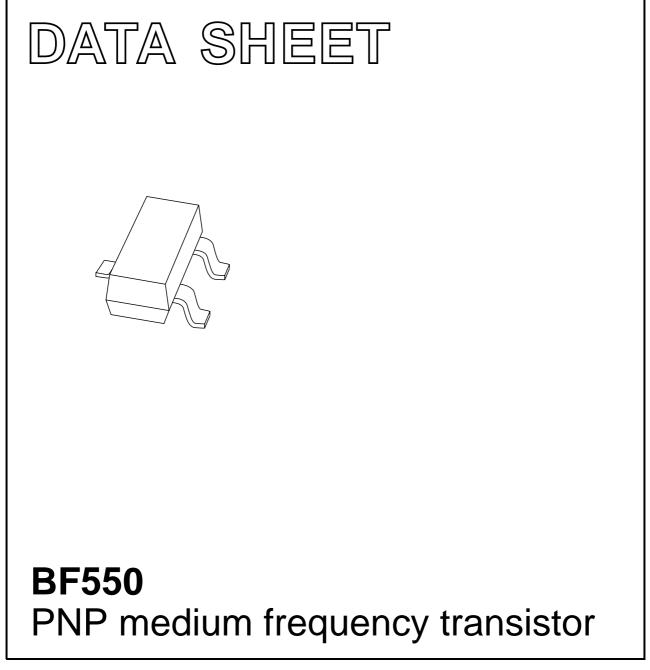
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If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

## DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1999 Apr 15 2004 Jan 16



# **PNP** medium frequency transistor

## FEATURES

- Low current (max. 25 mA)
- Low voltage (max. 40 V).

## APPLICATIONS

• Medium frequency applications in thick and thin film circuits.

## DESCRIPTION

PNP medium frequency transistor in a SOT23 plastic package.

## MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>		
BF550	LA*		

## Note

- 1. \* = p: Made in Hong Kong.
  - \* = t : Made in Malaysia.

\* = W : Made in China.

## **ORDERING INFORMATION**

TYPE		PACKAGE			
NUMBER	NAME	DESCRIPTION VERSIO			
BF550	_	plastic surface mounted package; 3 leads	SOT23		

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

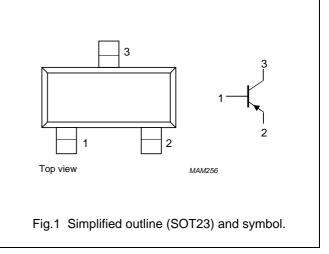
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	-40	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-4	V
I <sub>C</sub>	collector current (DC)		-	-25	mA
I <sub>CM</sub>	peak collector current		-	-25	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Тj	junction temperature		-	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

## Note

1. Transistor mounted on an FR4 printed-circuit board.

## PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



## **BF550**

# PNP medium frequency transistor

## BF550

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT	
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	500	K/W	

### Note

1. Transistor mounted on an FR4 printed-circuit board.

## **CHARACTERISTICS**

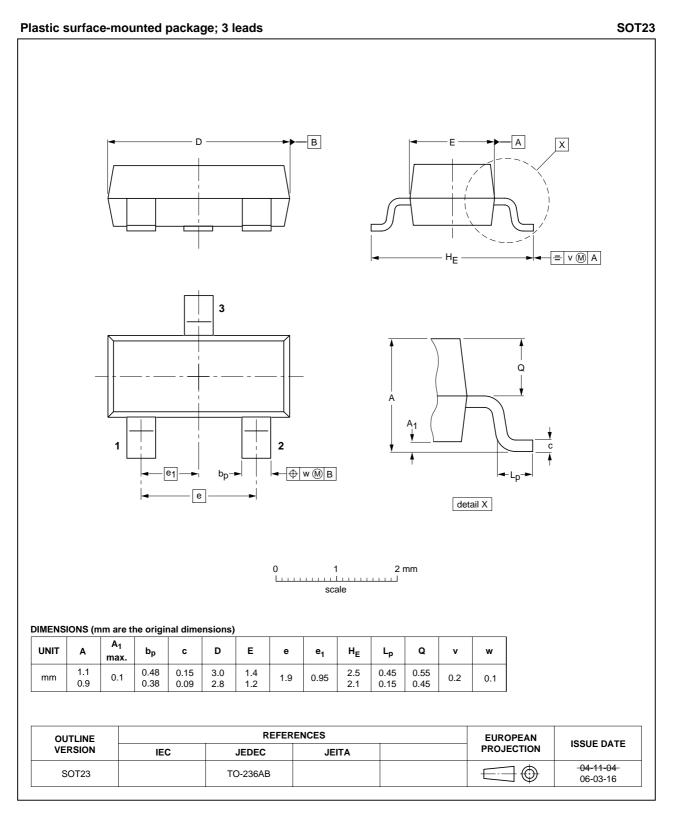
 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	$I_E = 0; V_{CB} = -30 V$	-	-	-50	nA
I <sub>EBO</sub>	emitter cut-off current	$I_{C} = 0; V_{EB} = -3 V$	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$I_{C} = -1 \text{ mA}; V_{CE} = -10 \text{ V}$	50	_	_	
V <sub>BE</sub>	base-emitter voltage	$I_{C} = -1 \text{ mA}; V_{CE} = -10 \text{ V}$	-	750	-	mV
C <sub>re</sub>	feedback capacitance	$I_{C} = -1 \text{ mA}; V_{CB} = -10 \text{ V}; \text{ f} = 1 \text{ MHz}$	-	0.5	-	pF
f <sub>T</sub>	transition frequency	$I_{C} = -1 \text{ mA}; V_{CE} = -10 \text{ V}; f = 100 \text{ MHz}$	-	325	-	MHz

BF550

# PNP medium frequency transistor

## PACKAGE OUTLINE



## PNP medium frequency transistor

BF550

## DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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# **NXP Semiconductors**

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com For sales offices addresses send e-mail to: salesaddresses@nxp.com

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