1. General description

PNP high-voltage low V_{CEsat} transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

NPN complement: PBHV8540Z

2. Features and benefits

- · High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- AEC-Q101 qualified

3. Applications

- · Electronic ballast for fluorescent lighting
- · LED driver for LED chain module
- LCD backlighting
- High Intensity Discharge (HID) front lighting
- · Automotive motor management
- Hook switch for wired telecom
- Switch mode power supply

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V	-	-	-500	V
V _{CEO}	collector-emitter voltage	open base	-	-	-400	V
I _C	collector current		-	-	-0.25	Α
h _{FE}	DC current gain	V_{CE} = -10 V; I_{C} = -50 mA; T_{amb} = 25 °C	100	200	-	



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	C
2	С	collector		в
3	Е	emitter		- Tw
4	С	collector	□ 1 □ 2 □ 3	Ė
			SC-73 (SOT223)	sym028

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBHV9040Z	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223		

7. Marking

Table 4. Marking codes

Type number	Marking code
PBHV9040Z	V9040Z

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

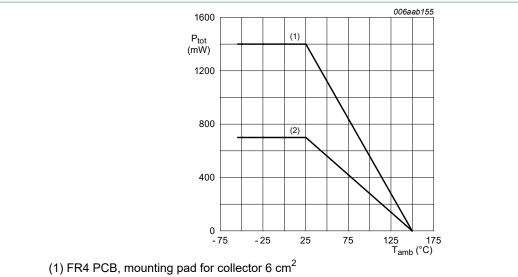
Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-500	V
V_{CEO}	collector-emitter voltage	open base		-	-400	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-500	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-0.25	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-0.5	А
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.7	W
			[2]	-	1.4	W
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

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(2) FR4 PCB, standard footprint

Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	175	K/W
			[2]	-	-	89	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

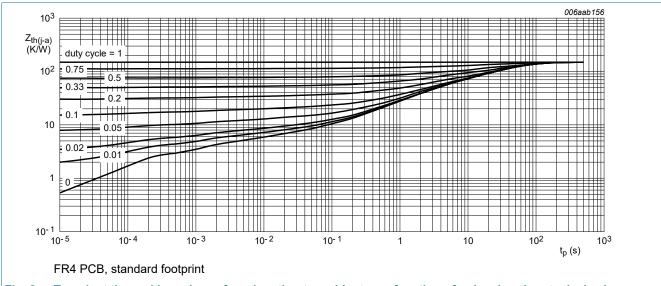
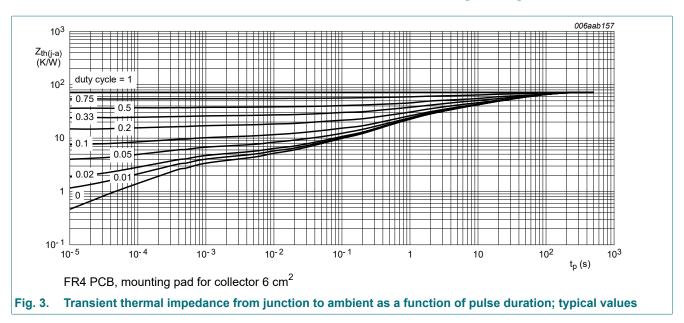


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -320 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
curre	current	V _{CB} = -320 V; I _E = 0 A; T _j = 150 °C	-	-	-10	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = -4 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = -320 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C	100	200	-	
		V_{CE} = -10 V; I_{C} = -100 mA; T_{amb} = 25 °C	80	200	-	
		V_{CE} = -10 V; I_{C} = -250 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	10	25	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = -100 mA; I_B = -20 mA; T_{amb} = 25 °C	-	-110	-200	mV
V _{BEsat}	base-emitter saturation voltage	I_C = -100 mA; I_B = -20 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-1	-1.1	V
t _d	delay time	V _{CC} = -2 V; I _C = -0.15 A; I _{Bon} = -0.03 A;	-	9	-	ns
t _r	rise time	I _{Boff} = 0.03 A; T _{amb} = 25 °C	-	1810	-	ns
t _{on}	turn-on time		-	1819	-	ns
ts	storage time		-	715	-	ns
t _f	fall time		-	1085	-	ns
t _{off}	turn-off time		-	1800	-	ns
f _T	transition frequency	V_{CE} = -10 V; I_{C} = -10 mA; f = 100 MHz; T_{amb} = 25 °C	-	55	-	MHz
C _c	collector capacitance	V_{CB} = -20 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	7	-	pF
C _e	emitter capacitance	V_{EB} = -0.5 V; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	150	-	pF

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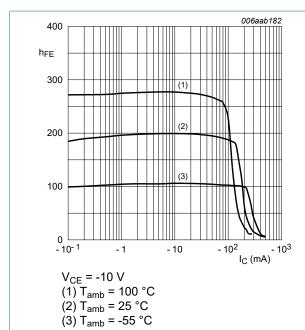


Fig. 4. DC current gain as a function of collector current; typical values

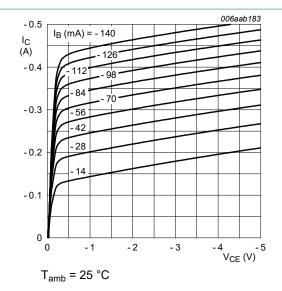
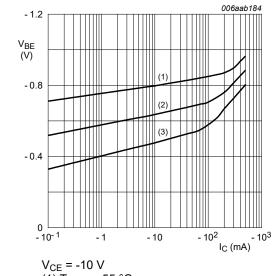


Fig. 5. Collector current as a function of collectoremitter voltage; typical values

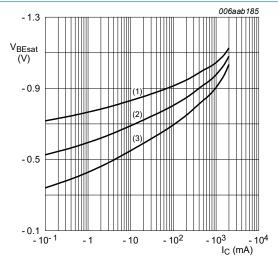


(1) $T_{amb} = -55 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) T_{amb} = 100 °C

Fig. 6. Base-emitter voltage as a function of collector current; typical values



 $I_C/I_B = 5$ (1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 100 \, ^{\circ}C$

Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

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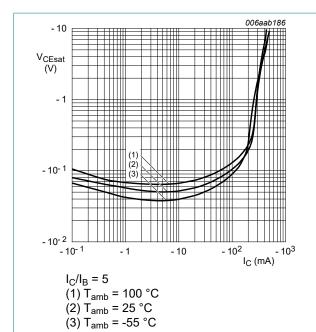


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

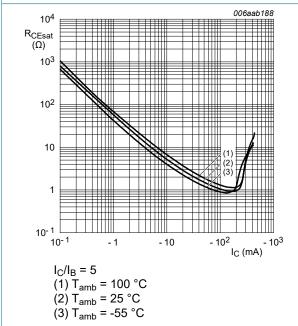


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

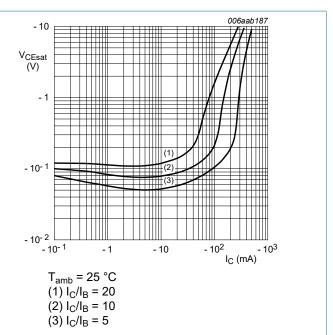


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

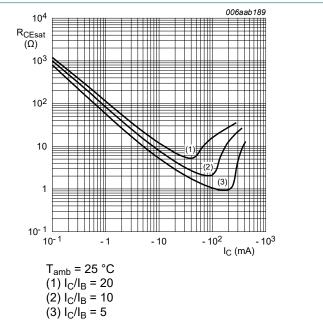
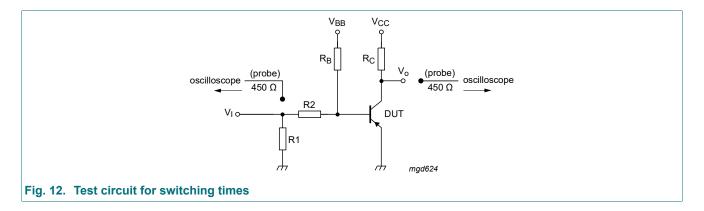


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

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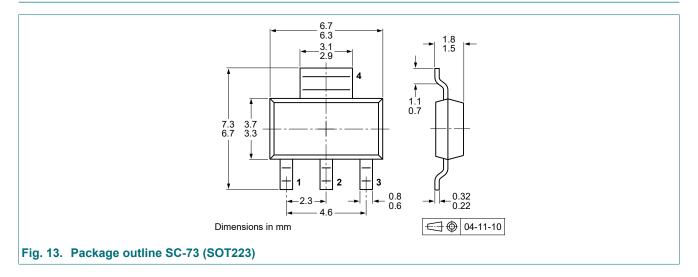
11. Test information



Quality information

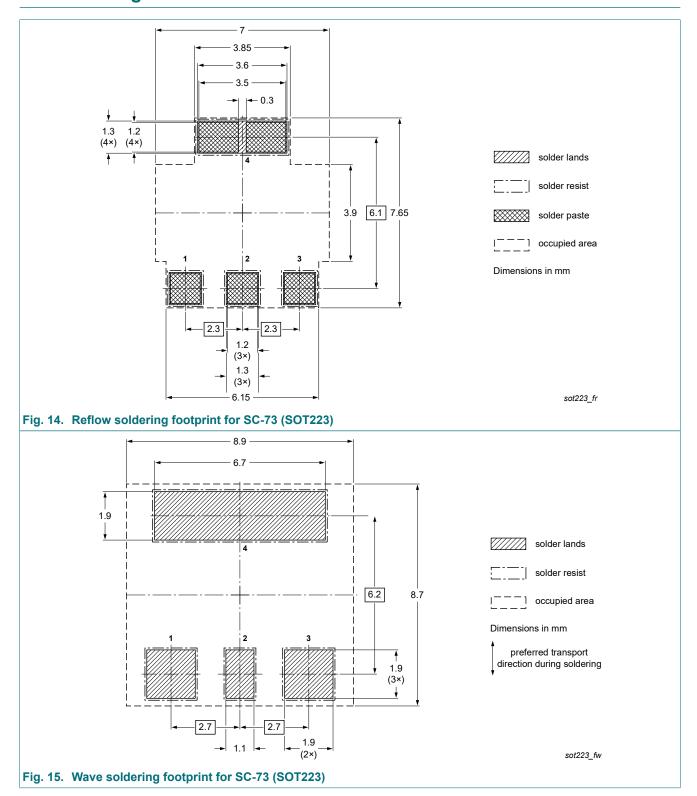
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PBHV9040Z v.3	20230717	Product data sheet	-	PBHV9040Z_2			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section "Packing information" removed. 						
PBHV9040Z_2	20090115	Product data sheet	-	PBHV9040Z_1			
PBHV9040Z_1	20080219	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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