



BUK7E2R6-60E

N-channel TrenchMOS standard level FET

11 September 2012

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel MOSFET in a SOT226 package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

1.2 Features and benefits

- AEC Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True standard level gate with VGS(th) rating of greater than 1V at 175 °C

1.3 Applications

- 12 V Automotive systems
- Electric and electro-hydraulic power steering
- Motors, lamps and solenoid control
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

1.4 Quick reference data

Table 1. Quick reference data

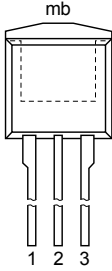
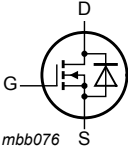
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	60	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; Fig. 1	[1]	-	120	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; Fig. 2	-	-	349	W
Static characteristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11	-	2.1	2.6	mΩ
Dynamic characteristics						
Q _{GD}	gate-drain charge	I _D = 25 A; V _{DS} = 48 V; V _{GS} = 10 V; Fig. 13 ; Fig. 14	-	45.5	-	nC

[1] Continuous current is limited by package.

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2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 I2PAK (SOT226)	 mbb076
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK7E2R6-60E	I2PAK	plastic single-ended package (I2PAK); TO-262	SOT226

4. Marking

Table 4. Marking codes

Type number	Marking code
BUK7E2R6-60E	BUK7E2R6-60E

5. Limiting values

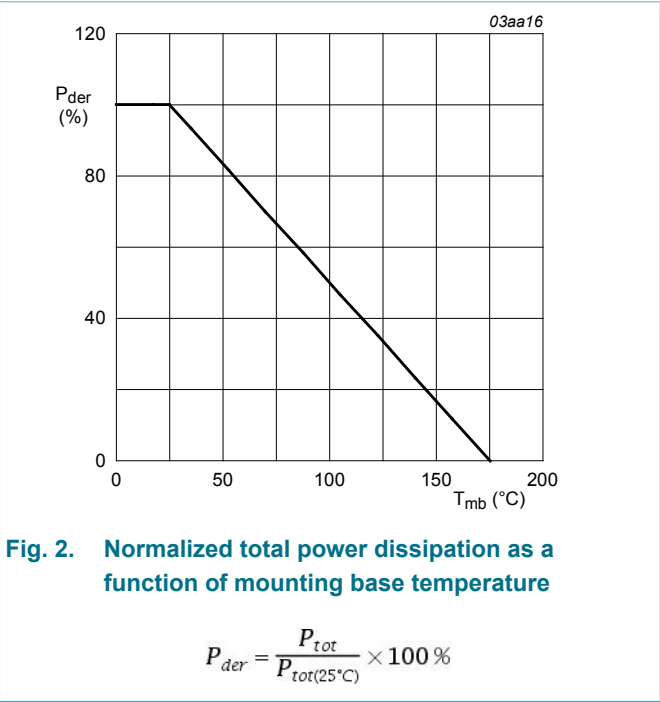
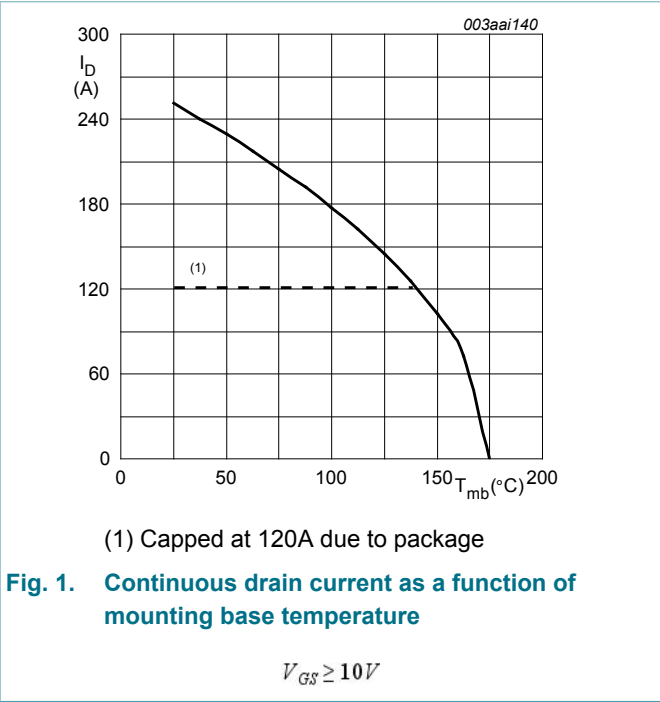
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	60	V
V _{DGR}	drain-gate voltage	R _{GS} = 20 kΩ		-	60	V
V _{GS}	gate-source voltage	T _j ≤ 175 °C; DC		-20	20	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 10 V; Fig. 1	[1]	-	120	A
		T _{mb} = 100 °C; V _{GS} = 10 V; Fig. 1	[1]	-	120	A
I _{DM}	peak drain current	T _{mb} = 25 °C; pulsed; t _p ≤ 10 μs; Fig. 4		-	995	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; Fig. 2		-	349	W
T _{stg}	storage temperature			-55	175	°C

Symbol	Parameter	Conditions		Min	Max	Unit
T _j	junction temperature			-55	175	°C
Source-drain diode						
I _S	source current	T _{mb} = 25 °C	[1]	-	120	A
I _{SM}	peak source current	pulsed; t _p ≤ 10 μs; T _{mb} = 25 °C		-	995	A
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I _D = 120 A; V _{sup} ≤ 60 V; R _{GS} = 50 Ω; V _{GS} = 60 V; T _{j(init)} = 25 °C; unclamped; Fig. 3	[2][3]	-	660	mJ

- [1] Continuous current is limited by package.
- [2] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.
- [3] Refer to application note AN10273 for further information.



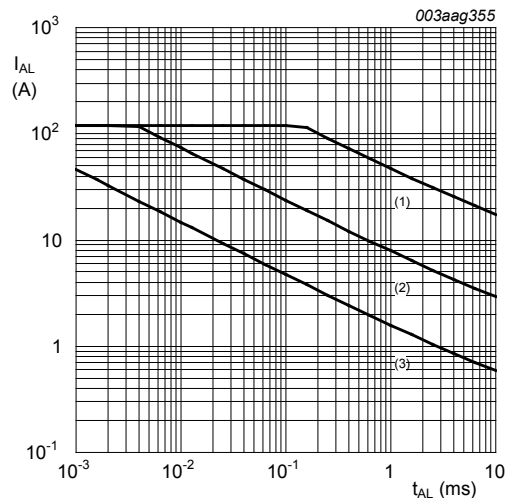


Fig. 3. Single-pulse and repetitive avalanche rating; avalanche current as a function of avalanche time

(1) $T_{j(jnt)} = 25^{\circ}\text{C}$; (2) $T_{j(jnt)} = 150^{\circ}\text{C}$; (3) Repetitive Avalanche

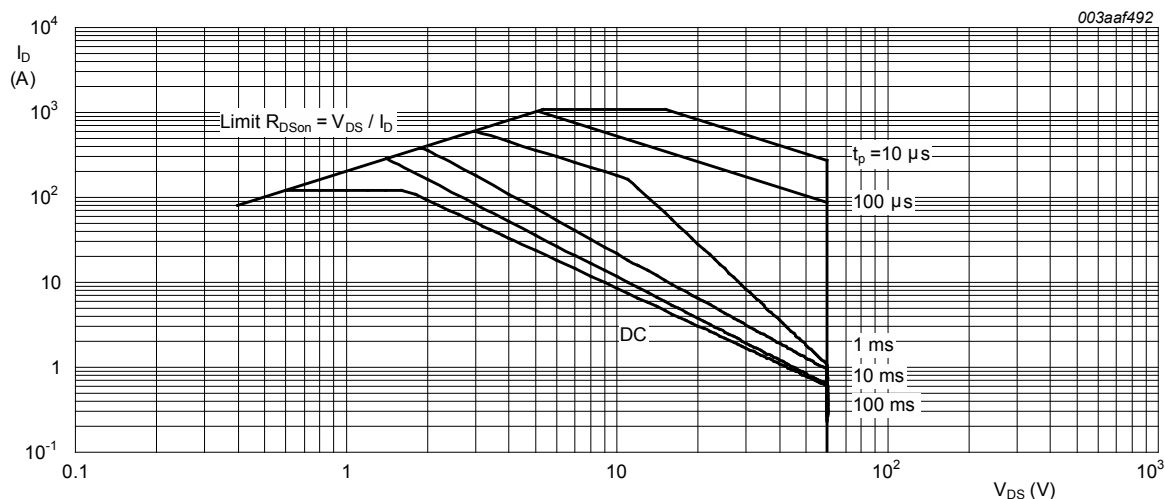


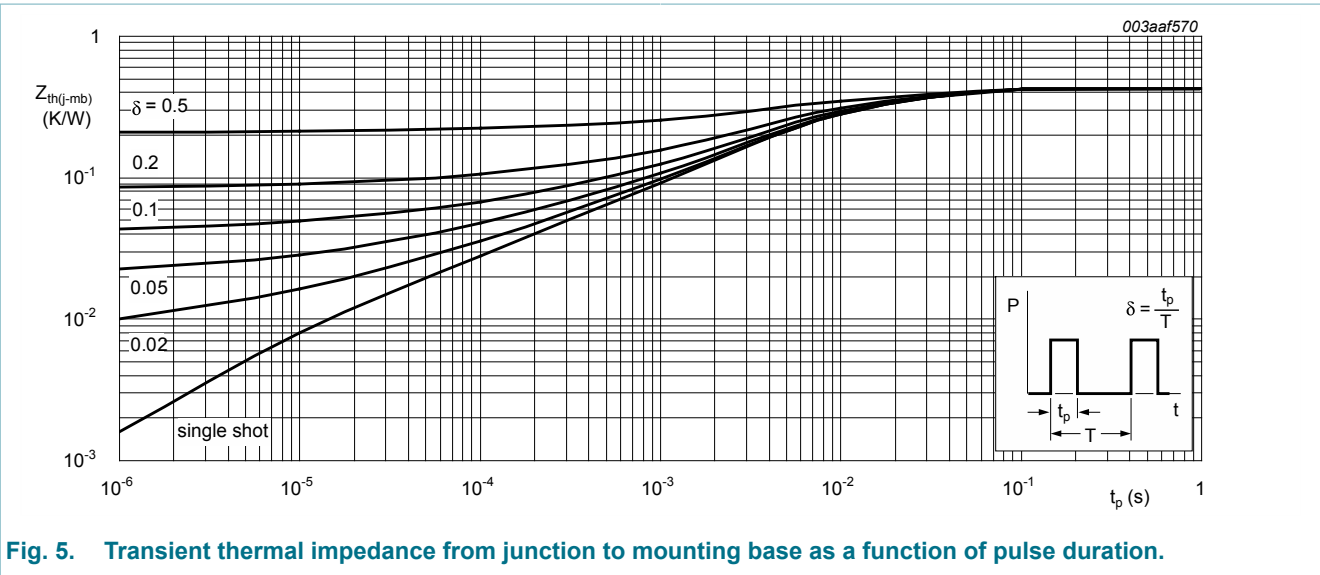
Fig. 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^{\circ}\text{C}$; I_{DM} is a single pulse

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5	-	-	0.43	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in still air	-	65	-	K/W



7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _J = 25 °C		60	-	-	V
		I _D = 250 μA; V _{GS} = 0 V; T _J = -55 °C		54	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _J = 25 °C; Fig. 9 ; Fig. 10		2.4	3	4	V
		I _D = 1 mA; V _{DS} = V _{GS} ; T _J = 175 °C; Fig. 9		1	-	-	V
		I _D = 1 mA; V _{DS} = V _{GS} ; T _J = -55 °C; Fig. 9		-	-	4.5	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _J = 25 °C		-	0.15	1	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _J = 175 °C		-	-	500	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _J = 25 °C		-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _J = 25 °C		-	2	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _J = 25 °C; Fig. 11		-	2.1	2.6	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _J = 175 °C; Fig. 11 ; Fig. 12		-	-	5.6	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 48 V; V _{GS} = 10 V; Fig. 13 ; Fig. 14		-	158	-	nC
Q _{GS}	gate-source charge			-	35.3	-	nC
Q _{GD}	gate-drain charge			-	45.5	-	nC

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz;		-	9380	11180	pF
C _{oss}	output capacitance	T _j = 25 °C; Fig. 15		-	1066	1280	pF
C _{rss}	reverse transfer capacitance			-	642	880	pF
t _{d(on)}	turn-on delay time	V _{DS} = 45 V; R _L = 1.8 Ω; V _{GS} = 10 V; R _{G(ext)} = 5 Ω		-	36	-	ns
t _r	rise time			-	50	-	ns
t _{d(off)}	turn-off delay time			-	130	-	ns
t _f	fall time			-	71	-	ns
L _D	internal drain inductance	from upper edge of mounting base to centre of die		-	2.5	-	nH
		from drain lead 6mm from package to centre of die		-	4.5	-	nH
L _S	internal source inductance	measured from source lead to source bond pad		-	7.5	-	nH
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _j = 25 °C; Fig. 16		-	0.77	1.2	V
t _{rr}	reverse recovery time	I _S = 20 A; dI _S /dt = -100 A/μs; V _{GS} = 0 V; V _{DS} = 25 V		-	54	-	ns
Q _r	recovered charge			-	89	-	nC

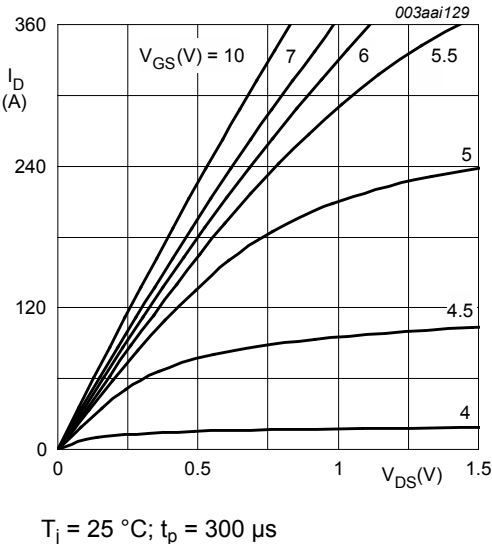


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

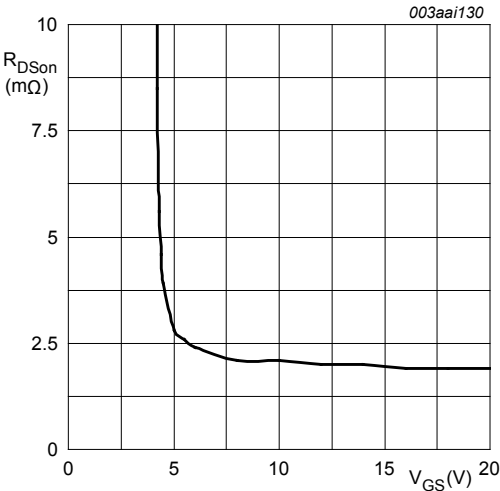


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

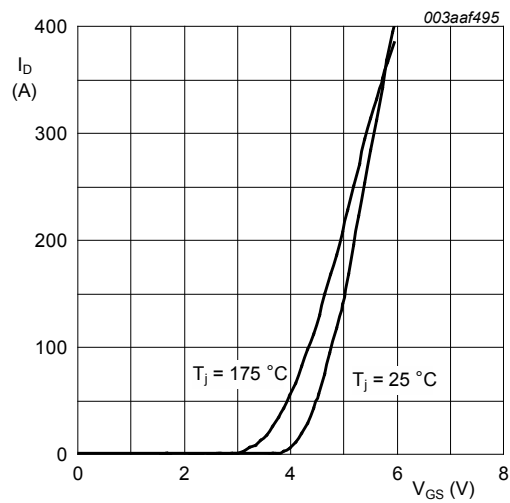


Fig. 8. Transfer characteristics: drain current as a function of gate-source voltage; typical values

$V_{DS} = 12\text{ V}$

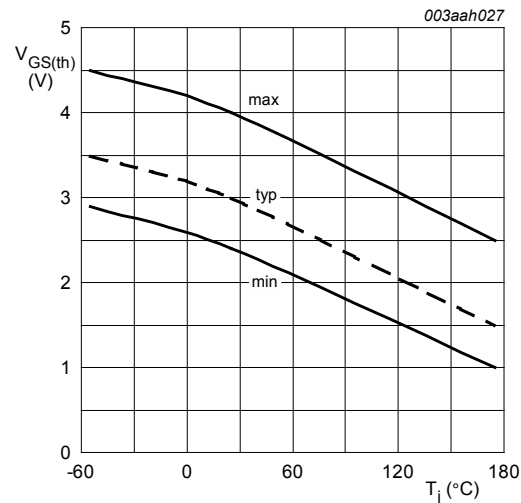


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$I_D = 1\text{ mA}; V_{DS} = V_{GS}$

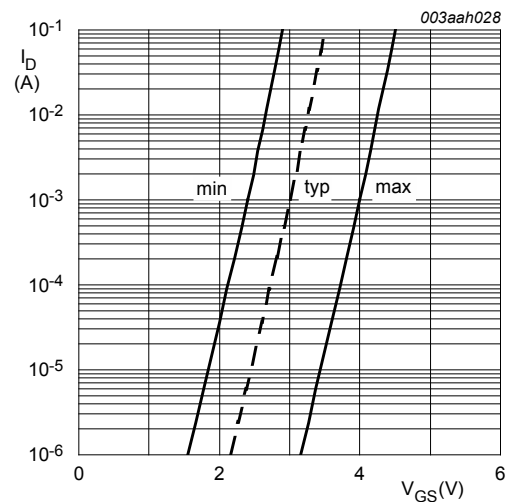


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$T_J = 25\text{ °C}; V_{DS} = 5\text{ V}$

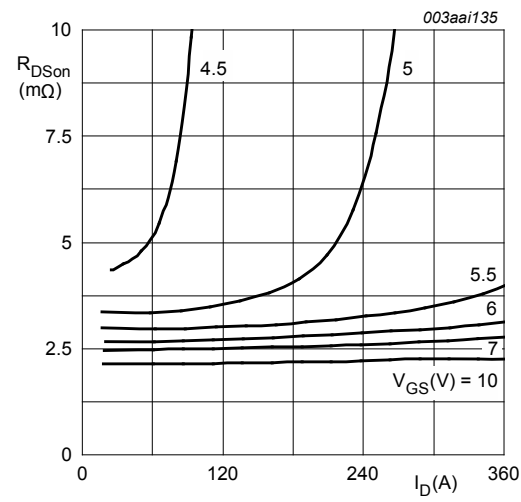


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

$T_J = 25\text{ °C}; t_p = 300\text{ }\mu\text{s}$

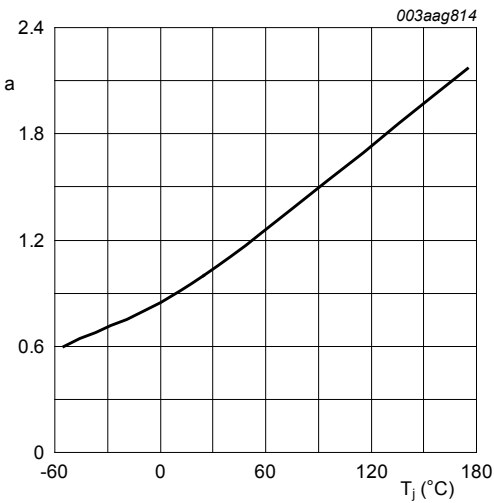


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25\text{ °C})}}$$

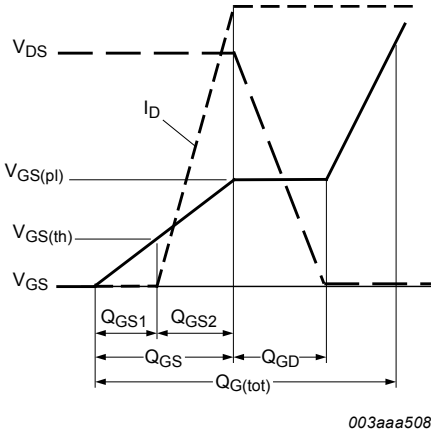


Fig. 13. Gate charge waveform definitions

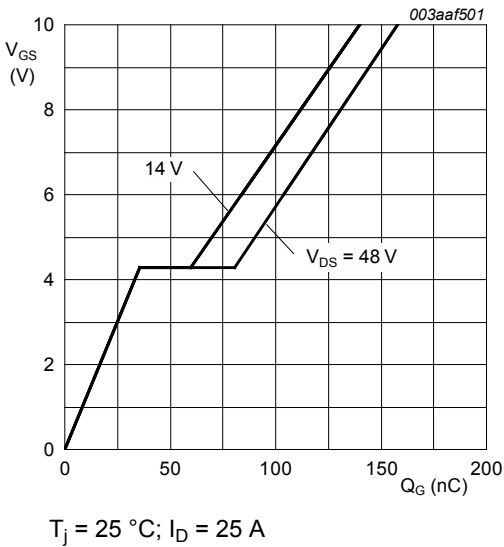


Fig. 14. Gate-source voltage as a function of gate charge; typical values

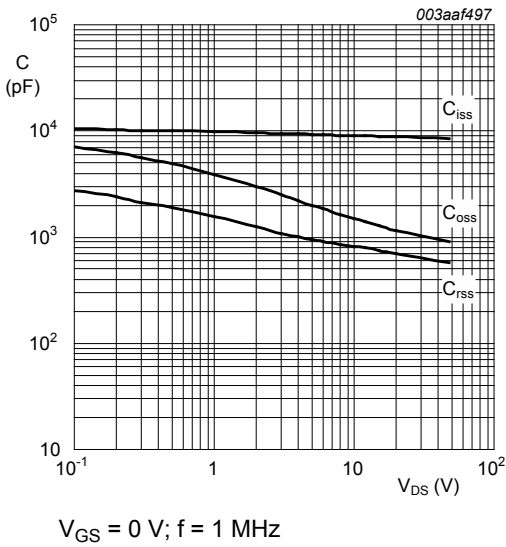
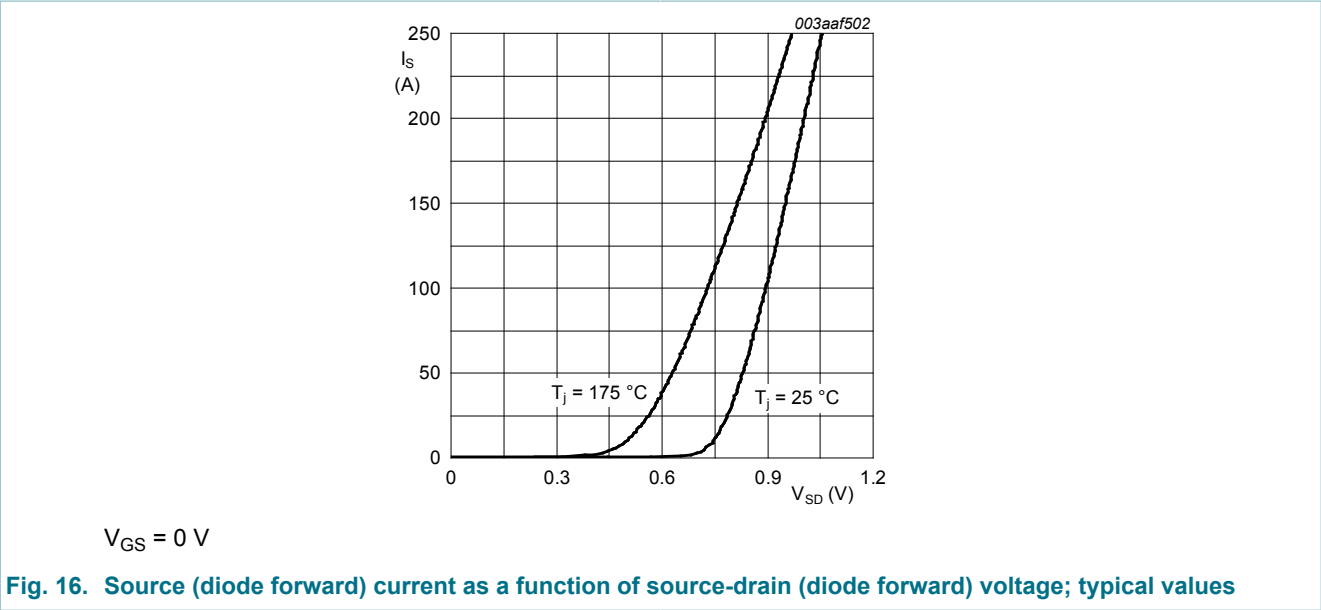


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



8. Package outline

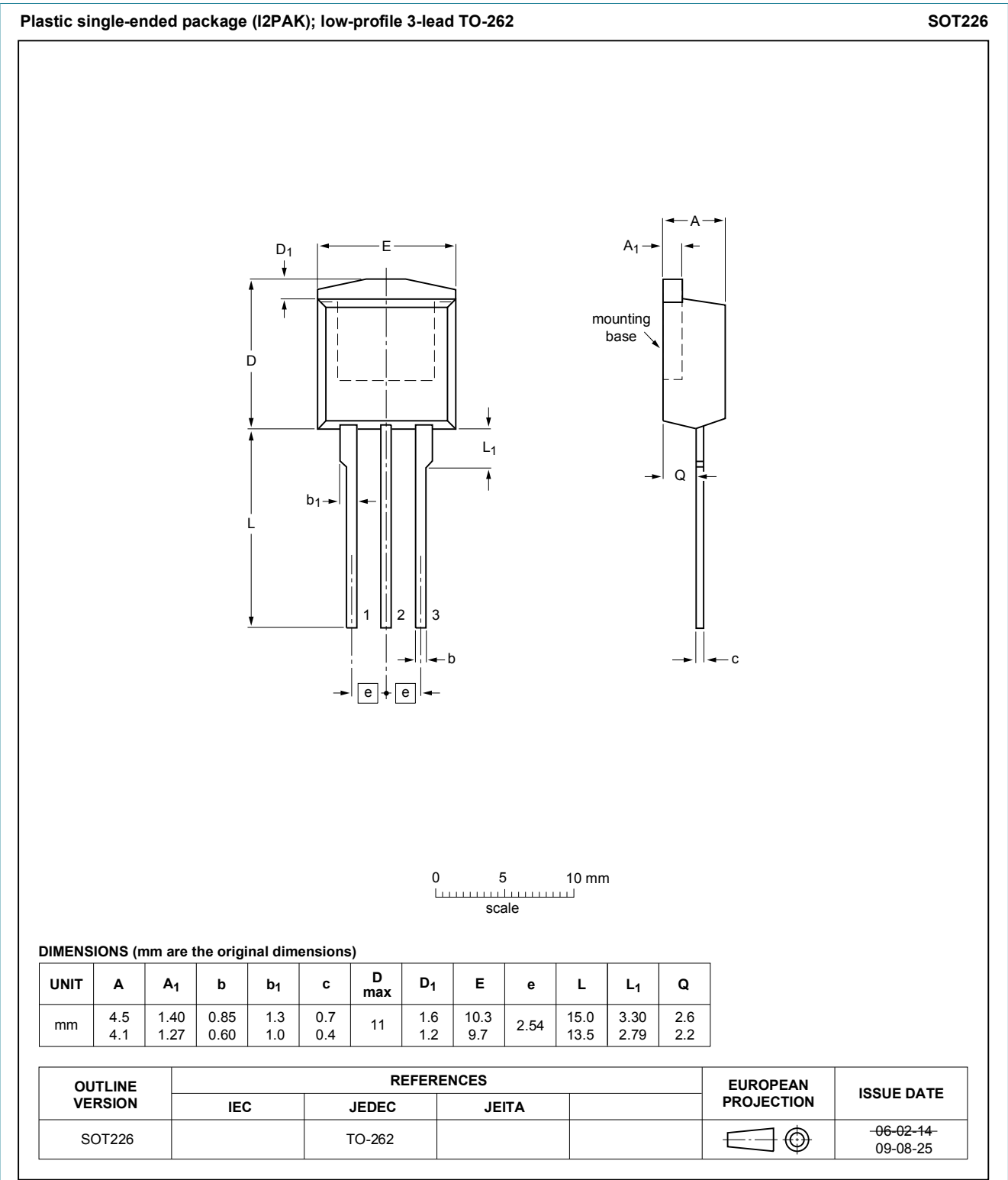


Fig. 17. Package outline I2PAK (SOT226)

9. Legal information

9.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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