

1. General description

Very low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in an SOD882 leadless ultra-small plastic package, designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- · Bidirectional ESD protection of one line
- Max. peak pulse power: P_{PPM} = 130 W
- Low clamping voltage: V_{(CL)R} = 14 V
- Ultra low leakage current: I_{RM} = 5 nA
- ESD protection up to 30 kV
- IEC 61000-4-2, level 4 (ESD)
- IEC 61000-4-5 (surge); I_{PPM} = 12 A
- Ultra small SMD plastic packages

3. Applications

- Cellular handsets and accessories
- Portable electronics
- Computers and peripherals
- Communication systems
- · Audio and video equipment

4. Quick reference data

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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	5	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	-	35	45	pF

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	1 2	к1- К1-Ю- К2
			Transparent top view	sym045
			DFN1006-2 (SOD882)	

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PESD5V0S1BL		plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOD882		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PESD5V0S1BL	Fl

8. Limiting values

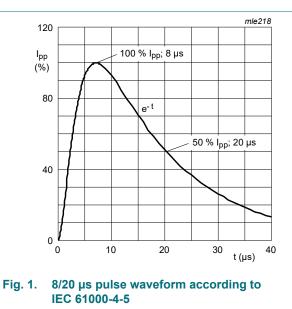
Table 5. Limiting values

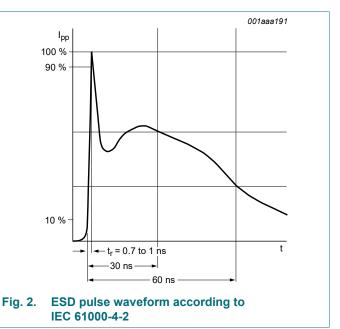
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	130	W
I _{PPM}	rated peak pulse current		[1] [2]	-	12	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum	n ratings					
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV
		MIL-STD-883; human body model (HBM)		-	10	kV

[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC 61000-4-5.

- [2] Measured from pin 1 to pin 2.
- [3] Device stressed with ten non-repetitive ElectroStatic Discharge (ESD) pulses.



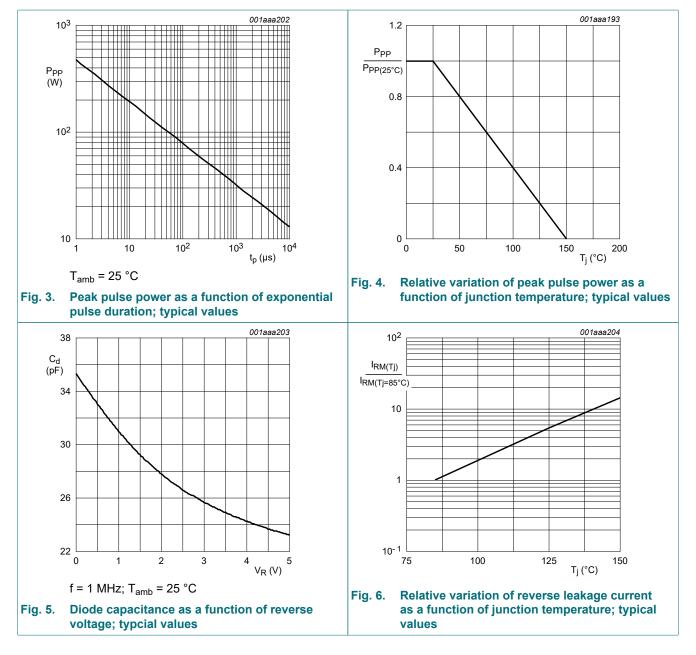


9. Characteristics

Table 6. Cha	racteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	5	V
V _{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		5.5	-	9.5	V
I _{RM}	reverse leakage current	V _{RWM} = 5 V; T _{amb} = 25 °C		-	5	100	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	35	45	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	-	10	V
		I _{PPM} = 12 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	-	14	V
R _{diff}	differential resistance	I _R = 1 mA; T _{amb} = 25 °C		-	-	50	Ω

[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC61000-4-5.

[2] Measures from pin 1 to pin 2.

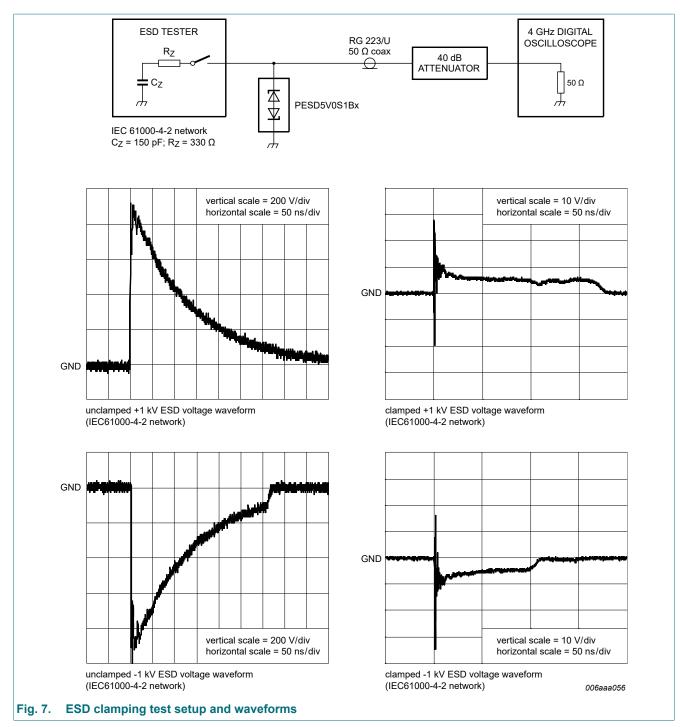


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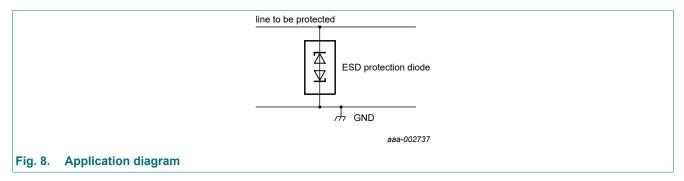
PESD5V0S1BL

Bidirectional ESD protection diode



10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

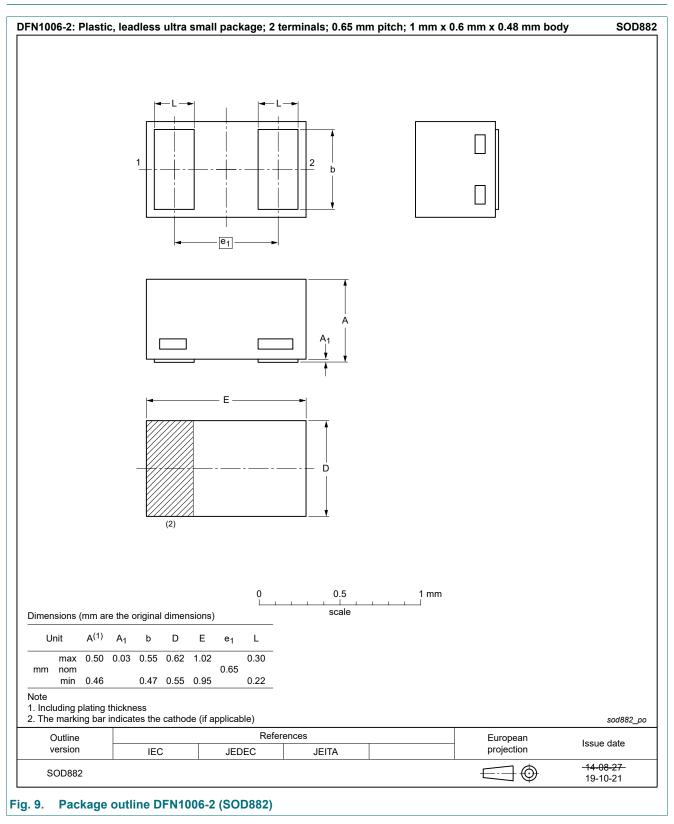


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

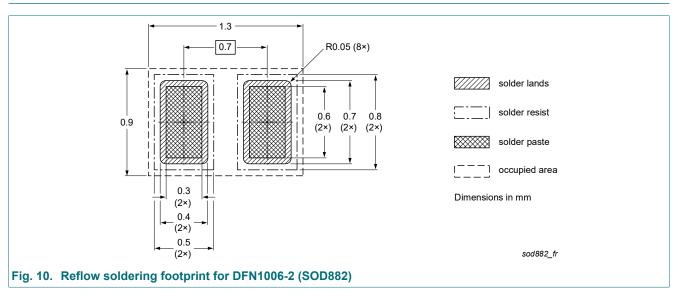
11. Package outline



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Bidirectional ESD protection diode

12. Soldering



13. Revision history

Table 7. Revision his	tory			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0S1BL v.6	20240429	Product data sheet	-	PESD5V0S1BL v.5
Modifications:		ed to non-automotive qual product alternative(s).	ification. Please refer t	o nexperia.com for
PESD5V0S1BL v.5	20180515	Product data sheet	-	PESD5V0S1BA _BB_BL_4
PESD5V0S1BA _BB_BL_4	20090820	Product data sheet	-	PESD5V0S1BA _BB_BL_3
PESD5V0S1BA _BB_BL_3	20041217	Product data sheet	-	PESD5V0S1BA _BB_BL_2
PESD5V0S1BA _BB_BL_2	20040802	Product data sheet	-	PESD5V0S1BA_1 PESD5V0S1BB_1

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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Bidirectional ESD protection diode

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