

PEMH14

50 V, 100 mA NPN/NPN resistor-equipped transistor; R1 = 47 k Ω , R2 = open

29 December 2022

Product data sheet

1. General description

NPN/NPN Resistor-Equipped Transistor (RET) in an ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package.

NPN/PNP complement: PEMD14

PNP/PNP complement: PEMB14

2. Features and benefits

- Built-in bias resistors
- Simplified circuit design
- . Reduces component count
- Reduces pick and place costs

3. Applications

- Low current peripheral driver •
- Controlling IC inputs
- Replacement of general purpose transistors in digital applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transistor	•						
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
I _O	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	33	47	61	kΩ

[1] See section "Test information" for resistor calculation and test conditions.



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND1	GND (emitter) TR1		O1 I2 GND2
2	11	input (base) TR1		
3	O2	output (collector) TR2		
4	GND2	GND (emitter) TR2		
5	12	input (base) TR2		R1
6	O1	output (collector) TR1	1 2 3 SOT666	GND1 I1 O2 sym090

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PEMH14	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PEMH14	5C

8. Limiting values

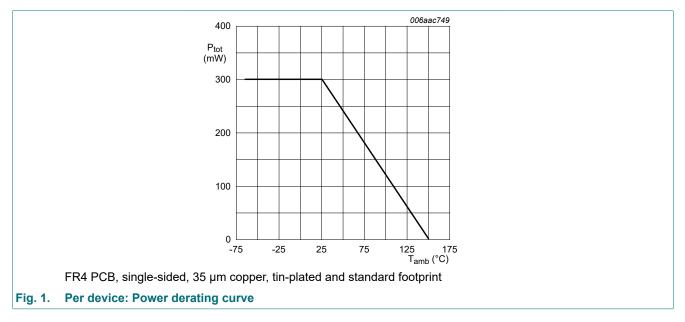
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or			-		
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _O	output current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	200	mW
Per device	L					
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.



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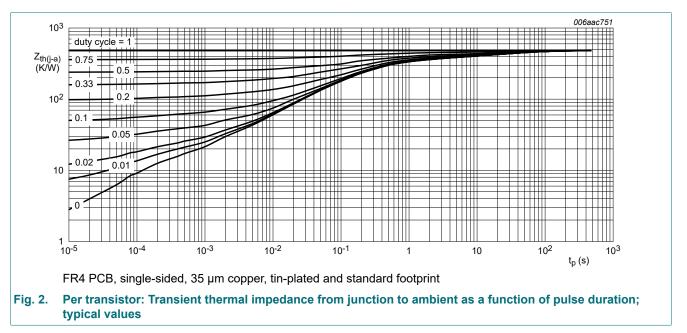
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	625	K/W
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

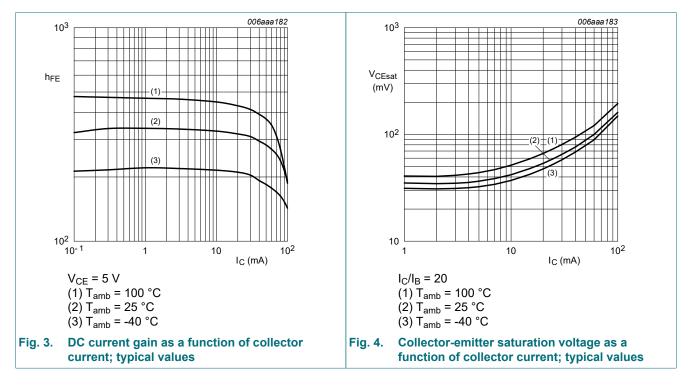
[2] Reflow soldering is the only recommended soldering method.



10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or						
V _{(BR)CBO}	collector-base breakdown voltage	I_{C} = 100 µA; I_{E} = 0 A; T_{amb} = 25 °C		50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A; T _{amb} = 25 °C		50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CEO} collector-emitter cut-off current	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	1	μA	
	V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C		-	-	50	μA	
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 1 mA; T _{amb} = 25 °C		100	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C		-	-	150	mV
R1	bias resistor 1 (input)		[1]	33	47	61	kΩ
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	-	2.5	pF

[1] See section "Test information" for resistor calculation and test conditions.

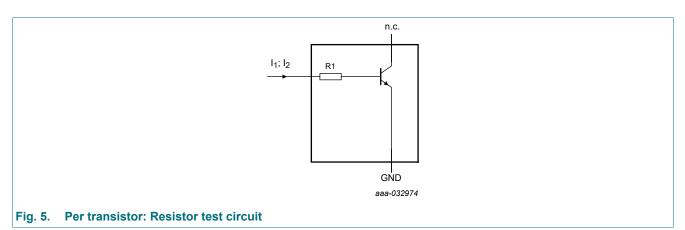


11. Test information

Resistor calculation

Calculation of bias resistor 1 (R1)

$$R_{1} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

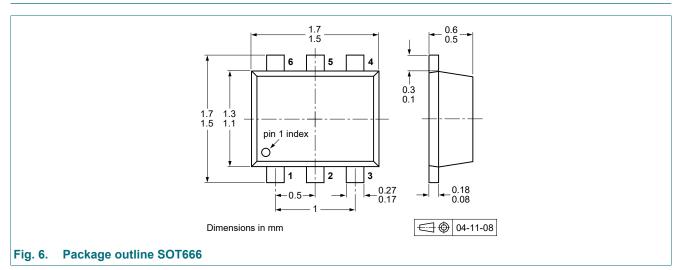


Resistor test conditions

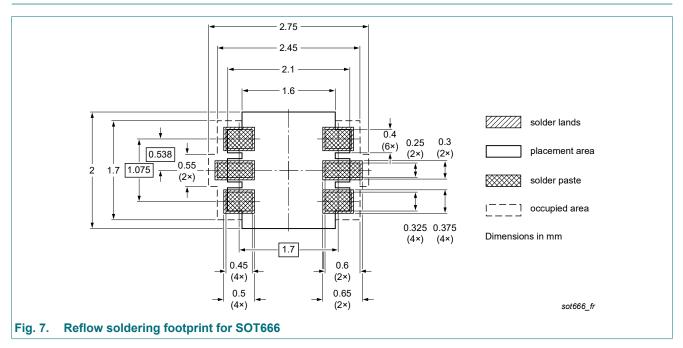
Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions	
			l ₁	l ₂
PEMH14	47	open	60 µA	110 µA

12. Package outline



13. Soldering



14. Revision history

Table 9. Revision histo	ry					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PEMH14 v.4	20221229	Product data sheet	-	PEMH14_PUMH14_3		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Family data sheet reduced to single type data sheet. Packing information removed. Product(s) changed to non-automotive qualification. 					
PEMH14_PUMH14_3	20091115	Product data sheet	-	PEMH14_PUMH14_2		
PEMH14_PUMH14_2	20050429	Product data sheet	-	PUMH14_1		
PUMH14_1	20031016	Product specification	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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Product data sheet

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