Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection encapsulated in small SOD123 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: I_F ≤ 1 A
- Reverse voltage V_R ≤ 60 V
- Low forward voltage, typ. V_F = 570 mV
- Low reverse current, typ. I_R = 11 μA
- Small SMD plastic package
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- · Low voltage rectification
- · High efficiency DC-to-DC conversion
- · Switch mode power supply
- · Reverse polarity protection
- Low power consumption applications
- Automotive applications

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|--------------------|-------------------------|---|-----|-----|-----|-----|------|
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 135 °C | | - | - | 1 | A |
| V_R | reverse voltage | T _j = 25 °C | | - | - | 60 | V |
| V _F | forward voltage | I_F = 1 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C | | - | 570 | 660 | mV |
| I_R | reverse current | V_R = 60 V; pulsed; T_j = 25 °C | [1] | - | 11 | 50 | μΑ |

[1] Very short test pulse to prevent junction self-heating.



5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--------------------|-------------------|
| 1 | K | cathode[1] | 1 2 | K - K- -A |
| 2 | A | anode | SOD123 | sym001 |

^[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

| Type number | Package | | | | | | |
|----------------|---------|---|---------|--|--|--|--|
| | Name | Description | Version | | | | |
| PMEG6010CEGW-Q | SOD123 | plastic, surface-mounted package; 2 leads; 2.675 mm x 1.6 mm x 1.15 mm body | SOD123 | | | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|----------------|--------------|
| PMEG6010CEGW-Q | G7 |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|--------------------|-------------------------------------|---|-----|-----|-----|------|
| V _R | reverse voltage | T _j = 25 °C | | - | 60 | V |
| I _F | forward current | T _{sp} ≤ 55 °C | | - | 1 | А |
| I _{F(AV)} | average forward current | δ = 0.5; f = 20 kHz; square wave; $T_{amb} \le$ 70 °C | [1] | - | 1 | A |
| | | δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 135 °C | | - | 1 | А |
| I _{FRM} | repetitive peak forward current | $t_p \le 1 \text{ ms}; \delta \le 0.25$ | | - | 7 | A |
| I _{FSM} | non-repetitive peak forward current | t_p = 8 ms; square wave; $T_{j(init)}$ = 25 °C | | - | 9 | A |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [2] | - | 410 | mW |
| | | | [1] | - | 675 | mW |
| T _j | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

PMEG6010CEGW-Q

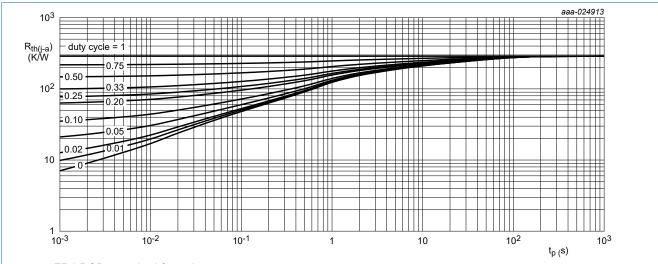
^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

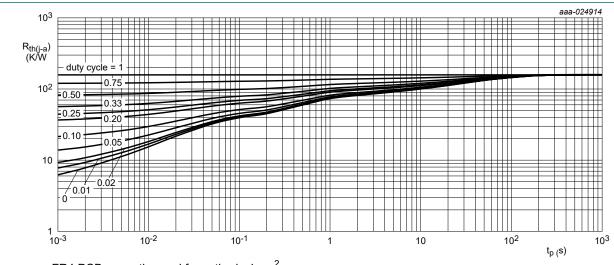
| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|---------------------|--|-------------|---------|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance from | in free air | [1] [2] | - | - | 305 | K/W |
| junction to ambient | | [1] [3] | - | - | 185 | K/W | |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | [4] | - | - | 21 | K/W |

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|----------------|---------------------------|---|-----|-----|-----|-----|------|
| $V_{(BR)R}$ | reverse breakdown voltage | I_R = 1 mA; $t_p \le 300$ μs; $\delta \le 0.02$; T_j = 25 °C | | 60 | - | - | V |
| V _F | forward voltage | I_F = 1 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C | | - | 210 | 250 | mV |
| | | I_F = 10 mA; $t_p \le 300 \mu s$; δ ≤ 0.02; T_j = 25 °C | | - | 270 | 310 | mV |
| | | I_F = 100 mA; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C | | - | 350 | 400 | mV |
| | | I_F = 500 mA; $t_p \le 300 \mu s$; δ ≤ 0.02; T_j = 25 °C | | - | 460 | 530 | mV |
| | | I_F = 700 mA; $t_p \le 300 \mu s$; δ ≤ 0.02; T_j = 25 °C | | - | 510 | 580 | mV |
| | | I_F = 1 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C | | - | 570 | 660 | mV |
| I _R | reverse current | $V_R = 5 \text{ V}$; pulsed; $T_j = 25 \text{ °C}$ | [1] | - | 0.8 | - | μΑ |
| | | V_R = 10 V; pulsed; T_j = 25 °C | [1] | - | 1.1 | - | μΑ |
| | | V_R = 60 V; pulsed; T_j = 25 °C | [1] | - | 11 | 50 | μΑ |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | | - | 60 | 68 | pF |

[1] Very short test pulse to prevent junction self-heating.

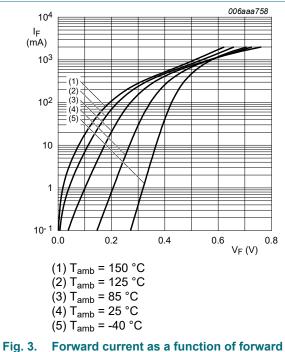


Fig. 3. Forward current as a function of forward voltage; typical values

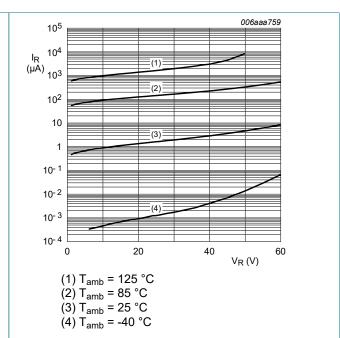


Fig. 4. Reverse current as a function of reverse voltage; typical values

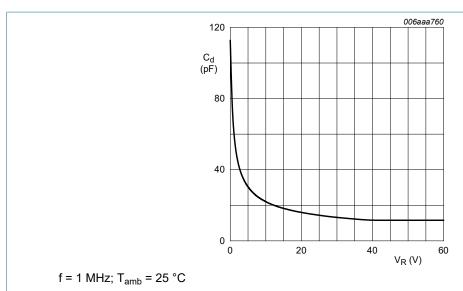
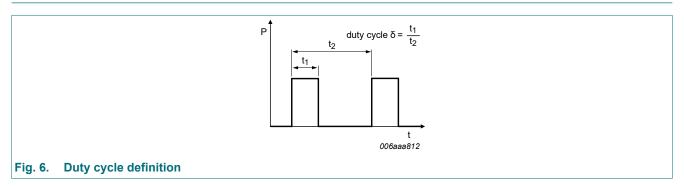


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

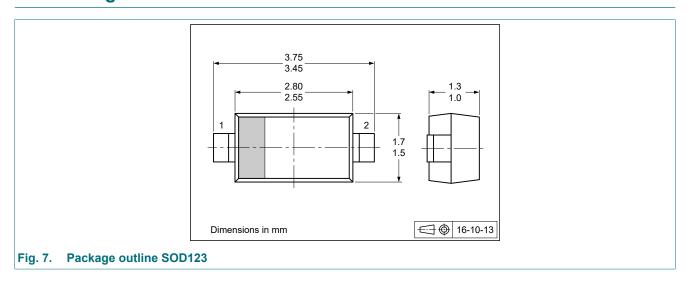
 $I_{RMS}=I_{F(AV)}$ at DC

 $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering

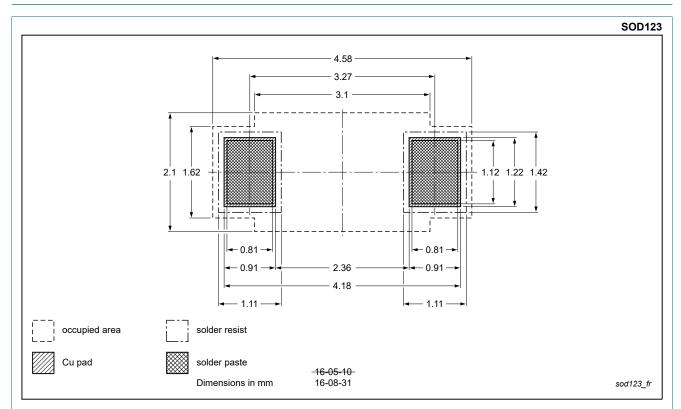
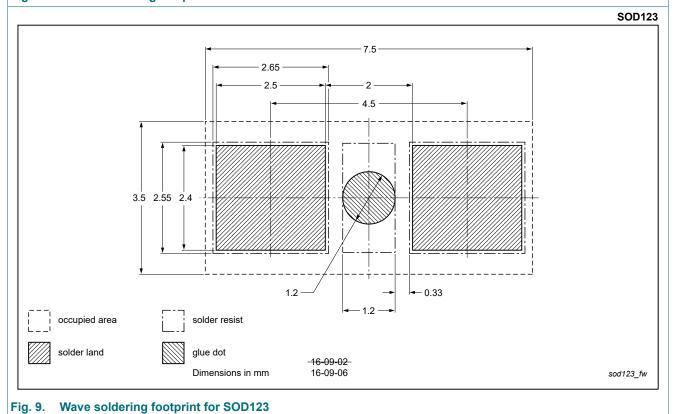


Fig. 8. Reflow soldering footprint for SOD123



14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes | | | |
|--------------------|--------------|--------------------|---------------|------------|--|--|--|
| PMEG6010CEGW-Q v.1 | 20221013 | Product data sheet | - | - | | | |

15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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