

# PDTC143ZU-Q

50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k $\Omega$ , R2 = 47 k $\Omega$ 

**22 February 2023** 

**Product data sheet** 

## 1. General description

NPN Resistor-Equipped Transistor (RET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- · Simplifies circuit design
- · Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- · Digital application in automotive and industrial segments
- · Cost-saving alternative for BC847-Q series in digital applications
- · Controlling IC inputs
- · Switching loads

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	8	10	12	

[1] See "Section 11: Test information" for resistor calculation and test conditions.



## 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	1	input (base)	3	
2	GND	ground (emitter)		R <sub>1</sub>
3	0	output (collector)	3C-70 (SOT323)	GND R2

## 6. Ordering information

### **Table 3. Ordering information**

Type number Package					
	Name	Description	Version		
PDTC143ZU-Q	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323		

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PDTC143ZU-Q	%54

[1] % = placeholder for manufacturing site code

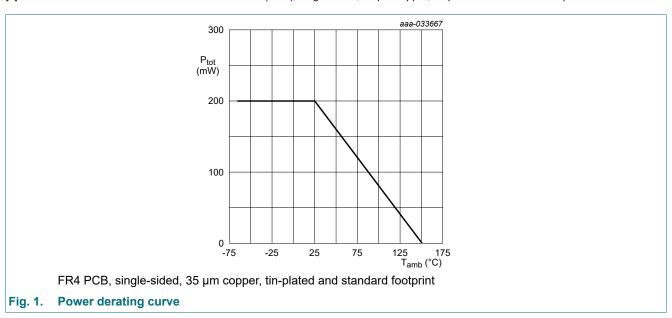
## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	50	V
$V_{CEO}$	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
VI	input voltage			-5	30	V
Io	output current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.

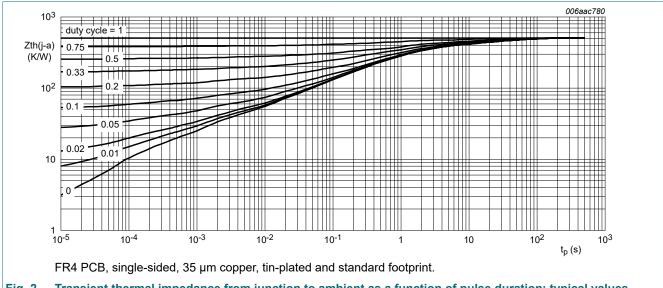


## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.



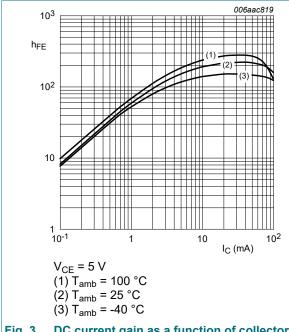
Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

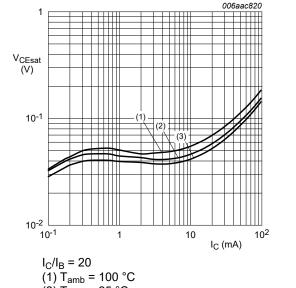
**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		50	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
I <sub>CEO</sub>	collector-emitter cut-off	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	170	μA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C		100	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.25 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	100	mV
$V_{I(off)}$	off-state input voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 μA; T <sub>amb</sub> = 25 °C		-	0.6	0.5	V
$V_{I(on)}$	on-state input voltage	$V_{CE}$ = 0.3 V; $I_{C}$ = 5 mA; $T_{amb}$ = 25 °C		1.3	0.9	-	V
R1	bias resistor 1 (input)		[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		[1]	8	10	12	
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	-	2.5	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 5 V; $I_{C}$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	[2]	-	230	-	MHz

- [1] See "Section 11: Test information" for resistor calculation and test conditions.
- [2] Characteristics of built-in transistor.





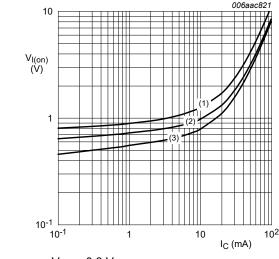


 $(2) T_{amb} = 25 °C$ 

(3)  $T_{amb} = -40 \, ^{\circ}C$ 

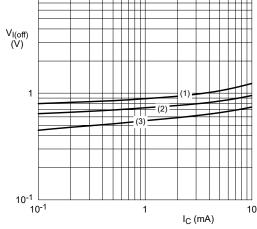
Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values

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 $V_{CE} = 0.3 V$ 

(1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = 100 °C



V<sub>CE</sub> = 5 V (1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C

(3)  $T_{amb} = 100 \, ^{\circ}C$ 

Fig. 5. On-state input voltage as a function of collector | Fig. 6. current; typical values



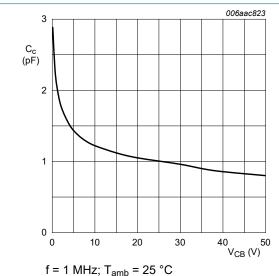
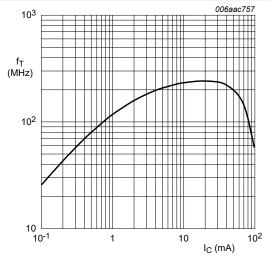


Fig. 7. Collector capacitance as a function of collector- Fig. 8. base voltage; typical values



 $V_{CE}$  = 5 V;  $T_{amb}$  = 25 °C

Transition frequency as a function of collector current; typical values of built-in transistor

## 11. Test information

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

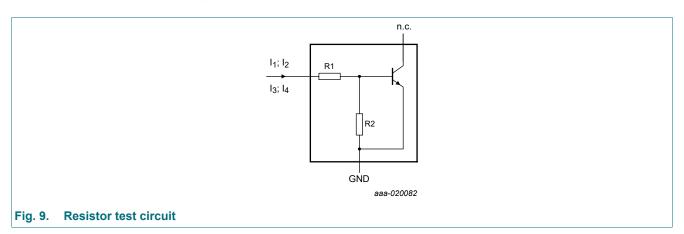
#### **Resistor calculation**

Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$



### **Resistor test conditions**

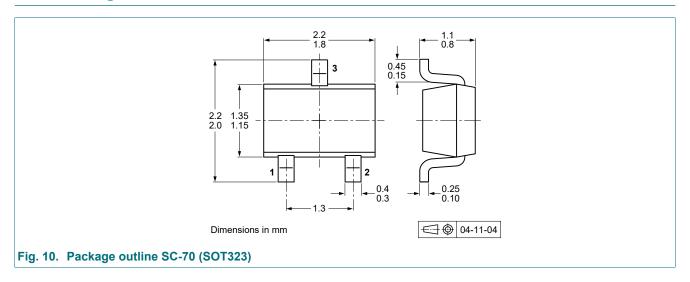
**Table 8. Resistor test conditions** 

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I <sub>1</sub>	l <sub>2</sub>	l <sub>3</sub>	I <sub>4</sub>
PDTC143ZU-Q	4.7	47	90 μΑ	140 µA	-55 μΑ	-105 µA

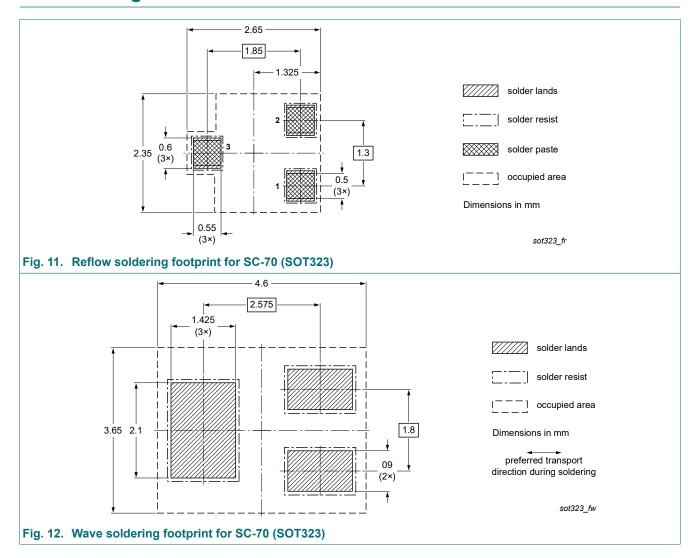
Nexperia PDTC143ZU-Q

50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k $\Omega$ , R2 = 47 k $\Omega$ 

## 12. Package outline



## 13. Soldering



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50 V, 100 mA NPN resistor-equipped transistor; R1 = 4.7 k $\Omega$ , R2 = 47 k $\Omega$ 

## 14. Revision history

#### Table 9. Revision history

Table 5. Revision matery							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PDTC143ZU-Q v.2	20230222	Product data sheet	-	PDTC143ZU-Q v.1			
Modification:	• Section 4 "Characteristics" I <sub>CEO</sub> value updated for T <sub>amb</sub> = 25°C from 1 μA to 100 n.						
PDTC143ZU-Q v.1	20220516	Product data sheet	-	-			

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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