

**45 V, 500 mA NPN general-purpose transistors** Rev. 2 — 28 October 2020

**Product data sheet** 

### 1. General description

NPN general-purpose transistor in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table	<b>1.</b> I	Prod	uct	over	vi	ew	
_		-			_		

Type number	Package			PNP complement
	Name	JEDEC	Version	
BC817-16QC	DFN1412D-3	MO-340CA	SOT8009	BC807-16QC
BC817-25QC				BC807-25QC
BC817-40QC				BC807-40QC

### 2. Features and benefits

- High power dissipation capability •
- High current
- Three current gain selections
- · Suitable for Automatic Optical Inspection (AOI) of solder joint
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm
- AEC-Q101 qualified

### 3. Applications

- General-purpose switching and amplification
- Space restricted applications

### 4. Quick reference data

Table 2. Qu	ick reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base; T <sub>amb</sub> = 25 °C		-	-	45	V
I <sub>C</sub>	collector current	T <sub>amb</sub> = 25 °C		-	-	500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms; T <sub>amb</sub> = 25 °C		-	-	1	A
h <sub>FE</sub>	DC current gain					·	
	BC817-16QC	$V_{CE}$ = 1 V; I <sub>C</sub> = 100 mA T <sub>amb</sub> = 25 °C	[1]	100	-	250	
	BC817-25QC		[1]	160	-	400	
	BC817-40QC		[1]	250	-	600	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# nexperia

### 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	E	emitter		
3	C	collector	3	B
			Bottom view	
			DFN1412D-3 (SOT8009)	

### 6. Ordering information

#### Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
BC817-16QC	DFN1412D-3	DFN1412D-3: plastic thermal enhanced ultra thin small outline	SOT8009			
BC817-25QC		package; no leads; 3 terminals; body: 1.4 x 1.2 x 0.5 mm	(MO-340CA)			
BC817-40QC						

### 7. Marking

Table 5. Marking	
Type number	Marking code
BC817-16QC	9M
BC817-25QC	9N
BC817-40QC	9P

### 8. Limiting values

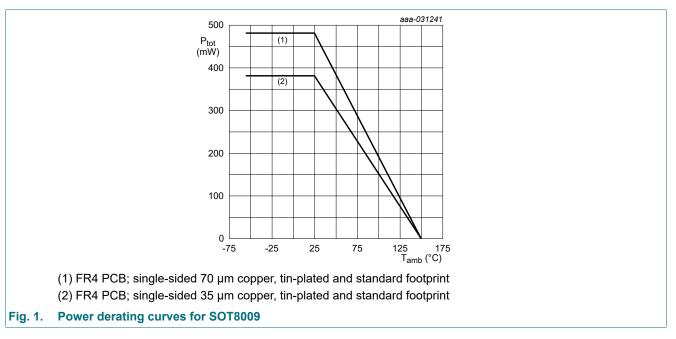
#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Conditions			Unit
V <sub>CBO</sub>	collector-base voltage	open emitter; T <sub>amb</sub> = 25 °C		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base; T <sub>amb</sub> = 25 °C		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector; T <sub>amb</sub> = 25 °C		-	5	V
I <sub>C</sub>	collector current	T <sub>amb</sub> = 25 °C	T <sub>amb</sub> = 25 °C		500	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1$ ms; $T_{amb} = 2$	25 °C	-	1	А
I <sub>BM</sub>	peak base current	single pulse; $t_p \le 1$ ms; $T_{amb} = 2$	25 °C	-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	380	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided 35  $\mu$ m copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided 70 µm copper, tin-plated and standard footprint.



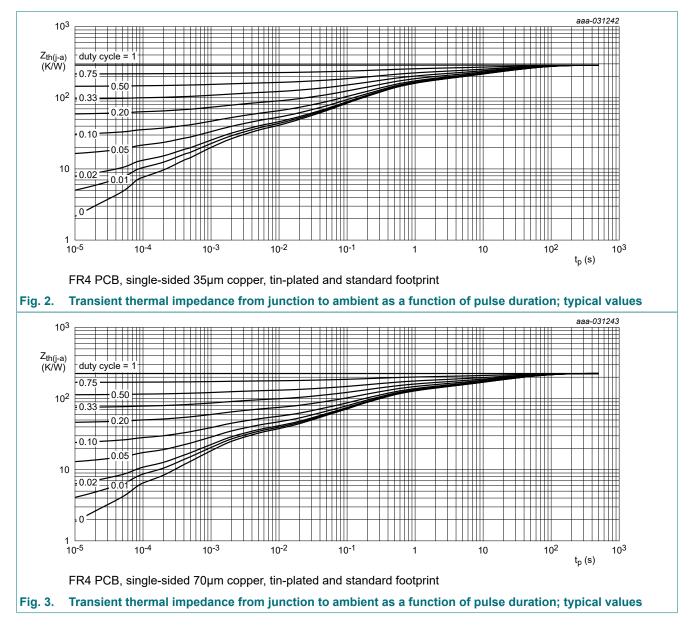
### 9. Thermal characteristics

#### **Table 7. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air;	[1]	-	-	329	K/W
		T <sub>amb</sub> = 25 °C	[2]	-	-	261	K/W

[1] Device mounted on an FR4 PCB, single-sided 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided 70 µm copper, tin-plated and standard footprint.

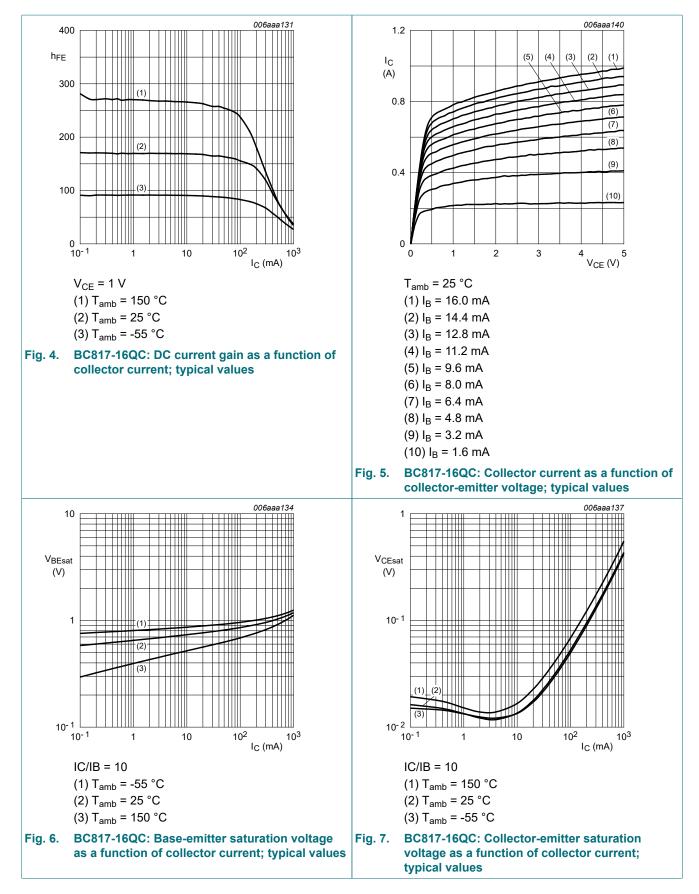


### **10. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		50	-		V	
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 10 mA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		45	-		V	
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		5	-		V	
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 20 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA	
	cut-off current	V <sub>CB</sub> = 20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA	
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA	
h <sub>FE</sub>	DC current gain							
	BC817-16QC	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	[1]	100	-	250		
	BC817-25QC	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	[1]	160	-	400		
	BC817-40QC	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	[1]	250	-	600		
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C		40	-	-		
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; T <sub>amb</sub> = 25 °C	[1]	-	-	700	mV	
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	-	-	1.2	V	
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		100	-	-	MHz	
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	3	-	pF	

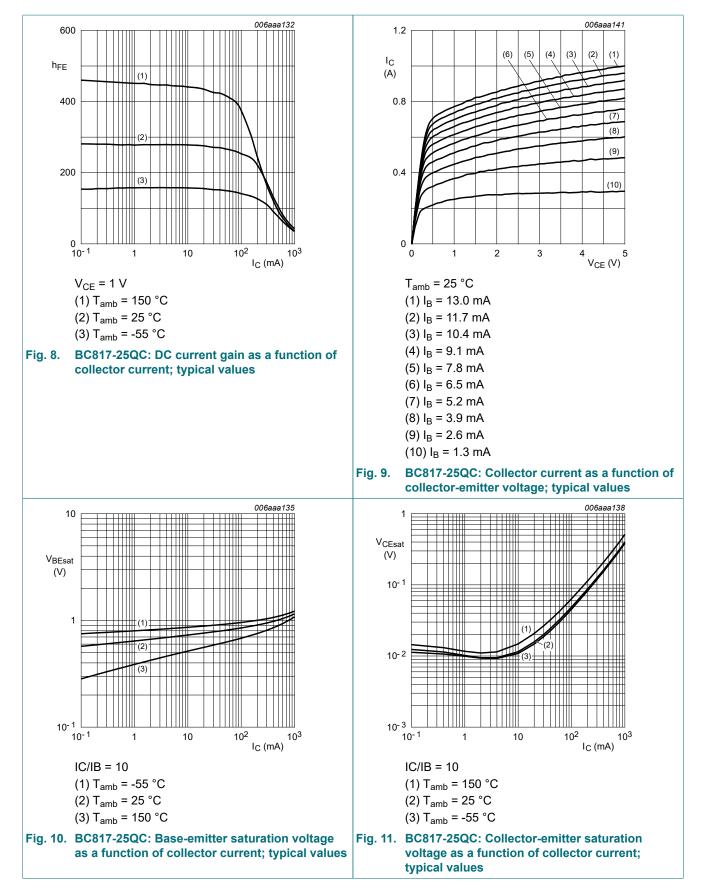
[1] pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

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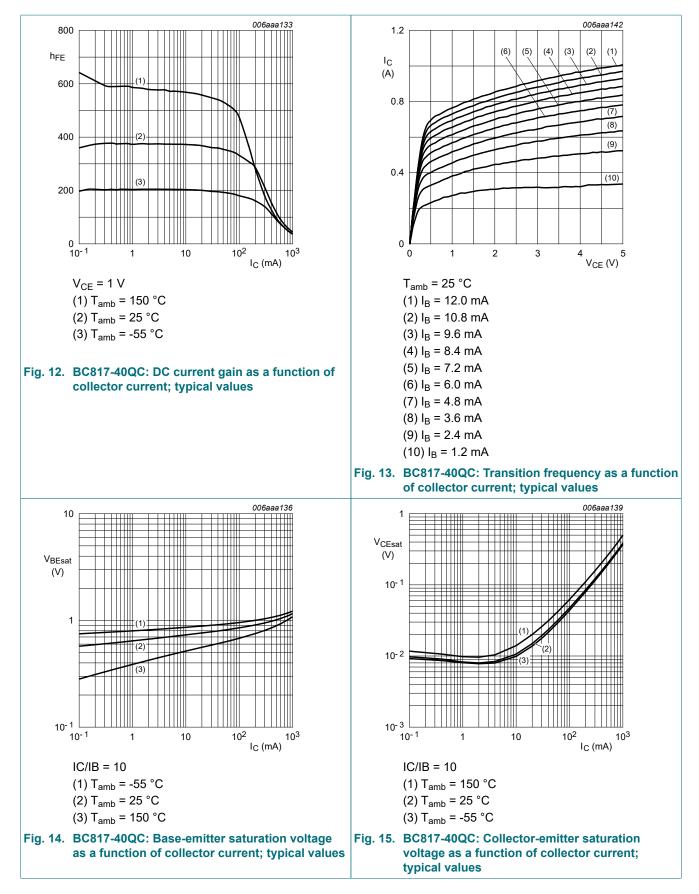


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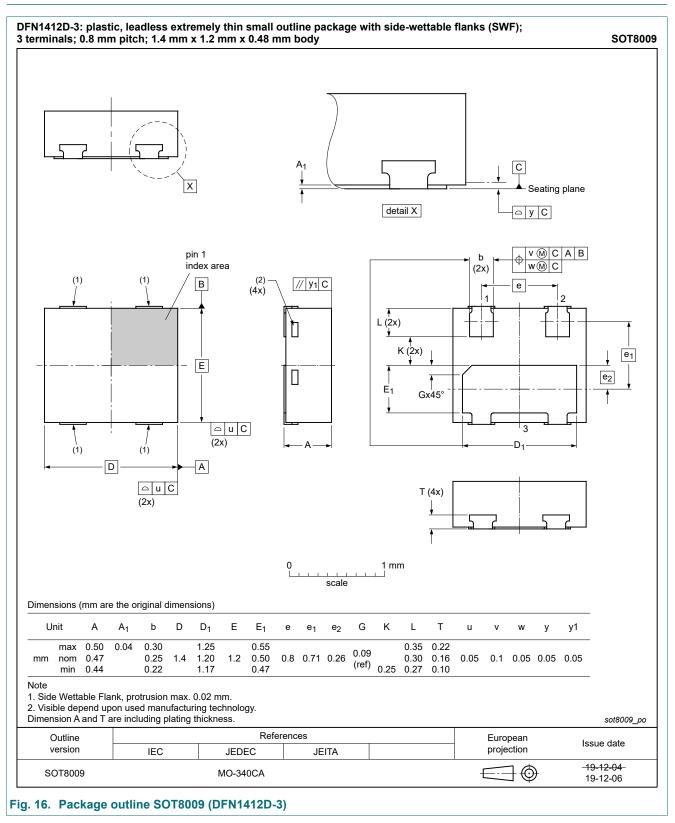


### **11. Test information**

#### 11.1. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

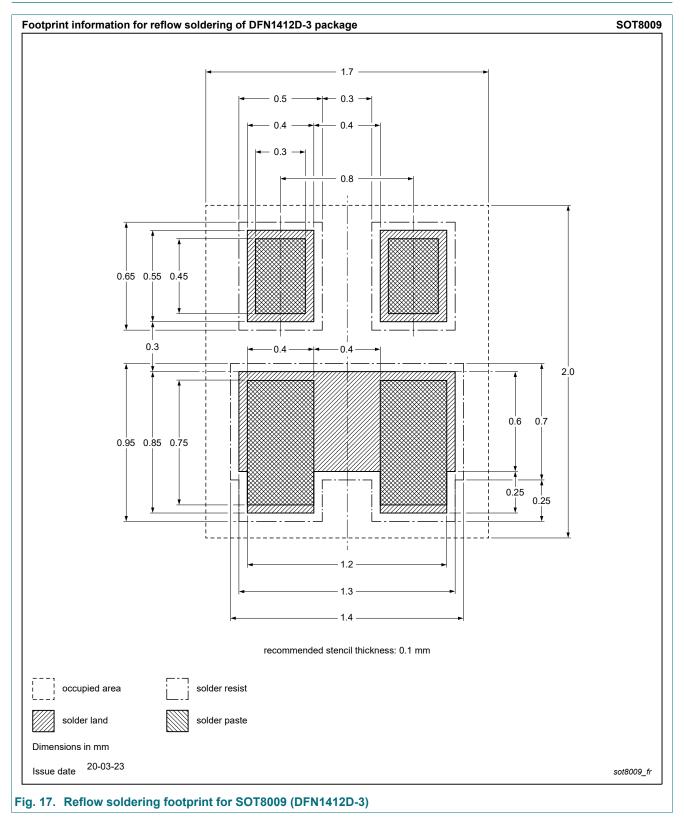
### 12. Package outline



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### 13. Soldering



### 14. Revision history

Table 9. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC817QC_SER v.2	20201028	Product data sheet	-	BC817QC_SER v.1			
Modifications:	Thermal characterist	Thermal characteristics: R <sub>th(j-sp)</sub> removed					
BC817QC_SER v.1	20200512	Product data sheet	-	-			

BC817QC\_SER

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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