

**Product data sheet** 

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006-3 (SOT883) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM
- Leadless ultra small and ultra thin SMD plastic package

### 3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	50	V
V <sub>GS</sub>	gate-source voltage	_		-8	-	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-	350	mA
Static chara	octeristics						
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 25 °C		-	2	2.8	Ω

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

# nexperia

# 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	G	gate	1	D			
2	S	source	2				
3	D	drain	Transparent top view DFN1006-3 (SOT883)	G G S 017aaa255			

# 6. Ordering information

### Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
NX5008NBKM	DFN1006-3	plastic, leadless ultra small package; 3 terminals; 0.35 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOT883				

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
NX5008NBKM	Y3

### 8. Limiting values

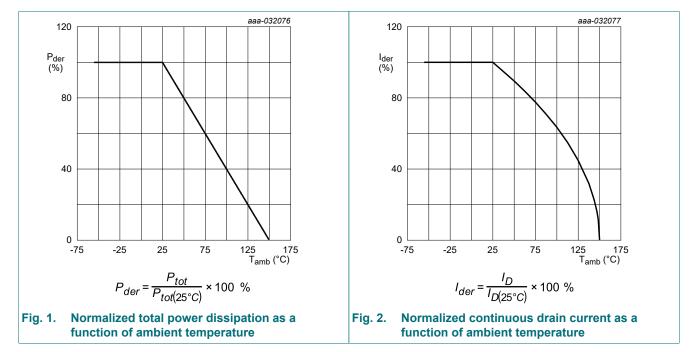
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

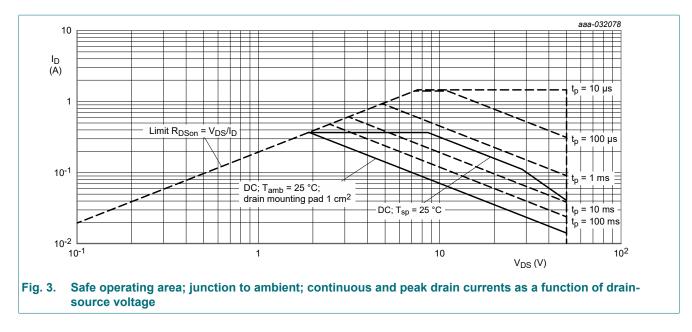
Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	50	V
V <sub>GS</sub>	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	350	mA
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	220	mA
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	1.45	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	350	mW
			[1]	-	700	mW
		T <sub>sp</sub> = 25 °C		-	2.8	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode					
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	350	mA

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



#### 50 V, N-channel Trench MOSFET

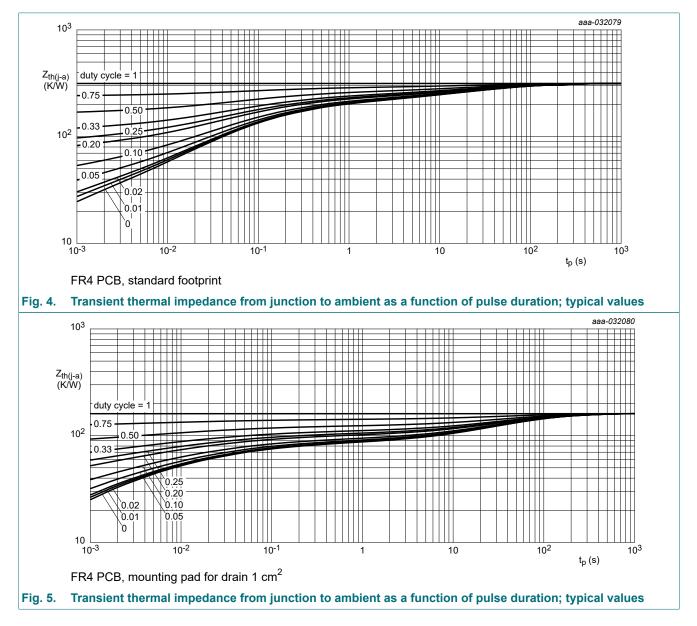


# 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	thermal resistance from	in free air	[1]	-	314	360	K/W
		[2]	-	159	180	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	35	40	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

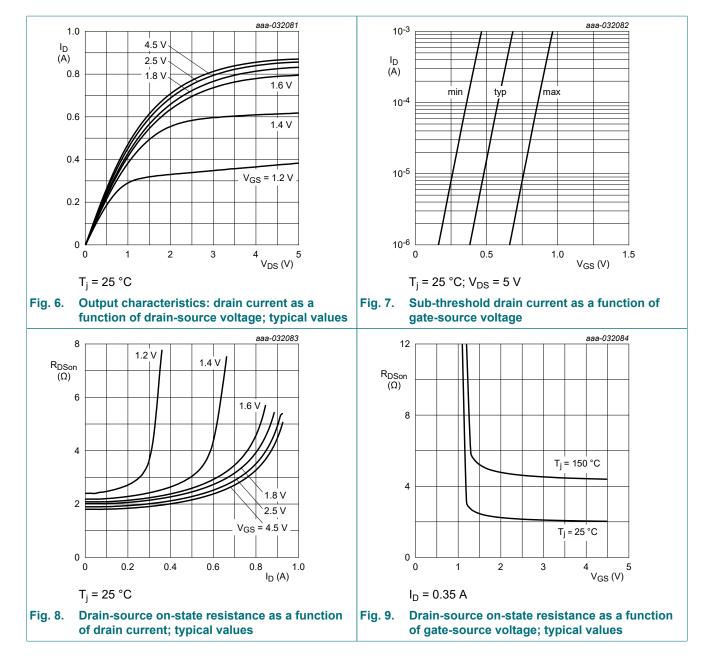
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.



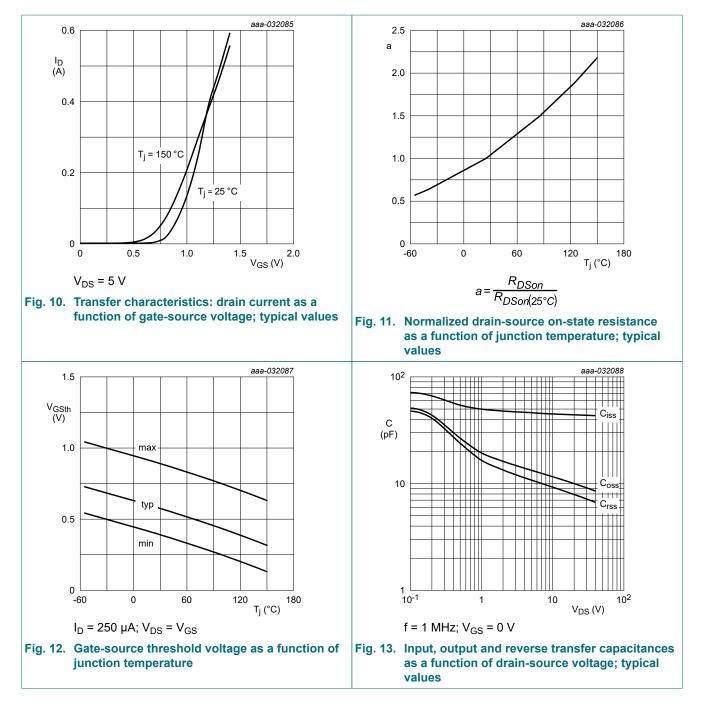
# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	50	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C	0.4	0.7	0.9	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 50 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μA
		V <sub>GS</sub> = -8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-10	μA
		V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
		V <sub>GS</sub> = -4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-1	μA
		V <sub>GS</sub> = 2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		V <sub>GS</sub> = -2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
Doon	drain-source on-state	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 25 °C	-	2	2.8	Ω
	resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 150 °C	-	4.3	6	Ω
		V <sub>GS</sub> = 2.5 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 25 °C	-	2.1	3	Ω
9fs	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 25 °C	-	1.1	-	S
Dynamic ch	aracteristics			_		
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 25 V; I <sub>D</sub> = 200 mA; V <sub>GS</sub> = 4.5 V;	-	0.47	0.7	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.04	-	nC
Q <sub>GD</sub>	gate-drain charge		-	0.1	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 25 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	29	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	2.9	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	2	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 25 V; I <sub>D</sub> = 200 mA; V <sub>GS</sub> = 4.5 V;	-	3	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	4	-	ns
t <sub>d(off)</sub>	turn-off delay time	1	-	17	-	ns
t <sub>f</sub>	fall time	1 -	-	3	-	ns
Source-drai	n diode	· ·	1			
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 350 mA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	0.9	1.2	V

#### 50 V, N-channel Trench MOSFET

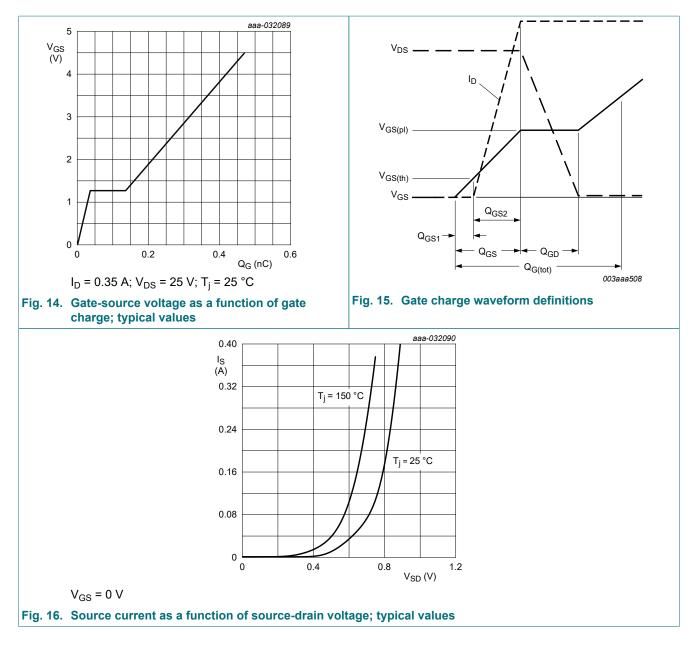


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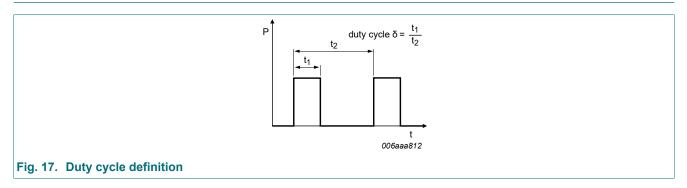


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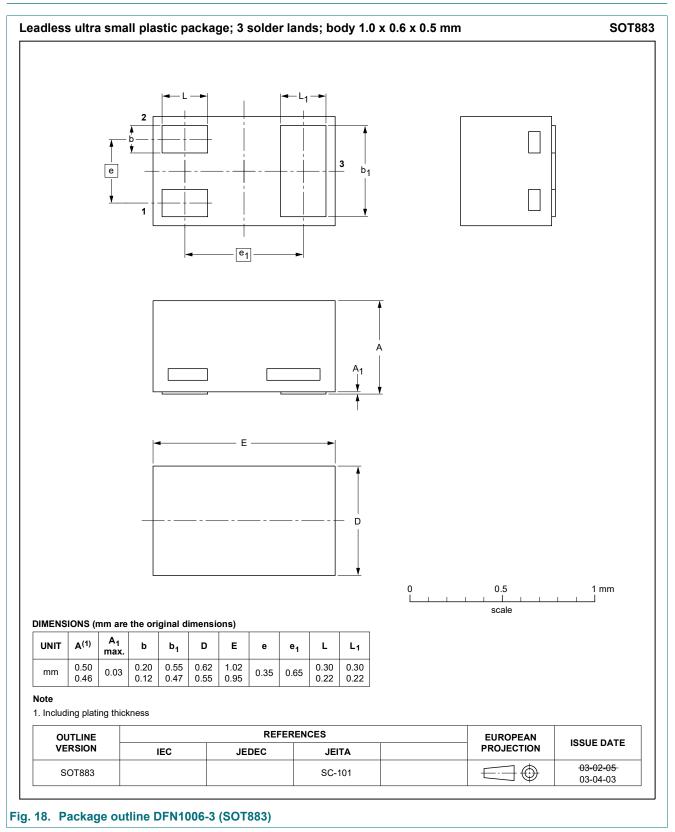


### 11. Test information



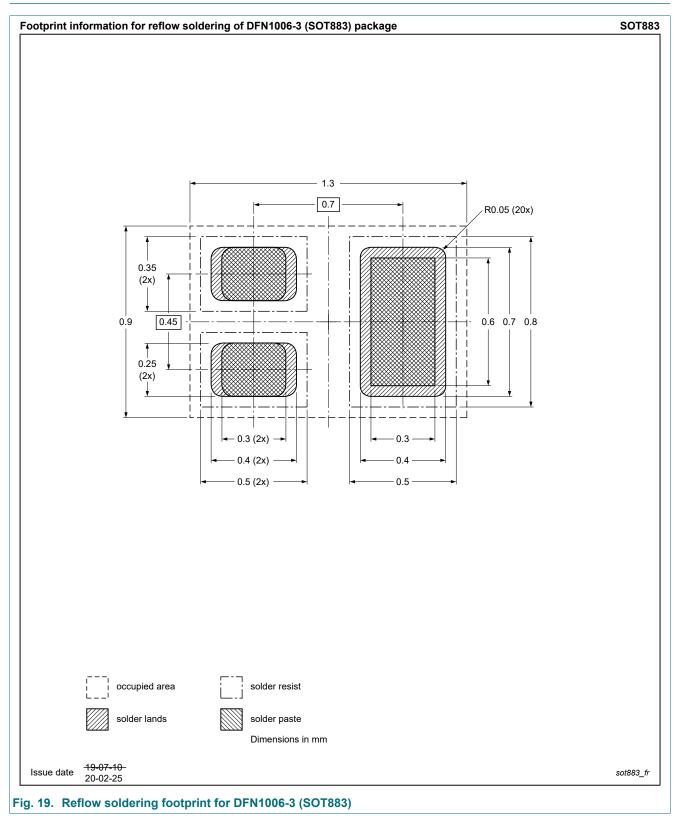
#### 50 V, N-channel Trench MOSFET

### 12. Package outline



#### 50 V, N-channel Trench MOSFET

### 13. Soldering



#### 50 V, N-channel Trench MOSFET

# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
NX5008NBKM v.1	20200901	Product data sheet	-	-		

#### 50 V, N-channel Trench MOSFET

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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