

# PMEG1020EJ-Q

10 V, 2 A ultra low VF Schottky barrier rectifier

29 August 2023

**Product data sheet** 

# 1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD323F (SC-90) small Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Forward current: ≤ 2 A
- Reverse voltage: ≤ 10 V
- Ultra low forward voltage
- Small and flat lead SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>R</sub>	reverse voltage		-	-	10	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 55 °C	-	-	2	A
I <sub>R</sub>	reverse current	V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	0.7	2	mA

# 5. Pinning information

#### Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode	1 2	K 🔣 A
2	A	anode	SC-90 (SOD323F)	sym001



# 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMEG1020EJ-Q	SC-90	plastic, surface-mounted package; 2 leads; 1.7 mm x 1.25 mm x 0.7 mm body	SOD323F			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG1020EJ-Q	СВ

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage			-	10	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 55 °C		-	2	A
I <sub>FRM</sub>	repetitive peak forward current	t <sub>p</sub> ≤ 1 ms; δ ≤ 0.25		-	7	A
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> = 8 ms; square wave		-	9	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	360	mW
			[2]	-	830	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

# 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient		[1] [2]	-	-	350	K/W	
		[3] [2]	-	-	150	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[4]	-	-	55	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

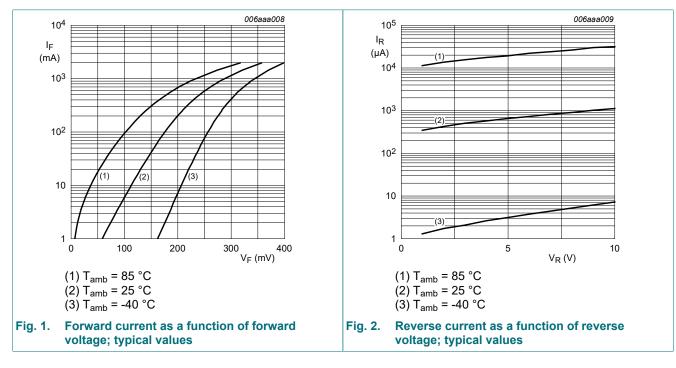
[2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[4] Soldering point of cathode tab.

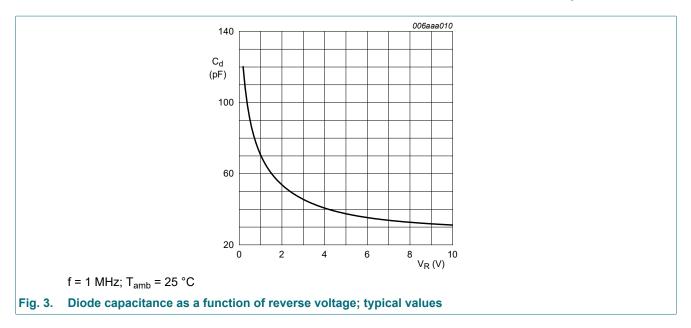
# **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.01 A; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	100	130	mV
		I <sub>F</sub> = 0.1 A; pulsed; t <sub>p</sub> ≤ 300 μs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	170	200	mV
		$I_F$ = 1 A; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	280	350	mV
		$I_F$ = 2 A; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	350	460	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	0.7	2	mA
		V <sub>R</sub> = 8 V; T <sub>amb</sub> = 25 °C	-	1	2.5	mA
		V <sub>R</sub> = 10 V; T <sub>amb</sub> = 25 °C	-	1.2	3	mA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 5 V; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	40	50	pF



# PMEG1020EJ-Q

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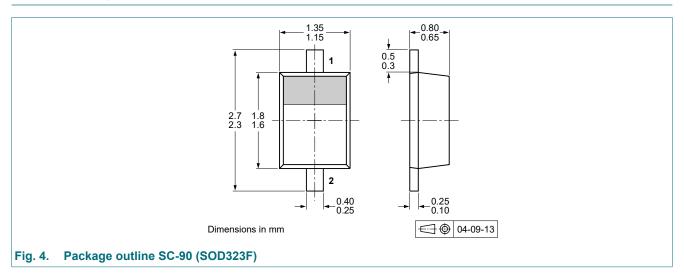


### **11. Test information**

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

#### **12. Package outline**

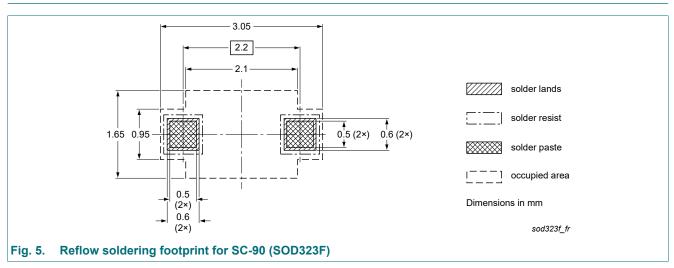


4/8

# PMEG1020EJ-Q

#### 10 V, 2 A ultra low VF Schottky barrier rectifier

# 13. Soldering



# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG1020EJ-Q v.1	20230829	Product data sheet	-	-		

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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### Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	1
6. Ordering information	2
7. Marking	2
8. Limiting values	2
9. Thermal characteristics	2
10. Characteristics	3
11. Test information	4
12. Package outline	4
13. Soldering	
14. Revision history	6
15. Legal information	

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**Product data sheet** 

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