

# BC68PAS series

20 V, 2 A NPN medium power transistors

Rev. 1 — 19 June 2015

Product data sheet

## 1. Product profile

### 1.1 General description

NPN medium power transistors in an ultra thin DFN2020D-3 (SOT1061D) leadless small Surface-Mounted Device (SMD) plastic package with medium power capability and visible and solderable side pads.

PNP complement: BC69PAS series

### 1.2 Features and benefits

- High collector current capability  
 $I_C$  and  $I_{CM}$
- Reduced Printed-Circuit Board (PCB) area requirements
- Exposed heat sink for excellent thermal and electrical conductivity
- AEC-Q101 qualified
- Two current gain selections
- Leadless very small SMD plastic package with medium power capability
- Suitable for Automatic Optical Inspection (AOI) of solder joint

### 1.3 Applications

- Linear voltage regulators
- Battery driven devices
- MOSFET drivers
- Low-side switches
- Power management
- Amplifiers

### 1.4 Quick reference data

Table 1. Quick reference data

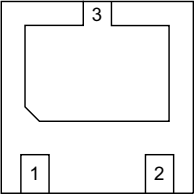
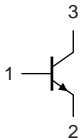
$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	20	V
$I_C$	collector current		-	-	2	A
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1\text{ ms}$	-	-	3	A
$h_{FE}$	DC current gain	$V_{CE} = 1\text{ V}$ ; $I_C = 500\text{ mA}$	[1]	85	-	375
	$h_{FE}$ selection -25	$V_{CE} = 1\text{ V}$ ; $I_C = 500\text{ mA}$	[1]	160	-	375

[1] Pulse test:  $t_p \leq 300\text{ }\mu\text{s}$ ;  $\delta \leq 0.02$ .

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	base	 <p>Transparent top view</p>	 <p>sym021</p>
2	emitter		
3	collector		

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC68PAS	DFN2020D-3	plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body $2 \times 2 \times 0.65$ mm.	SOT1061D
BC68-25PAS			

## 4. Marking

Table 4. Marking codes

Type number	Marking code
BC68PAS	BY
BC68-25PAS	BZ

## 5. Limiting values

Table 5. Limiting values

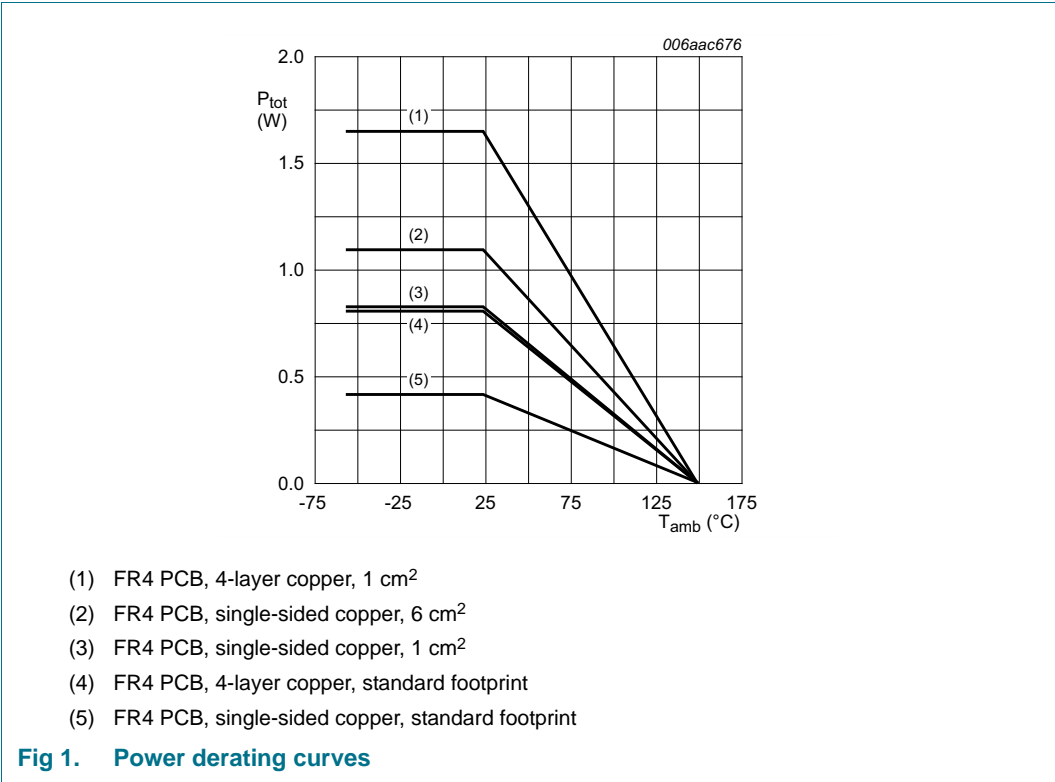
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	-	32	V
$V_{CEO}$	collector-emitter voltage	open base	-	20	V
$V_{EBO}$	emitter-base voltage	open collector	-	5	V
$I_C$	collector current		-	2	A
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	3	A
$I_B$	base current		-	0.4	A

Table 5. Limiting values ...continued  
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	420 mW
			[2]	-	830 mW
			[3]	-	1.1 W
			[4]	-	810 mW
			[5]	-	1.65 W
T <sub>j</sub>	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	150	°C
T <sub>stg</sub>	storage temperature		-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.
- [4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- [5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.

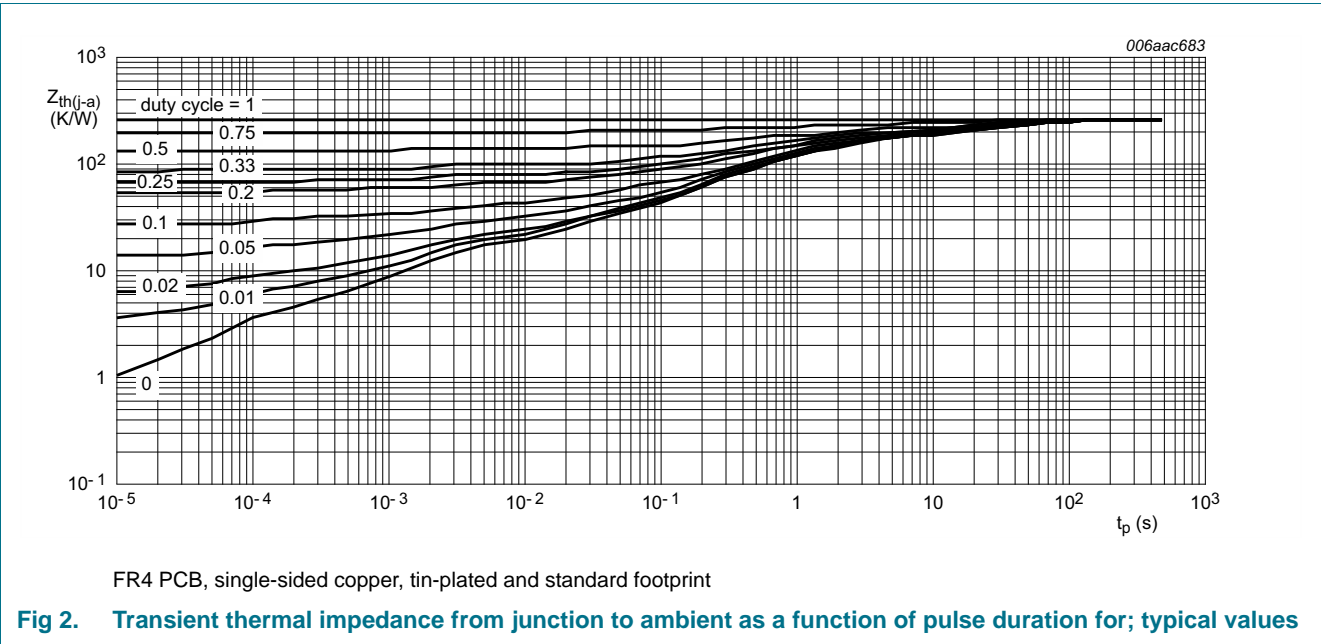


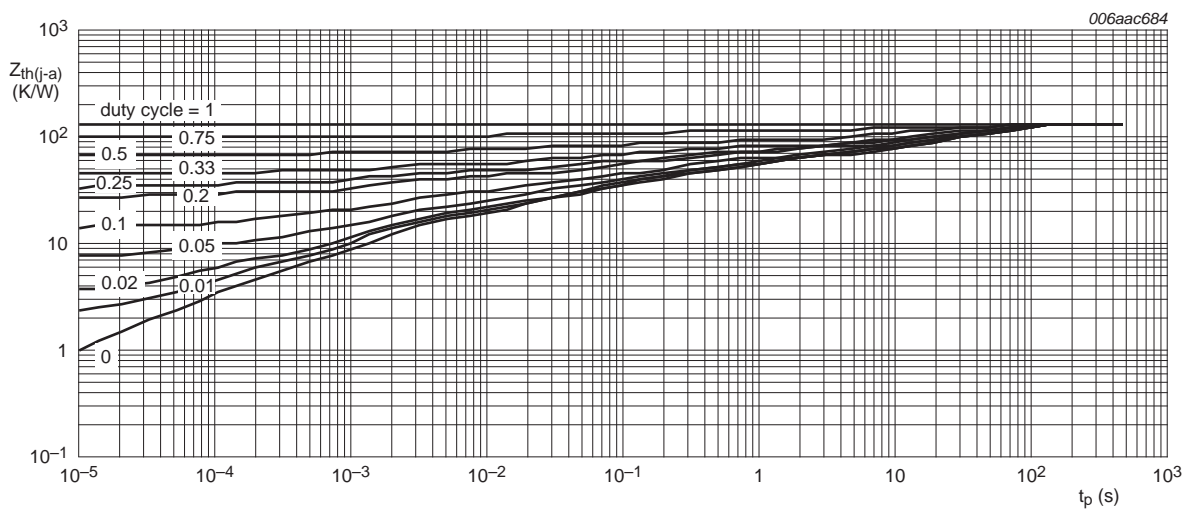
6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] 298	K/W
			[2] 151	K/W
			[3] 114	K/W
			[4] 154	K/W
			[5] 76	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point	in free air	20	K/W

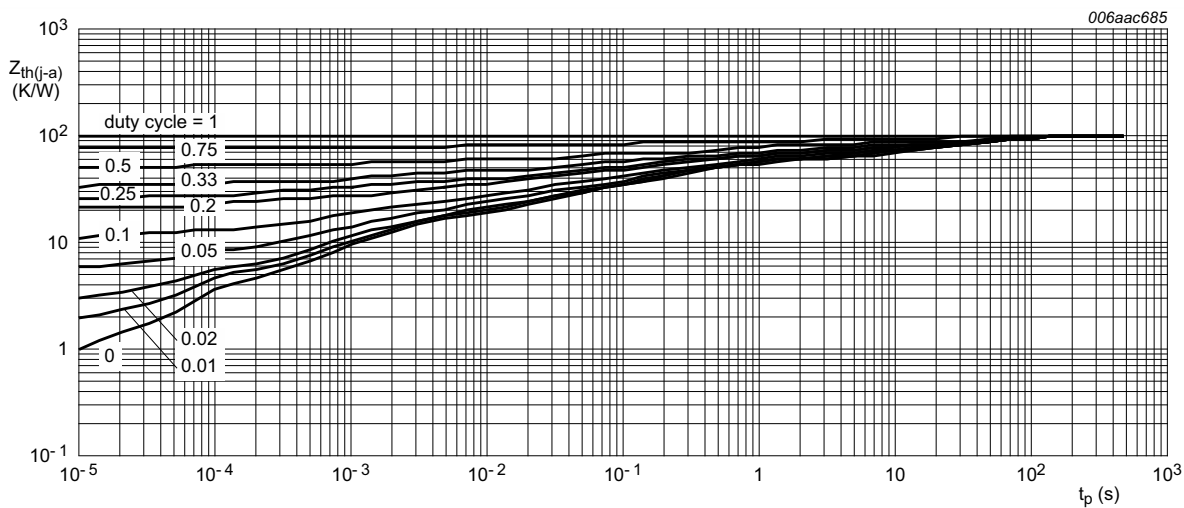
- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.
- [4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- [5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>





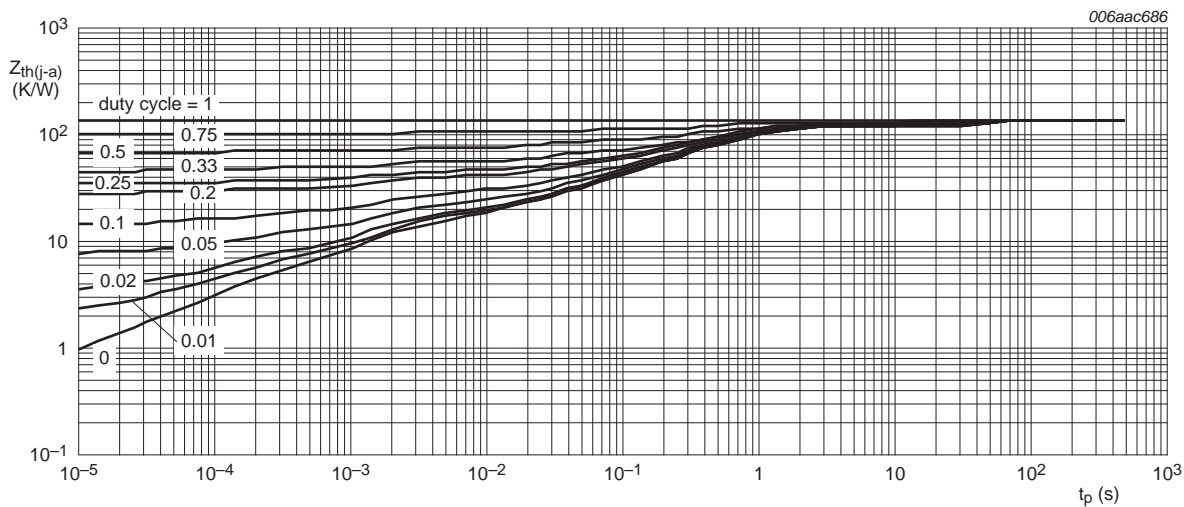
FR4 PCB, single-sided copper, tin-plated and mounting pad for collector  $1\text{ cm}^2$

Fig 3. Transient thermal impedance from junction to ambient as a function of pulse duration for; typical values



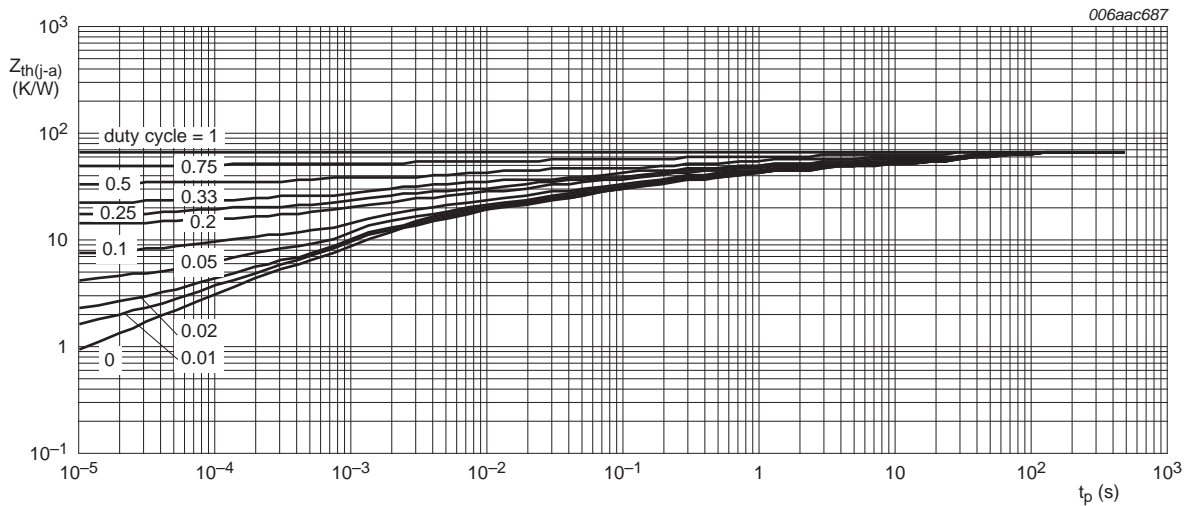
FR4 PCB, single-sided copper, tin-plated and mounting pad for collector  $6\text{ cm}^2$

Fig 4. Transient thermal impedance from junction to ambient as a function of pulse duration for; typical values



FR4 PCB, 4-layer copper, tin-plated and standard footprint

Fig 5. Transient thermal impedance from junction to ambient as a function of pulse duration for; typical values



FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>

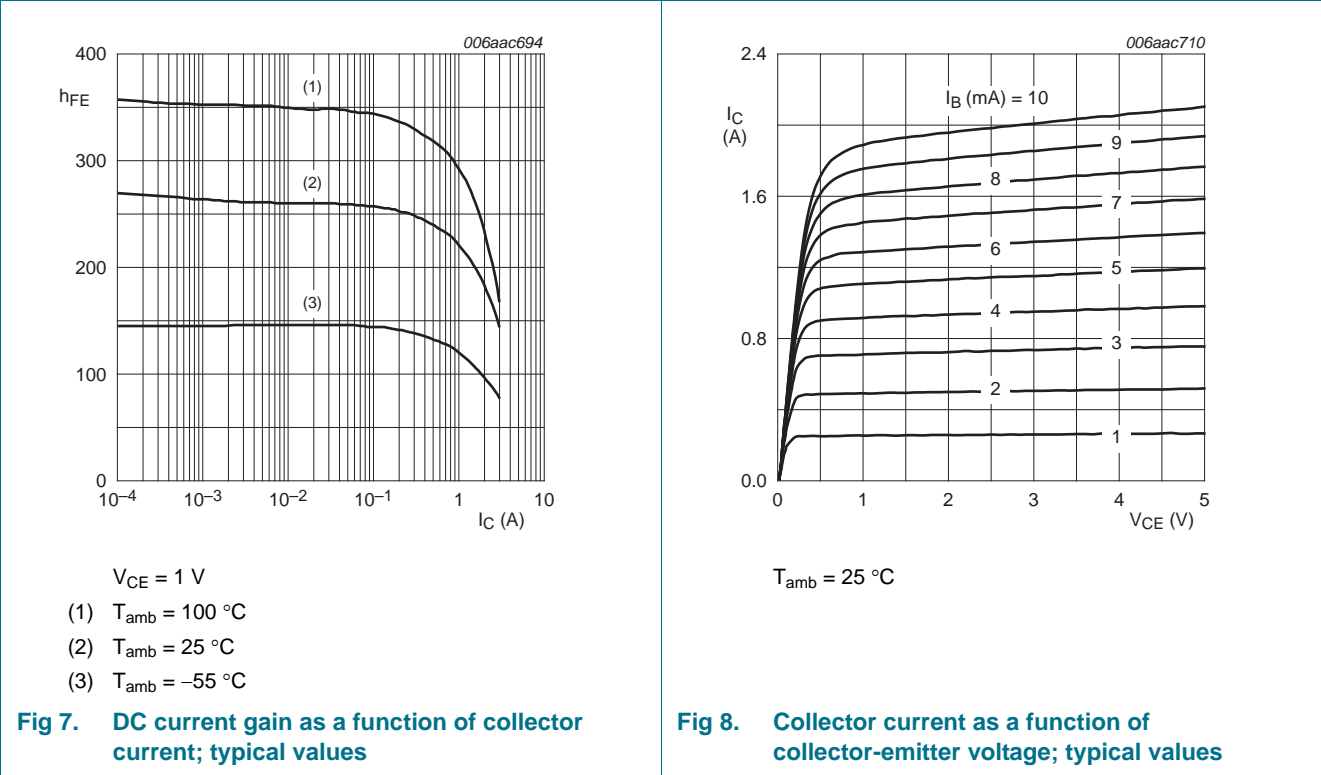
Fig 6. Transient thermal impedance from junction to ambient as a function of pulse duration for; typical values

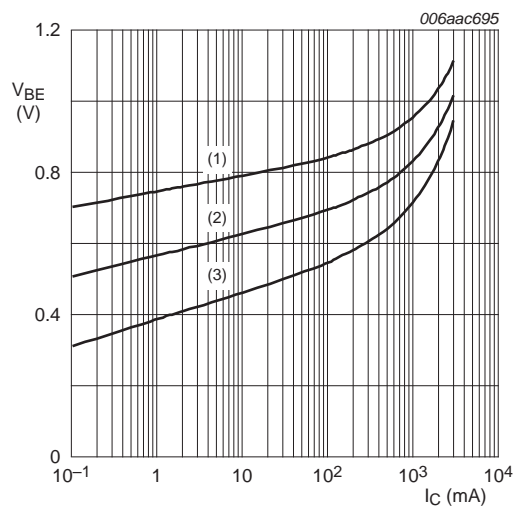
7. Characteristics

Table 7. Characteristics  
*T<sub>amb</sub> = 25 °C unless otherwise specified*

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A	-	-	100	nA
		V <sub>CB</sub> = 25 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 5 mA	50	-	-	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1] 85	-	375	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 1 A	[1] 60	-	-	
		V <sub>CE</sub> = 1 V; I <sub>C</sub> = 2 A	[1] 40	-	-	
	h <sub>FE</sub> selection -25	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	[1] 160	-	375	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA	[1] -	-	0.5	V
		I <sub>C</sub> = 2 A; I <sub>B</sub> = 200 mA	[1] -	-	0.6	V
V <sub>BE</sub>	base-emitter voltage	I <sub>C</sub> = 5 mA; V <sub>CE</sub> = 10 V	[1] -	-	0.7	V
		I <sub>C</sub> = 1 A; V <sub>CE</sub> = 1 V	[1] -	-	1	V
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz	40	170	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz	-	22	-	pF

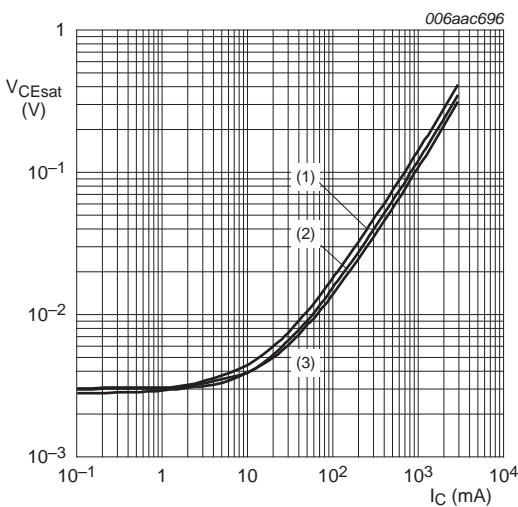
[1] Pulse test: t<sub>p</sub> ≤ 300 μs; δ ≤ 0.02.





- $V_{CE} = 1\text{ V}$
- (1)  $T_{amb} = -55^\circ\text{C}$
  - (2)  $T_{amb} = 25^\circ\text{C}$
  - (3)  $T_{amb} = 100^\circ\text{C}$

Fig 9. Base-emitter voltage as a function of collector current; typical values



- $I_C/I_B = 10$
- (1)  $T_{amb} = 100^\circ\text{C}$
  - (2)  $T_{amb} = 25^\circ\text{C}$
  - (3)  $T_{amb} = -55^\circ\text{C}$

Fig 10. Collector-emitter saturation voltage as a function of collector current; typical values

## 8. Test information

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 9. Package outline

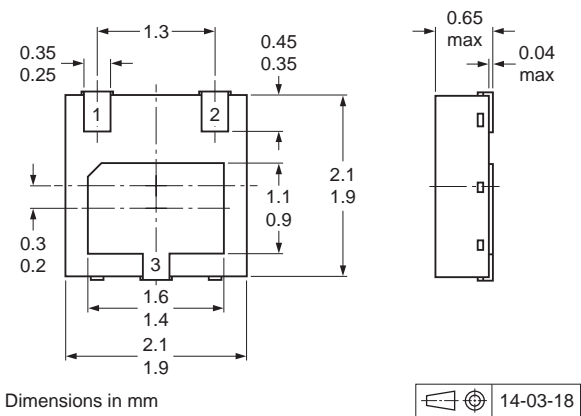
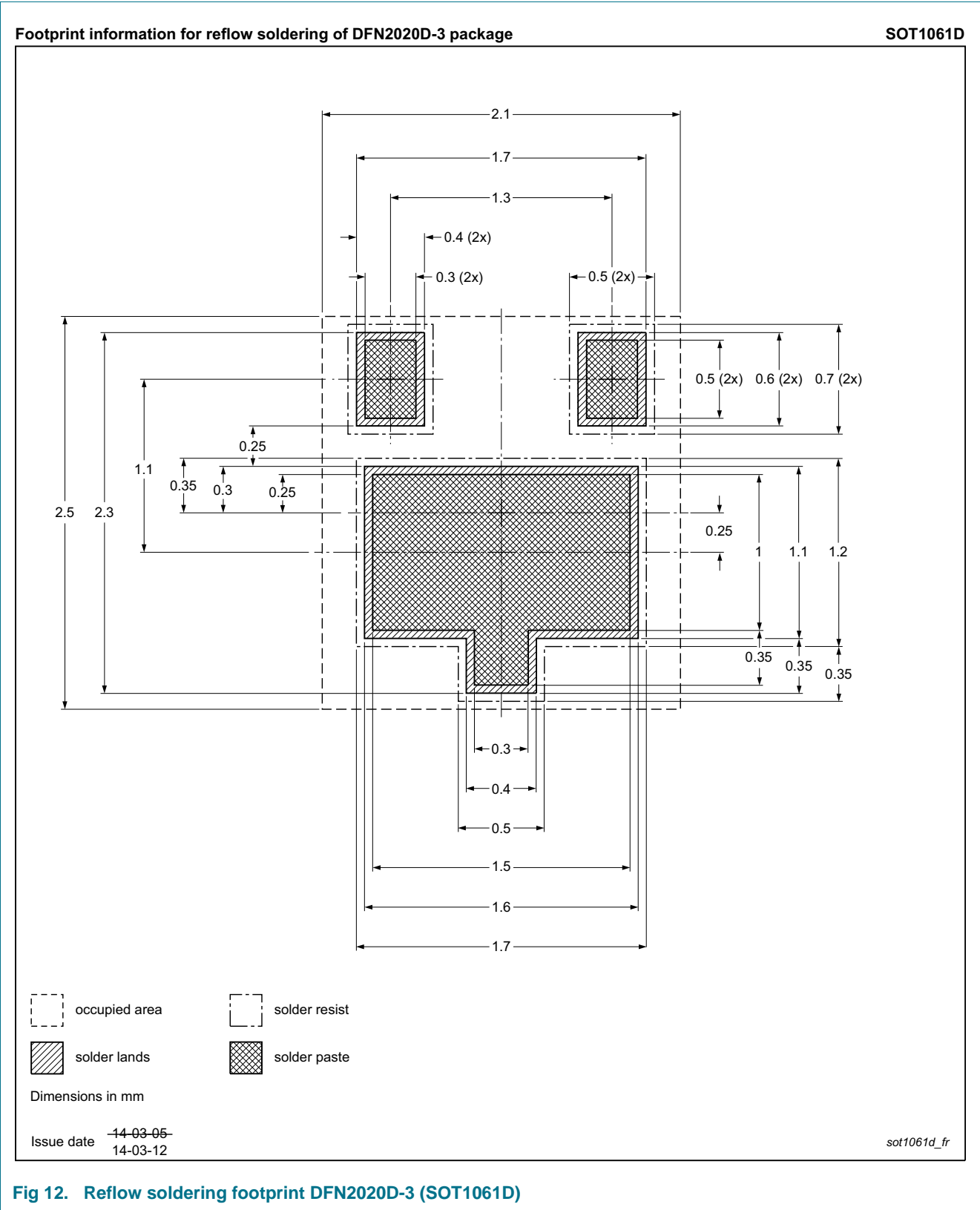


Fig 11. Package outline DFN2020D-3 (SOT1061D)



10. Soldering



## 11. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC68PAS_SER v.1	20150619	Product data sheet	-	-

## 12. Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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