Low-power Schmitt trigger inverter Rev. 11 — 12 July 2023

1. General description

The 74AUP1G14 is a single inverter with Schmitt-trigger input. This device ensures very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low static power consumption; I_{CC} = 0.9 µA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8C (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Applications

- Wave and pulse shaper
- Astable multivibrator
- Monostable multivibrator

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4. Ordering information

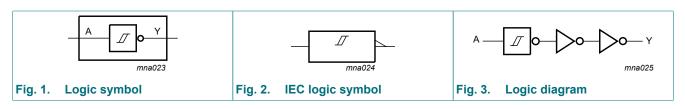
Table 1. Ordering i	nformation			
Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G14GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	<u>SOT353-1</u>
74AUP1G14GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	<u>SOT753</u>
74AUP1G14GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	<u>SOT886</u>
74AUP1G14GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	<u>SOT1115</u>
74AUP1G14GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	<u>SOT1202</u>
74AUP1G14GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	<u>SOT1226-3</u>
74AUP1G14GX4	-40 °C to +125 °C	X2SON4	plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 × 0.6 × 0.32 mm	<u>SOT1269-2</u>

5. Marking

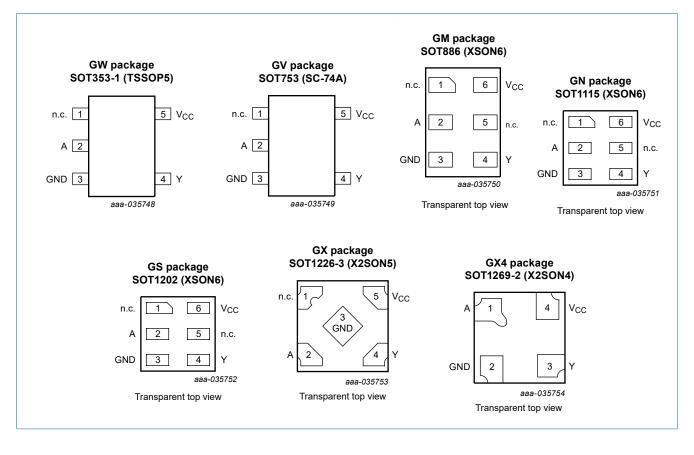
Table 2. Marking	
Type number	Marking code[1]
74AUP1G14GW	pF
74AUP1G14GV	pF
74AUP1G14GM	pF
74AUP1G14GN	pF
74AUP1G14GS	pF
74AUP1G14GX	pF
74AUP1G14GX4	pF

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram



7. Pinning information



7.1. Pinning

7.2. Pin description

Symbol	Pin	Pin				
	TSSOP5, SC-74A and X2SON5	XSON6	X2SON4			
n.c.	1	1, 5	-	not connected		
A	2	2	1	data input		
GND	3	3	2	ground (0 V)		
Y	4	4	3	data output		
V _{CC}	5	6	4	supply voltage		

8. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
Α	Y
L	Н
Н	L

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
l _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I _{ОК}	output clamping current	V _O < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
lo	output current	$V_{O} = 0 V$ to V_{CC}		-	±20	mA
I _{CC}	supply current			-	+50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C				
		TSSOP5, SC-74A, XSON6 and X2SON5 package	[2]	-	250	mW
		X2SON4 package	[3]	-	150	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P_{tot} derates linearly with 3.8 mW/K above 85 °C. For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT1115 (XSON6) package: P_{tot} derates linearly with 3.2 mW/K above 71 °C. For SOT1202 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT1226-3 (X2SON5) package: P_{tot} derates linearly with 3.0 mW/K above 67 °C.

[3] For SOT1269-2 (X2SON4) package: P_{tot} derates linearly with 1.7 mW/K above 57 °C.

10. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V_{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C

Table 6. Recommended operating conditions

11. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	25 °C					
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
lı	input leakage current	V_1 = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.2	μA
ΔI _{OFF}	additional power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V	-	-	±0.2	μA
I _{CC}	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
Δl _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μA
CI	input capacitance	V_{I} = GND or V_{CC} ; V_{CC} = 0 V to 3.6 V	-	1.1	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.7	-	pF
T _{amb} = -	40 °C to +85 °C	1				1
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
I _I	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.5	μA
∆I _{OFF}	additional power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V	-	-	±0.6	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μA
T _{amb} = -4	40 °C to +125 °C	-	I			
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		$I_0 = 20 \ \mu A; V_{CC} = 0.8 \ V \ to \ 3.6 \ V$	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V; } V_{CC} = 0 \text{ V}$	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V	-	-	±0.75	μA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μA

12. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5.

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C t	o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 5 p	F			1						
t _{pd}	propagation	A to Y; see Fig. 4 [2]							
	delay	V _{CC} = 0.8 V	-	19.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.7	5.9	11.0	2.4	11.1	2.4	11.2	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.3	6.6	2.4	7.1	2.4	7.4	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	3.7	5.4	2.0	6.0	2.0	6.2	ns
		V _{CC} = 2.3 V to 2.7 V	2.0	3.0	4.1	1.7	4.5	1.7	4.7	ns
		V _{CC} = 3.0 V to 3.6 V	1.9	2.8	3.6	1.5	3.9	1.5	4.0	ns
C _L = 10	pF	-								
t _{pd}	propagation	A to Y; see Fig. 4 [2]]							
	delay	V _{CC} = 0.8 V	-	23.4	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.9	6.8	12.7	2.8	12.8	2.8	12.9	ns
		V _{CC} = 1.4 V to 1.6 V	2.8	5.0	7.7	2.6	8.2	2.6	8.6	ns
		V _{CC} = 1.65 V to 1.95 V	2.7	4.2	6.2	2.5	6.7	2.5	7.1	ns
		V _{CC} = 2.3 V to 2.7 V	2.3	3.6	4.8	2.1	5.2	2.1	5.5	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	3.3	4.3	2.0	4.5	2.0	4.7	ns
C _L = 15	pF									
t _{pd}	propagation	A to Y; see <u>Fig. 4</u> [2]							
	delay	V _{CC} = 0.8 V	-	26.9	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.3	7.6	14.3	3.0	14.5	3.0	14.7	ns
		V _{CC} = 1.4 V to 1.6 V	3.3	5.5	8.6	2.9	9.4	2.9	9.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.8	4.7	7.0	2.8	7.7	2.8	8.1	ns
		V _{CC} = 2.3 V to 2.7 V	2.7	4.0	5.5	2.4	5.9	2.4	6.2	ns
		V _{CC} = 3.0 V to 3.6 V	2.6	3.8	4.8	2.2	5.2	2.2	5.4	ns
C _L = 30	pF					·			·	
t _{pd}	propagation	A to Y; see Fig. 4 [2]]							
	delay	V _{CC} = 0.8 V	-	37.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.0	9.8	18.7	3.9	19.6	3.9	20.0	ns
		V _{CC} = 1.4 V to 1.6 V	3.7	7.1	11.2	3.8	12.3	3.8	12.9	ns
		V _{CC} = 1.65 V to 1.95 V	3.6	6.0	9.1	3.6	10.0	3.6	10.6	ns
		V _{CC} = 2.3 V to 2.7 V	3.5	5.2	6.9	3.2	7.5	3.2	7.9	ns
		V _{CC} = 3.0 V to 3.6 V	3.3	4.8	6.1	3.1	7.1	3.1	7.4	ns

Low-power Schmitt trigger inverter

Symbol Parameter		meter Conditions		25 °C ·		-40 °C t	-40 °C to +85 °C		-40 °C to +125 °C	
			Min	Typ[1]	Мах	Min	Max	Min	Мах	1
C _L = 5 p	F, 10 pF, 15 p	F and 30 pF								
C _{PD}	power	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{\text{CC}}$ [3]								
	dissipation capacitance	V _{CC} = 0.8 V	-	2.6	-	-	-	-	-	pF
	capacitanee	V _{CC} = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.9	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.1	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.7	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.3	-	-	-	-	-	pF

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] \dot{C}_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

12.1. Waveform and test circuit

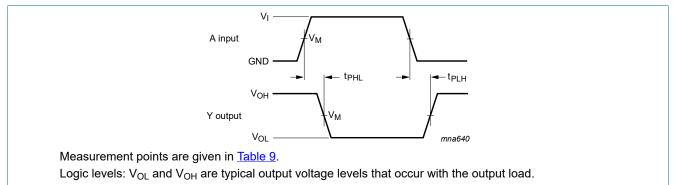


Fig. 4. The data input (A) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input					
V _{cc}	V _M	V_{M} V_{l} $t_{r} = t_{f}$					
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{CC}	≤ 3.0 ns			

Low-power Schmitt trigger inverter

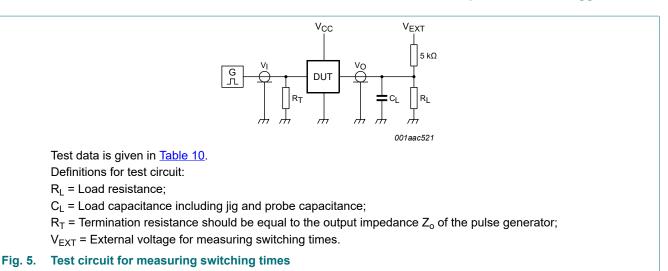


Table 10. Test data

Supply voltage	Load	V _{EXT}					
V _{cc}	C _L R _L [1]		/ _{CC} C _L R _L [1]		t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}		

[1] For measuring enable and disable times $R_L = 5 k\Omega$. For measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

12.2. Transfer characteristics

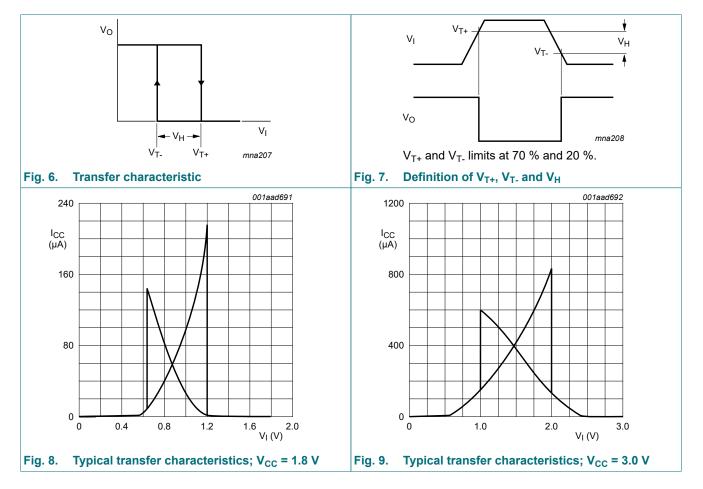
Table 11. Transfer characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Мах	Min	Max	
V _{T+}	positive-going threshold voltage	see Fig. 6 and Fig. 7								
		V _{CC} = 0.8 V	0.30	-	0.60	0.30	0.60	0.30	0.62	V
		V _{CC} = 1.1 V	0.53	-	0.90	0.53	0.90	0.53	0.92	V
		V _{CC} = 1.4 V	0.74	-	1.11	0.74	1.11	0.74	1.13	V
		V _{CC} = 1.65 V	0.91	-	1.29	0.91	1.29	0.91	1.31	V
		V _{CC} = 2.3 V	1.37	-	1.77	1.37	1.77	1.37	1.80	V
		V _{CC} = 3.0 V	1.88	-	2.29	1.88	2.29	1.88	2.32	V
V _{T-}	negative-going threshold voltage	see Fig. 6 and Fig. 7								
		V _{CC} = 0.8 V	0.10	-	0.60	0.10	0.60	0.10	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	0.26	0.65	0.26	0.65	V
		V _{CC} = 1.4 V	0.39	-	0.75	0.39	0.75	0.39	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	0.47	0.84	0.47	0.84	V
		V _{CC} = 2.3 V	0.69	-	1.04	0.69	1.04	0.69	1.04	V
		V _{CC} = 3.0 V	0.88	-	1.24	0.88	1.24	0.88	1.24	V

Symbol Parameter		Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Мах	1
V _H	hysteresis voltage	see <u>Fig. 6, Fig. 7, Fig. 8</u> and <u>Fig. 9</u>								
		V _{CC} = 0.8 V	0.07	-	0.50	0.07	0.50	0.07	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	0.08	0.46	0.08	0.46	V
		V _{CC} = 1.4 V	0.18	-	0.56	0.18	0.56	0.18	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	0.27	0.66	0.27	0.66	V
		V _{CC} = 2.3 V	0.53	-	0.92	0.53	0.92	0.53	0.92	V
		V _{CC} = 3.0 V	0.79	-	1.31	0.79	1.31	0.79	1.31	V

12.3. Waveforms transfer characteristics



13. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{ad} = f_i \times (t_r \times I_{CC(AV)} + t_f \times I_{CC(AV)}) \times V_{CC}$ where:

- P_{ad} = additional power dissipation (μW);
- f_i = input frequency (MHz);
- t_r = input rise time (ns); 10 % to 90 %;
- t_f = input fall time (ns); 90 % to 10 %;
- I_{CC(AV)} = average additional supply current (µA).

Average I_{CC} differs with positive or negative input transitions, as shown in Fig. 10.

An example of a relaxation circuit using the 74AUP1G14 is shown in Fig. 11.

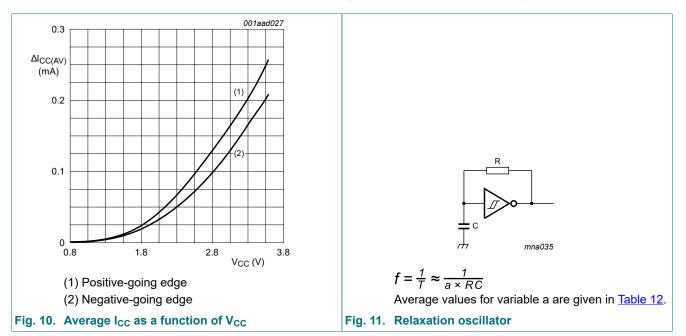


Table 12. Variable values

Supply voltage	Variable a
1.1 V	1.28
1.5 V	1.22
1.8 V	1.24
2.8 V	1.34
3.3 V	1.45

14. Package outline

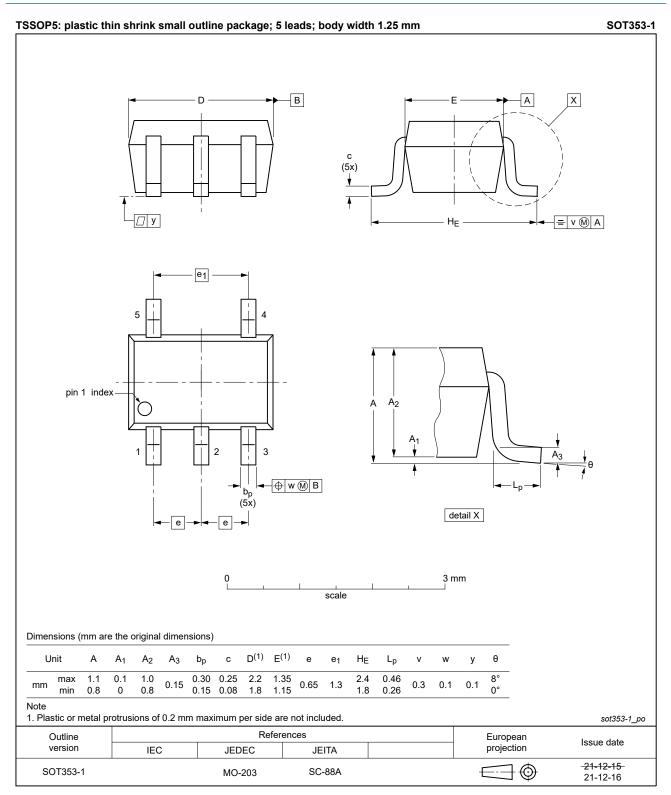


Fig. 12. Package outline SOT353-1 (TSSOP5)

Low-power Schmitt trigger inverter



SOT753

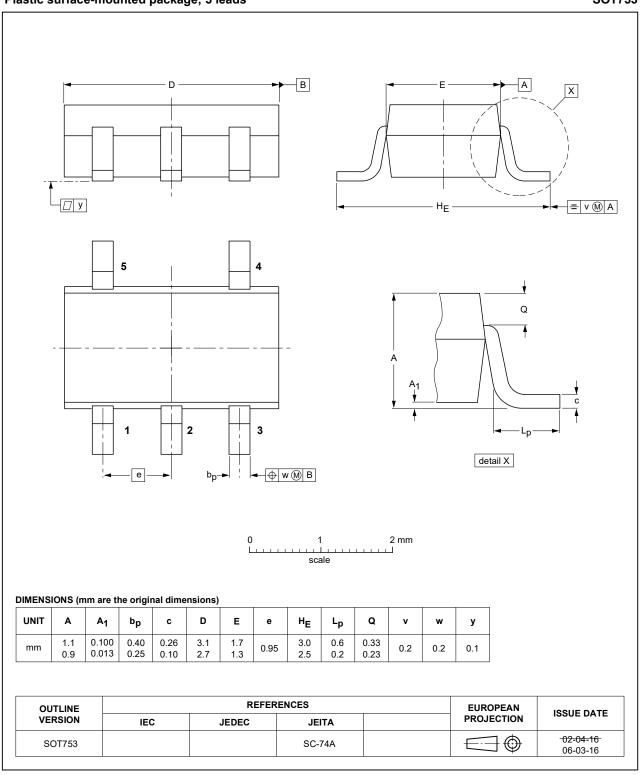


Fig. 13. Package outline SOT753 (SC-74A)

Low-power Schmitt trigger inverter

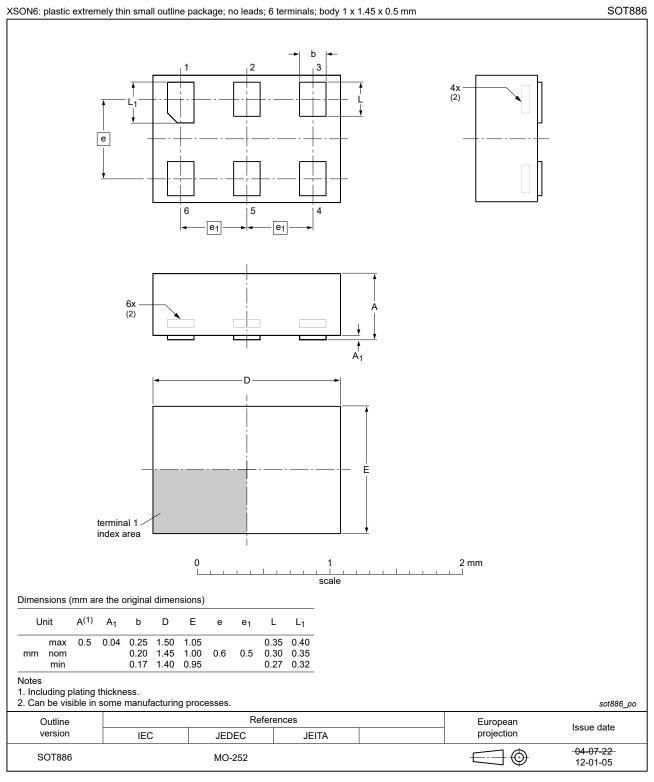


Fig. 14. Package outline SOT886 (XSON6)

Low-power Schmitt trigger inverter

XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

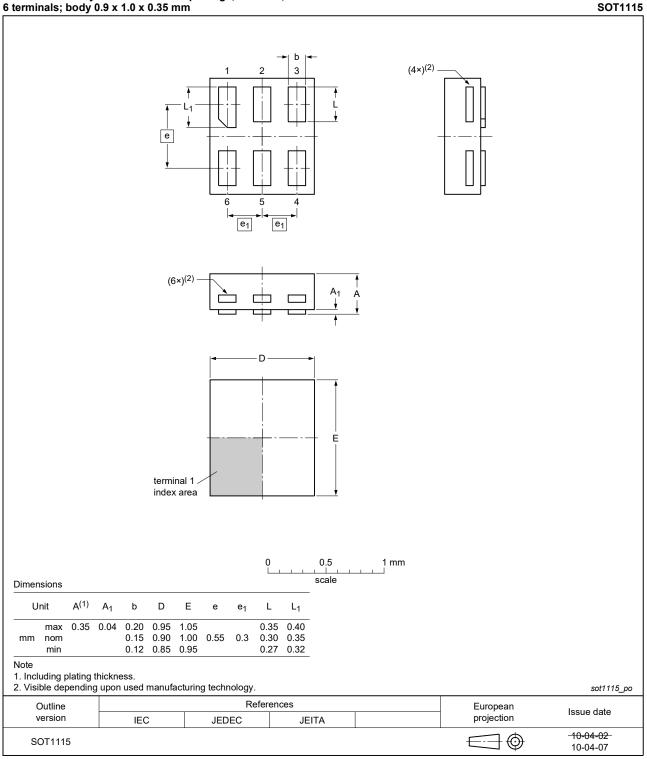
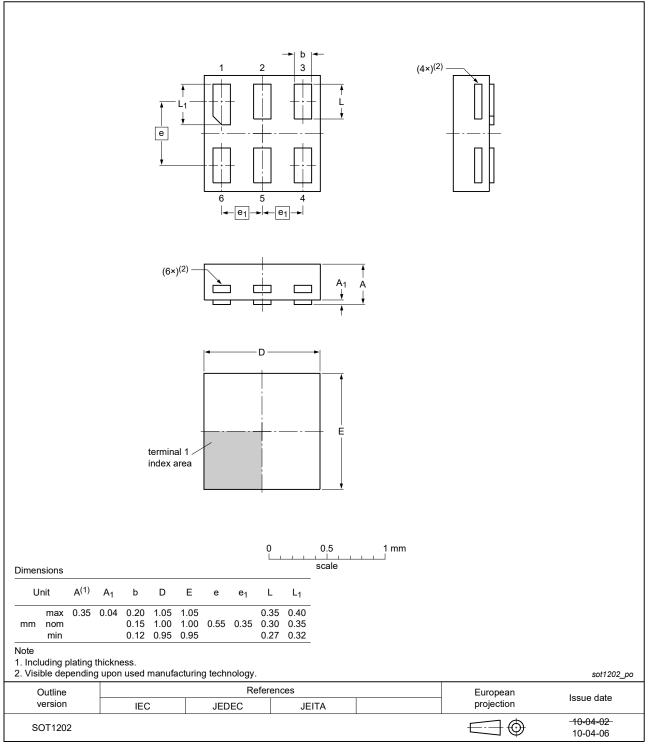


Fig. 15. Package outline SOT1115 (XSON6)

SOT1202

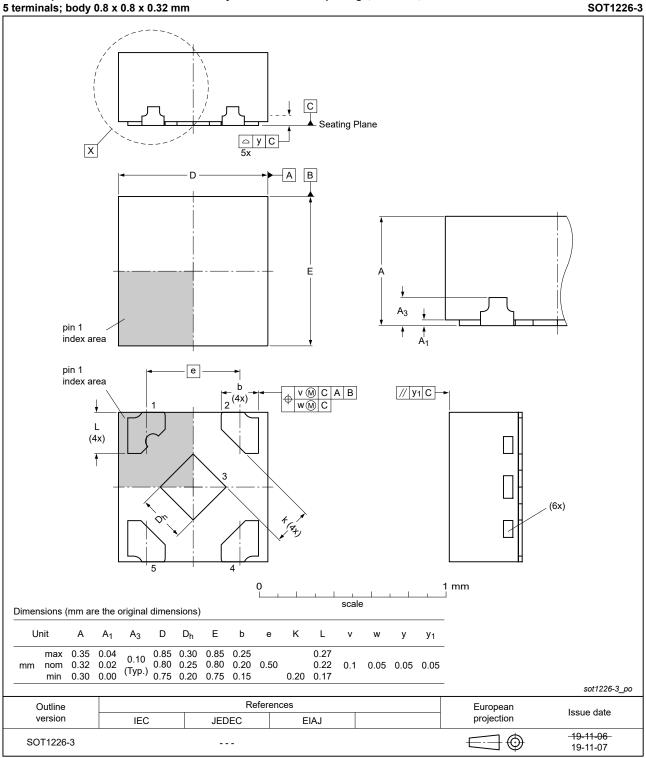
Low-power Schmitt trigger inverter

XSON6: extremely thin small outline package; no leads;	
6 terminals; body 1.0 x 1.0 x 0.35 mm	





Low-power Schmitt trigger inverter



X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.32 mm

Fig. 17. Package outline SOT1226-3 (X2SON5)

Low-power Schmitt trigger inverter

X2SON4: plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 x 0.6 x 0.32 mm SOT1269-2 Х В Α D pin 1 . ID area 1 A'1 detail X С // y₁ C ---___у е 2 3 (4x) е 4 1 ⊕ v (M) C | A | B b ⊕ w (M) C (4x) 1 mm 0 scale Dimensions (mm are the original dimensions) Unit А D Е b A_1 е L v w у У1 0.35 0.04 0.65 0.65 0.20 0.23 max mm nom 0.32 0.02 0.60 0.60 0.15 0.40 0.18 0.10 0.05 0.05 0.05 min 0.30 0.00 0.55 0.55 0.10 0.13 sot1269-2_po References Outline European Issue date projection version IEC JEDEC JEITA



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SOT1269-2

15. Abbreviations

Table 13. Abbreviations				
Acronym	Description			
CDM	Charged Device Model			
DUT	Device Under Test			
ESD	ElectroStatic Discharge			
НВМ	Human Body Model			

16. Revision history

Table 14. Revision history Document ID Release date Data sheet status Change notice Supersedes 74AUP1G14 v.11 20230712 Product data sheet 74AUP1G14 v.10 Modifications: Section 2 : ESD specification updated according to the latest JEDEC standard. 74AUP1G14 v.10 Product data sheet 74AUP1G14 v.9 20220114 Modifications: Section 2 updated. • Fig. 12: Package outline drawing for SOT353-1 (TSSOP5) has changed. 74AUP1G14 v.9 20210713 Product data sheet 74AUP1G14 v.8 Modifications: SOT1226 (X2SON5) package changed to SOT1226-3 (X2SON5) package. Type number 74AUP1G14GF (SOT891 / XSON6) removed. Table 5: Derating values for Ptot total power dissipation updated. 74AUP1G14 v.8 20180608 Product data sheet 74AUP1G14 v.7 Modifications: The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. • Added type number 74AUP1G14GX4 (SOT1269-2) 74AUP1G14 v.7 20161104 Product data sheet 74AUP1G14 v.6 Modifications: Added type number 74AUP1G14GV (SOT753) • 74AUP1G14 v.6 20120628 Product data sheet 74AUP1G14 v.5 Modifications: Added type number 74AUP1G14GX (SOT1226) • • Package outline drawing of SOT886 (Fig. 14) modified. 74AUP1G14 v.5 20111128 Product data sheet 74AUP1G14 v.4 Modifications: Legal pages updated. • 74AUP1G14 v.4 Product data sheet 74AUP1G14 v.3 20100713 74AUP1G14 v.3 20090708 Product data sheet 74AUP1G14 v.2 _ 74AUP1G14 v.1 74AUP1G14 v.2 20060828 Product data sheet 74AUP1G14 v.1 20050718 Product data sheet

17. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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