74LVCU04A-Q100

Hex unbuffered inverter Rev. 5 — 20 January 2025

Product data sheet

1. General description

The 74LVCU04A-Q100 is a hex unbuffered inverter. Inputs can be driven from either 3.3 V or 5 V devices. This feature allows the use of these devices as translators in mixed 3.3 V and 5 V environments.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

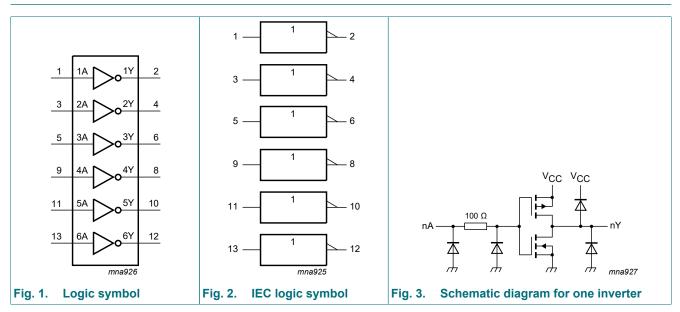
- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 1.2 V to 3.6 V
- Inputs accept voltages up to 5.5 V
- CMOS low power consumption
- Direct interface with TTL levels
- Complies with JEDEC standard:
 - JESD8-7A (1.65 V to 1.95 V)
 - JESD8-5A (2.3 V to 2.7 V)
 - JESD8-C/JESD36 (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- DHVQFN package with Side-Wettable Flanks enabling Automatic Optical Inspection (AOI) of solder joints

3. Ordering information

Table 1. Ordering infor	mation							
Type number	Package							
	Temperature range	Name	Description	Version				
74LVCU04AD-Q100	-40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width 3.9 mm	<u>SOT108-1</u>				
74LVCU04APW-Q100	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	<u>SOT402-1</u>				

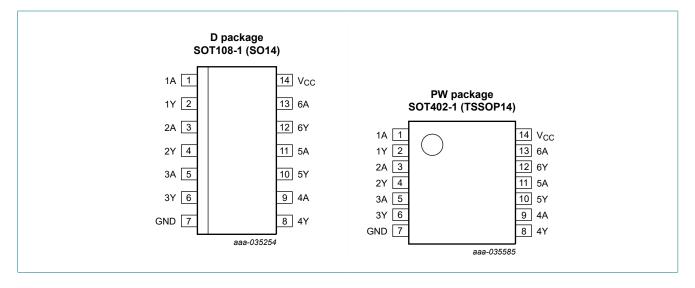
nexperia

4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description Symbol Pin Description 1A, 2A, 3A, 4A, 5A, 6A 1, 3, 5, 9, 11, 13 data input 1Y, 2Y, 3Y, 4Y, 5Y, 6Y 2, 4, 6, 8, 10, 12 data output GND 7 ground (0 V) 14 V_{CC} supply voltage

2/12

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level

Input nA	Output nY
L	Н
Н	L

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+6.5	V
I _{IK}	input clamping current	V ₁ < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+6.5	V
I _{OK}	output clamping current	$V_{\rm O}$ > $V_{\rm CC}$ or $V_{\rm O}$ < 0 V		-	±50	mA
Vo	output voltage		[2]	-0.5	V _{CC} + 0.5	V
I _O	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±50	mA
I _{CC}	supply current			-	100	mA
I _{GND}	ground current			-100	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[3]	-	500	mW

[1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.

[2] The output voltage ratings may be exceeded if the output current ratings are observed.

[3] For SOT108-1 (SO14) package: P_{tot} derates linearly with 10.1 mW/K above 100 °C.

For SOT402-1 (TSSOP14) package: Ptot derates linearly with 7.3 mW/K above 81 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		1.65	-	3.6	V
		functional	1.2	-	-	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature	in free air	-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 1.65 V to 2.7 V	0	-	20	ns/V
		V _{CC} = 2.7 V to 3.6 V	0	-	10	ns/V

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	°C to +85	°C	-40 °C to	+125 °C	Unit
			Min	Тур [1]	Max	Min	Max	
V _{IH}	HIGH-level	V _{OL(max)} = 0.5 V; I _O = -100 μA						
	input voltage	V _{CC} = 1.2 V	1.08	-	-	1.12	-	V
		V _{CC} = 1.65 V to 1.95 V	1.3	-	-	1.5	-	V
		V _{CC} = 2.3 V to 2.7 V	1.8	-	-	2.0	-	V
		V _{CC} = 3.0 V	2.0	-	-	2.4	-	V
		V _{CC} = 3.6 V	2.4	-	-	2.8	-	V
V _{IL}	LOW-level input voltage	V _{OH(min)} = V _{CC} - 0.5 V; I _O = -100 μA						
		V _{CC} = 1.2 V	-	-	0.12	-	0.1	V
		V _{CC} = 1.65 V to 1.95 V	-	-	0.6	-	0.4	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.6	-	0.5	V
		V _{CC} = 3.0 V	-	-	1.0	-	0.6	V
		V _{CC} = 3.6 V	-	-	1.2	-	0.7	V
V _{OH}		V _I = GND						
	output voltage	V _{CC} = 3.0 V; I _O = -100 μA	V _{CC} - 0.2	-	-	V _{CC} - 0.3	-	V
		V _{CC} = 1.65 V; I _O = -4 mA	1.2	-	-	1.05	-	V
		V _{CC} = 2.3 V; I _O = -8 mA	1.8	-	-	1.65	-	V
		V _{CC} = 2.7 V; I _O = -12 mA	2.2	-	-	2.05	-	V
		V _{CC} = 3.0 V; I _O = -18 mA	2.4	-	-	2.25	-	V
		V _{CC} = 3.0 V; I _O = -24 mA	2.2	-	-	2.0	-	V
V _{OL} LOW-level	V _I = V _{CC}							
	output voltage	V _{CC} = 3.0 V; I _O = 100 μA	-	-	0.20	-	0.60	V
		V _{CC} = 1.65 V; I _O = 4 mA	-	-	0.45	-	0.65	V
		V _{CC} = 2.3 V; I _O = 8 mA	-	-	0.60	-	0.80	V
		V _{CC} = 2.7 V; I _O = 12 mA	-	-	0.40	-	0.60	V
		V _{CC} = 3.0 V; I _O = 24 mA	-	-	0.55	-	0.80	V
II	input leakage current	V _{CC} = 3.6 V; V _I = 5.5 V or GND	-	±0.1	±5	-	±20	μA
I _{CC}	supply current	V_{CC} = 3.6 V; V_{I} = V_{CC} or GND; I_{O} = 0 A	-	0.1	10	-	40	μA
ΔI _{CC}	additional supply current	per input pin; $V_{CC} = 2.7 V \text{ to } 3.6 V;$ $V_1 = V_{CC} - 0.6 V; I_O = 0 A$	-	5	500	-	5000	μA
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V _I = GND to V _{CC}	-	5.5	-	-	-	pF

[1] All typical values are measured at V_{CC} = 3.3 V (unless stated otherwise) and T_{amb} = 25 °C.

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Fig. 7.

Symbol	Parameter	Conditions		-40 °C to +85 °C			-40 °C to +125 °C		Unit
				Min	Typ [1]	Max	Min	Max	
t _{pd}	propagation delay	nA to nY; see Fig. 4	[2]						
		V _{CC} = 1.2 V		-	6.0	-	-	-	ns
		V _{CC} = 1.65 V to 1.95 V		0.3	3.7	7.8	0.3	9.0	ns
		V _{CC} = 2.3 V to 2.7 V		0.5	2.2	4.4	0.5	5.2	ns
		V _{CC} = 2.7 V		0.5	2.0	4.5	0.5	6.0	ns
		V _{CC} = 3.0 V to 3.6 V		0.5	2.0	4.0	0.5	5.0	ns
t _{sk(o)}	output skew time	V _{CC} = 3.0 V to 3.6 V	[3]	-	-	1.0	-	1.5	ns
C _{PD}	power dissipation	per inverter; V_I = GND to V_{CC}	[4]						
	capacitance	V _{CC} = 1.65 V to 1.95 V		-	2.3	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V		-	5.5	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V		-	8.4	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.2 V, 1.8 V, 2.5 V, 2.7 V, and 3.3 V respectively.

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] Skew between any two outputs of the same package switching in the same direction. This parameter is guaranteed by design.

[4] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} \times N + \Sigma (C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$

 f_i = input frequency in MHz; f_o = output frequency in MHz

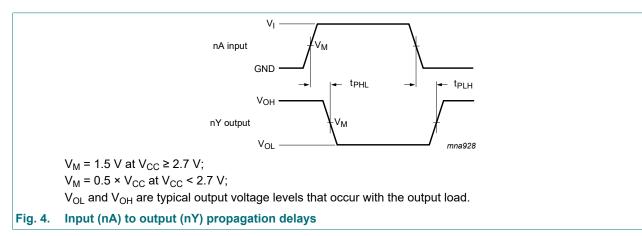
 C_L = output load capacitance in pF

V_{CC} = supply voltage in Volts

N = number of inputs switching

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs

10.1. Waveforms and test circuit

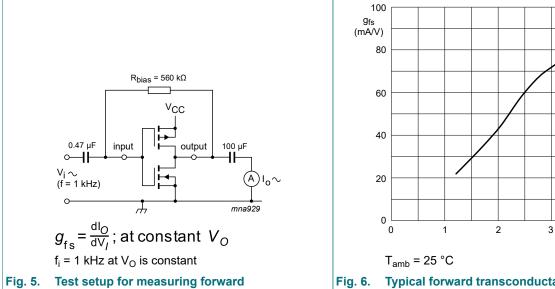


74LVCU04A-Q100

Hex unbuffered inverter

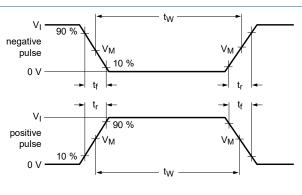
mna930

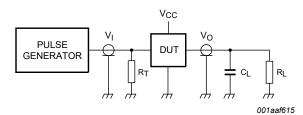
4 $V_{CC}(V)$











Test data is given in Table 8. Definitions for test circuit:

R_I = Load resistance.

- C_L = Load capacitance including jig and probe capacitance.
- R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.

Test circuit for measuring switching times Fig. 7.

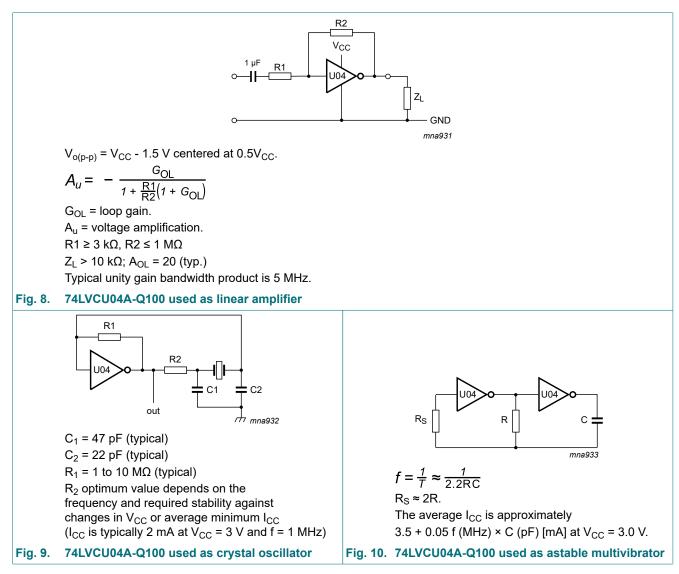
Table 8. Test data							
Supply voltage	Input		Load				
V _{cc}	VI	t _r , t _f	CL	RL			
1.2 V	V _{CC}	≤ 2 ns	30 pF	1 kΩ			
1.65 V to 1.95 V	V _{CC}	≤ 2 ns	30 pF	1 kΩ			
2.3 V to 2.7 V	V _{CC}	≤ 2 ns	30 pF	500 Ω			
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω			
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω			

Hex unbuffered inverter

11. Application information

Some applications for the 74LVCU04A-Q100 are:

- Linear amplifier: see Fig. 8
- Crystal oscillator designs; see Fig. 9
- Astable multivibrator; see Fig. 10



7 / 12

Hex unbuffered inverter

12. Package outline

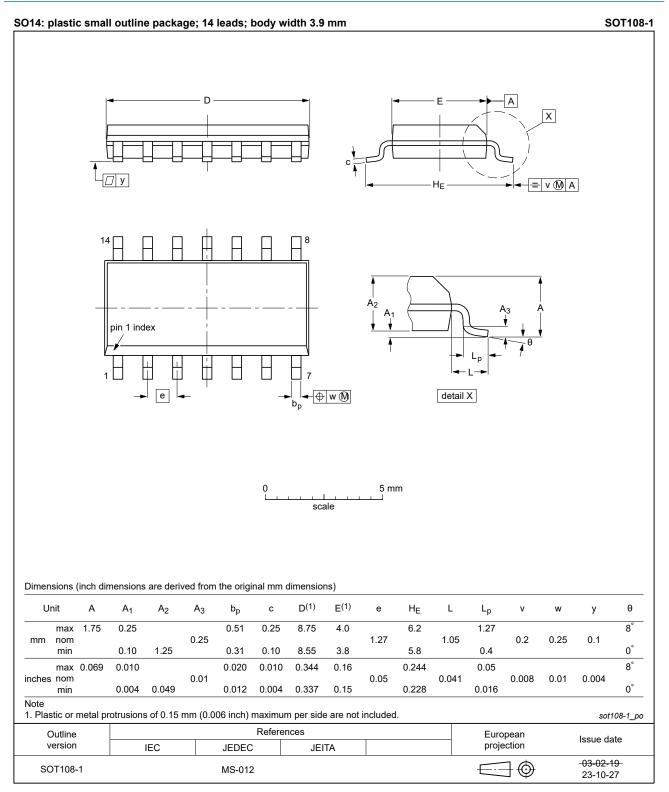


Fig. 11. Package outline SOT108-1 (SO14)

74LVCU04A-Q100

Hex unbuffered inverter

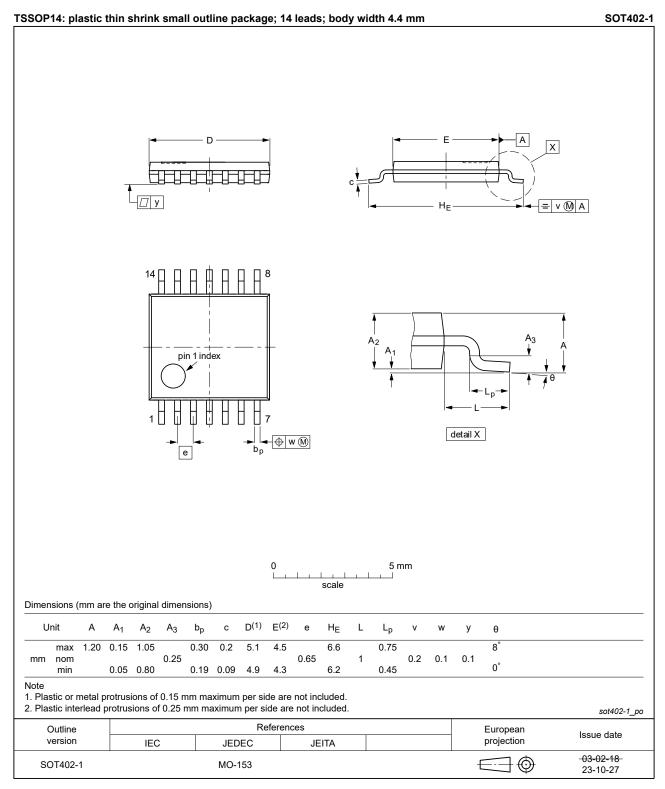


Fig. 12. Package outline SOT402-1 (TSSOP14)

13. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 10. Revision history								
Document ID	Release date	Data sheet status	Change notice	Supersedes				
74LVCU04A_Q100 v.5	20250120	Product data sheet	-	74LVCU04A_Q100 v.4				
Modifications:	• <u>Table 6</u> : V _O	<u>Table 6</u> : $V_{OL(max)}$ at T_{amb} = +125 °C and V_{CC} = 2.7 V changed to 0.6 V. (Errata)						
74LVCU04A_Q100 v.4	20240228	Product data sheet	-	74LVCU04A_Q100 v.3				
Modifications:	• <u>Fig. 11, Fig.</u> and MO-15	÷	P package outlin	e drawings to JEDEC MS-012				
74LVCU04A_Q100 v.3	20230830	Product data sheet	-	74LVCU04A_Q100 v.2				
Modifications:	 <u>Section 1</u> u <u>Section 2</u>: E 	pdated. SD specification updated a	according to the la	atest JEDEC standard.				
74LVCU04A_Q100 v.2	20210331	Product data sheet	-	74LVCU04A_Q100 v.1				
Modifications:	guidelines c Legal texts Section 1 al	guidelines of Nexperia.						
74LVCU04A_Q100 v.1	20160921	Product data sheet	-	-				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications — This Nexperia product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or

74LVCU04A-Q100

Hex unbuffered inverter

equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <u>http://www.nexperia.com/profile/terms</u>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Hex unbuffered inverter

Contents

1. General description	1
2. Features and benefits	1
3. Ordering information	1
4. Functional diagram	2
5. Pinning information	2
5.1. Pinning	2
5.2. Pin description	2
6. Functional description	3
7. Limiting values	3
8. Recommended operating conditions	3
9. Static characteristics	4
10. Dynamic characteristics	5
10.1. Waveforms and test circuit	5
11. Application information	7
12. Package outline	8
13. Abbreviations	10
14. Revision history	10
15. Legal information	11

© Nexperia B.V. 2025. All rights reserved

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 20 January 2025

单击下面可查看定价,库存,交付和生命周期等信息

>>Nexperia(安世)