

PDTB143XU

50 V, 500 mA PNP resistor-equipped transistor

8 April 2024

Product data sheet

1. General description

PNP Resistor-Equipped Transistor (RET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTD143XU

2. Features and benefits

- 500 mA output current capability
- Reduces pick and place costs •
- Built-in bias resistors .
- ±10 % resistor ratio tolerance
- Simplifies circuit design
- Reduces component count •
- High temperature applications up to 175 °C
- AEC-Q101 qualified

3. Applications

- Cost-saving alternative for BC807 series in digital applications
- Control of IC inputs •
- Switching loads •

4. Quick reference data

Symbol	Parameter	Conditions	M	lin	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base	-		-	-50	V
lo	output current		-		-	-500	mA
R1	bias resistor 1 (input)	T _{amb} = 25 °C	3.	.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio	=	1.	.91	2.13	2.34	

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	G	GND (emitter)		
3	0	output (collector)		
			1 🗌 🔄 2 SC-70 (SOT323)	sym003

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PDTB143XU	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	<u>SOT323</u>

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTB143XU	ZM%

[1] % = placeholder for manufacturing site code

8. Limiting values

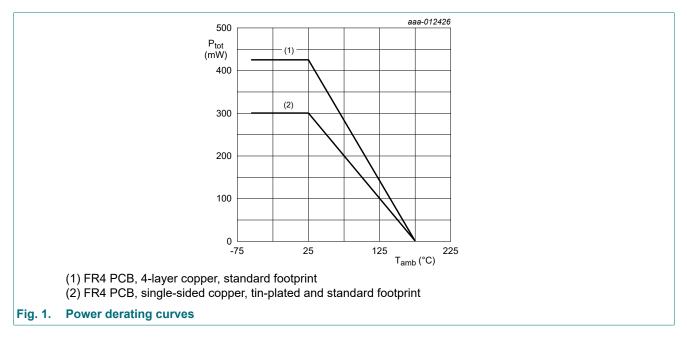
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-50	V
V _{EBO}	emitter-base voltage	open collector		-	-7	V
VI	input voltage			-30	7	V
I _O	output current			-	-500	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
		T _{amb} ≤ 25 °C	[2]	-	425	mW
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-55	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

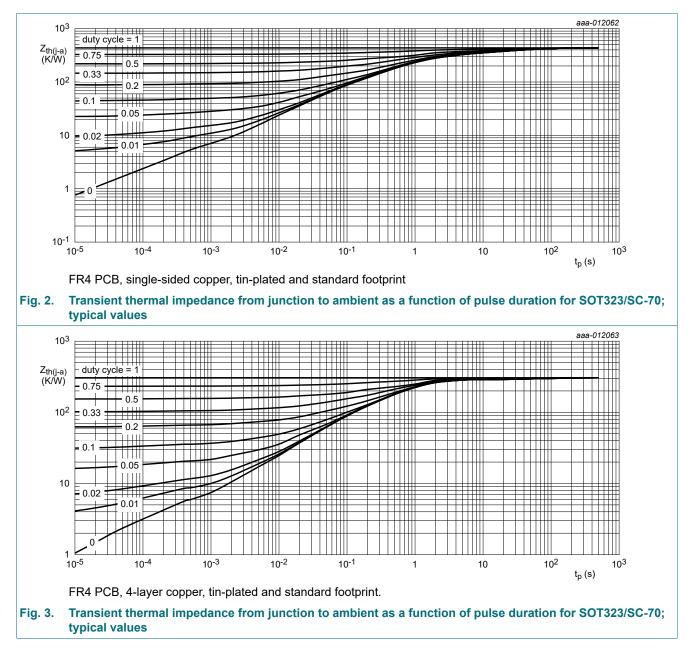


9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	-	500	K/W
	junction to ambient		[2]	-	-	353	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

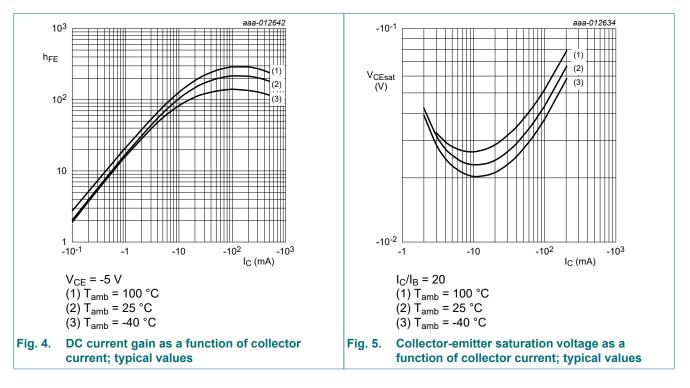
[2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.



10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -40 V; I _E = 0 A; T _{amb} = 25 °C		-	-	-100	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -50 V; I _B = 0 A; T _{amb} = 25 °C		-	-	-0.5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	-0.6	mA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -50 mA; T _{amb} = 25 °C		70	-	-	
V _{CEsat}	collector-emitter saturation voltage	I_{C} = -50 mA; I_{B} = -2.5 mA; T_{amb} = 25 °C		-	-	-100	mV
V _{I(off)}	off-state input voltage	V_{CE} = -5 V; I _C = -100 µA; T _{amb} = 25 °C		-0.5	-0.75	-1.1	V
V _{I(on)}	on-state input voltage	V_{CE} = -0.3 V; I _C = -20 mA; T _{amb} = 25 °C		-1	-1.4	-2	V
R1	bias resistor 1 (input)	T _{amb} = 25 °C		3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio			1.91	2.13	2.34	
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	11	-	pF
f _T	transition frequency	V _{CE} = -5 V; I _C = -50 mA; f = 100 MHz; T _{amb} = 25 °C	[1]	-	140	-	MHz

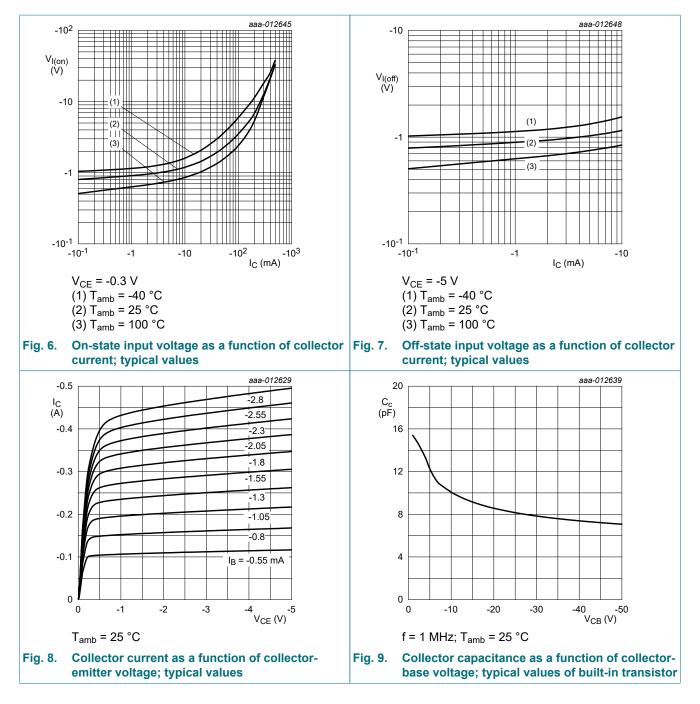
[1] Characteristics of built-in transistor.



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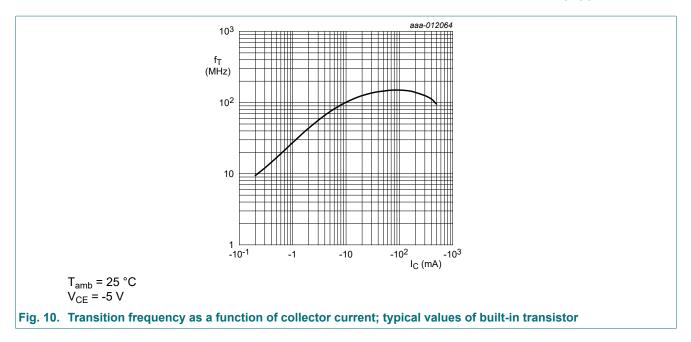
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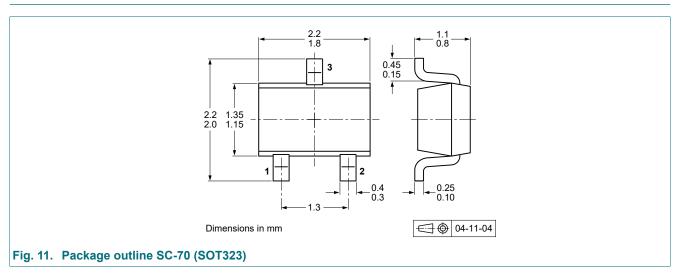


11. Test information

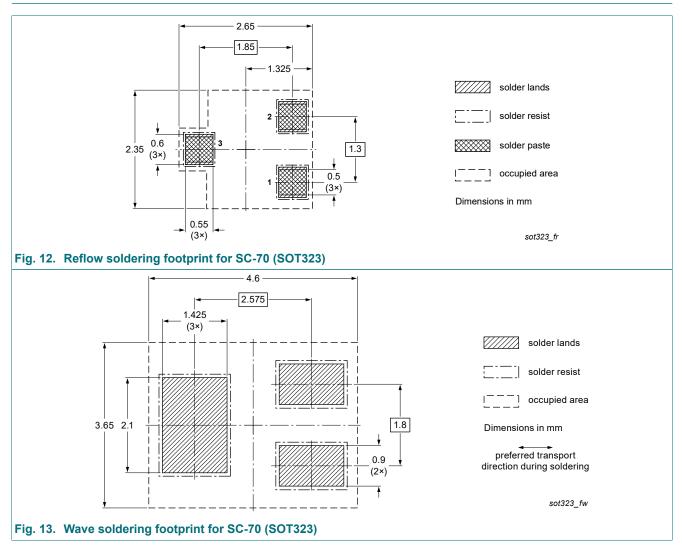
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PDTB143XU v.2	20240408	Product data sheet	-	PDTB1XXXU_SER v.1			
Modifications:	Family data sheet reduced to single type data sheet.						
PDTB1XXXU_SER v.1	20140506	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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