

1. General description

Symmetrical bidirectional ElectroStatic Discharge (ESD) protection diode array, part of the TrEOS protection family. This device is housed in a DFN0603-3 (SOT8013) leadless ultra small Surface-Mounted Device (SMD) package designed to protect two signal lines from the damage caused by ESD and other transients.

2. Features and benefits

- Bidirectional ESD protection of two lines
- Very low diode capacitance C_d = 0.31 pF
- Extremely low clamping to protect sensitive I/Os
- · Extremely low-inductance protection path to ground
- · Extremely symmetrical layout
- ESD protection up to ±25 kV according to IEC 61000-4-2; surge robustness 11.0 A 8/20 μs
- Ultra small SMD package
- Placed on one differential line pair, almost no extra PCB space demand for protection

3. Applications

- USB3.2 and HDMI2.1 data lines
- Cellular handsets and accessories
- Portable electronics
- Communication systems
- Computers and peripherals

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{RWM}	reverse standoff voltage			-4	-	4	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	0.31	0.37	pF

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)[1]	1 2 3	
3	K3	cathode (diode 3)		K2 aaa-030288
			Transparent top view DFN0603-3 (SOT8013)	

[1] recommended for GND connection.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD4V0Z2BCDF	DFN0603-3	DFN0603-3; plastic, ultra small and leadless full encapsulated package; 3 terminals; 0.225 mm pitch; 0.63 mm x 0.33 mm x 0.25 mm body	SOT8013

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD4V0Z2BCDF	Z

8. Limiting values

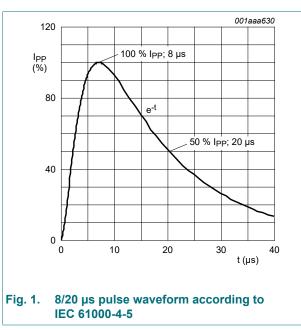
Table 5. Limiting values

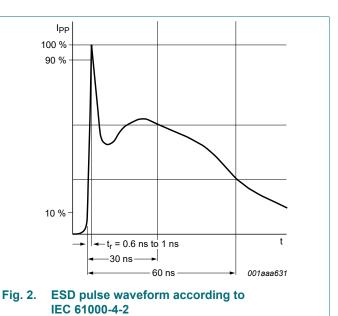
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{RWM}	reverse standoff voltage			-4	4	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-11	11	А
T _{amb}	ambient temperature			-40	125	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	um ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[2]	-25	25	kV
	voltage	IEC 61000-4-2; air discharge	[2]	-25	25	kV

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Device stressed with ten non-repetitive ESD pulses.





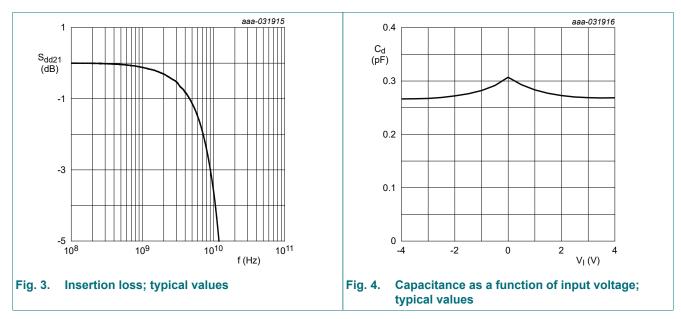
PESD4V0Z2BCDF

9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		-	7.4	-	V
I _{RM}	reverse leakage current	V _{RWM} = 5 V; T _{amb} = 25 °C		-	1	30	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	0.31	0.37	pF
		f = 1 MHz; V _R = 1.5 V; T _{amb} = 25 °C		-	0.28	-	pF
V _{CL}	clamping voltage	I _{PPM} = 11 A; 8/20 μs; T _{amb} = 25 °C	[1]	-	5.8	-	V
		I _{PP} = 8 A; TLP; t _p = 100 ns	[2]	-	4.4	-	V
		I _{PP} = 16 A; TLP; t _p = 100 ns; T _{amb} = 25 °C	[2]	-	5.7	-	V
R _{dyn}	dynamic resistance	I _R = 10 A; T _{amb} = 25 °C	[2]	-	0.19	-	Ω
		I _R = -10 A; T _{amb} = 25 °C	[2]	-	0.19	-	Ω
f _{-3dB}	-3 dB cut-off frequency	T_{amb} = 25 °C; normalized to attenuation at 1 MHz		-	> 9	-	GHz

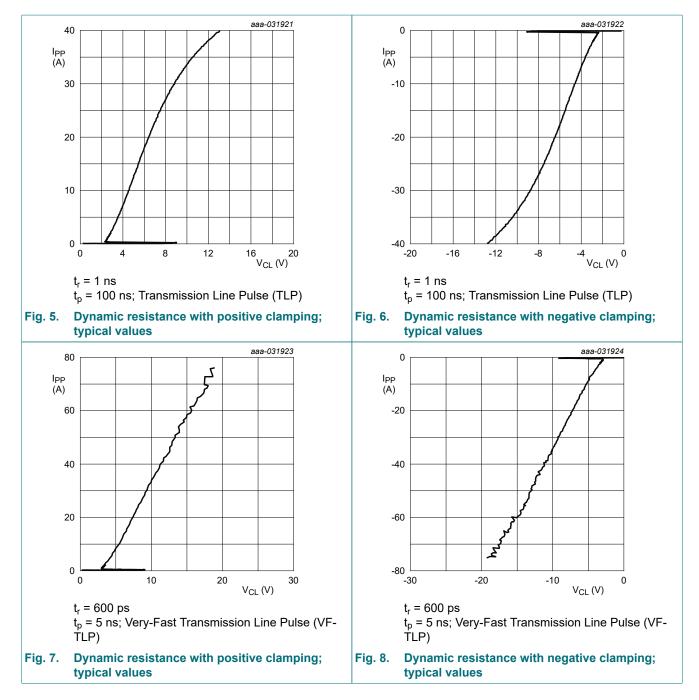
[1]

Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5. Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008. [2]



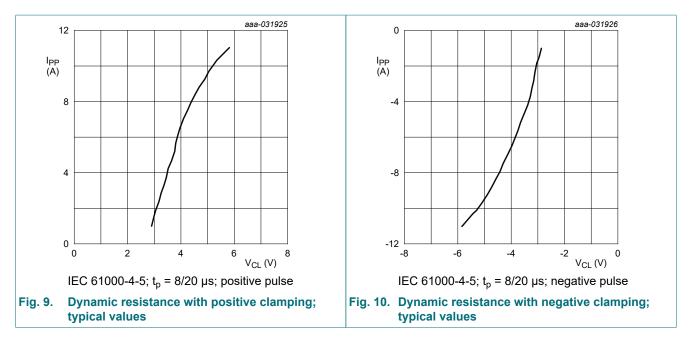
PESD4V0Z2BCDF

Extremely low clamping low capacitance ESD protection



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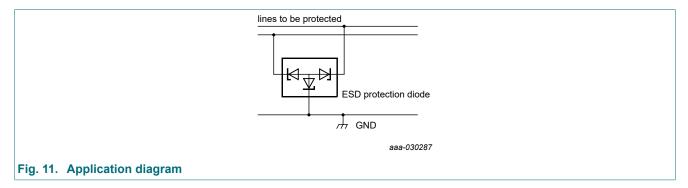
Extremely low clamping low capacitance ESD protection



10. Application information

The device is designed for the protection of two signal lines from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both, positive and negative with respect to ground.

The device uses an advanced clamping structure showing a negative dynamic resistance. This snap-back behavior strongly reduces the clamping voltage system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid keeping the ESD protection device snap-back state after exceeding breakdown voltage (due to an ESD pulse for instance).

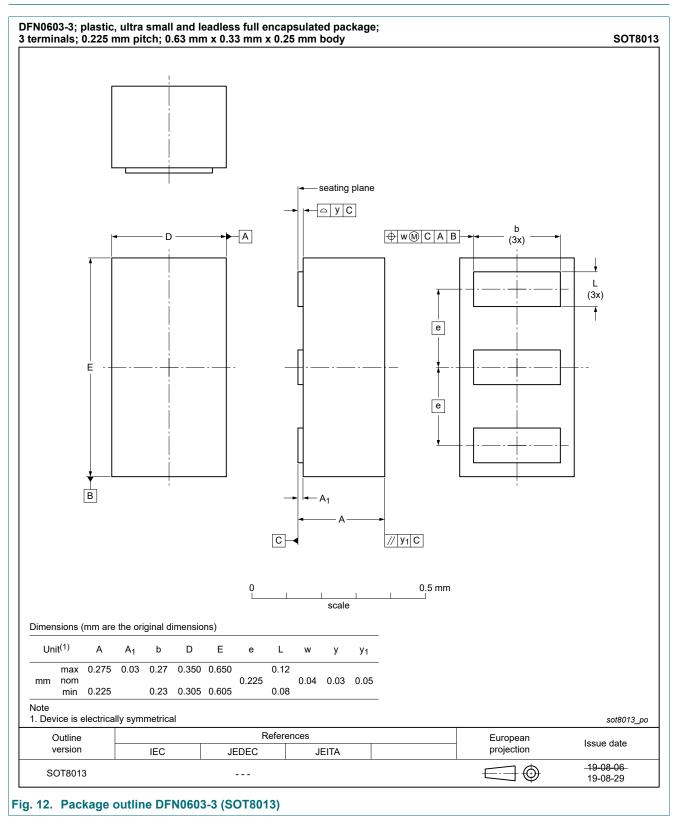


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

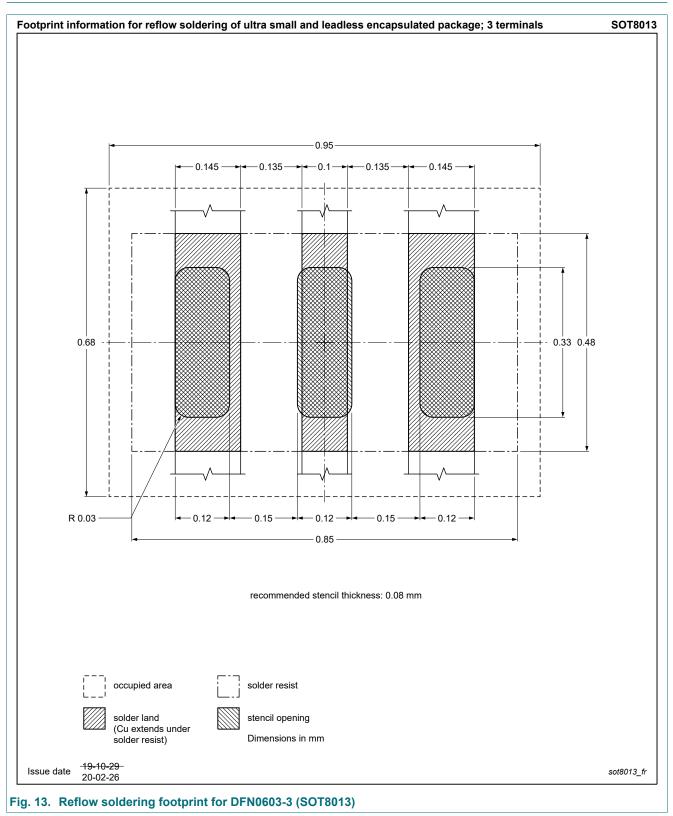
11. Package outline



PESD4V0Z2BCDF

Extremely low clamping low capacitance ESD protection

12. Soldering



13. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD4V0Z2BCDF v.1	20200817	Product data sheet	-	-		

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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Product data sheet

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