## 1. General description

NPN/PNP general-purpose transistor pair in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- · Low collector capacitance
- Low collector-emitter saturation voltage
- · Closely matched current gain
- Reduces number of components and board space
- No mutual interference between the transistors

## 3. Applications

· General-purpose switching and amplification

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor; for the PNP transistor with negative polarity							
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C		200	-	450	



### 45 V, 100 mA NPN/PNP general-purpose transistor

## 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1	6 5 4	
3	C2	collector TR2		(TR1 TR2)
4	E2	emitter TR2		
5	B2	base TR2	∐1 ∐2 ∐3	
6	C1	collector TR1	TSSOP6 (SOT363)	sym139

## 6. Ordering information

### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
BC847BPN		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363		

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
BC847BPN	13%

[1] % = placeholder for manufacturing site code

### 45 V, 100 mA NPN/PNP general-purpose transistor

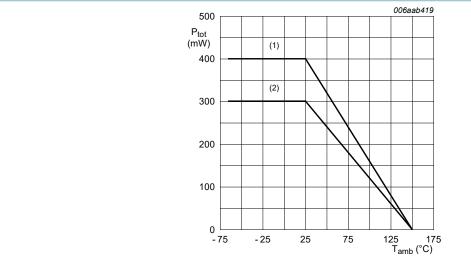
## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or; for the PNP transistor wit	h negative polarity			'	
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current	-		-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	220	mW
			[2]	-	250	mW
Per device			,	'		
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
			[2]	-	400	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided, 35 μm copper, tin-plated, mounting pad for collector 1 cm².



- (1) FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>
- (2) FR4 PCB, single-sided, 35  $\mu m$  copper, tin-plated and standard footprint

Fig. 1. Per device: Power derating curves SOT363 (SC-88)

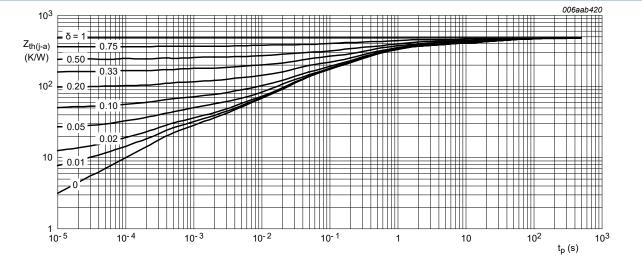
#### 45 V, 100 mA NPN/PNP general-purpose transistor

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

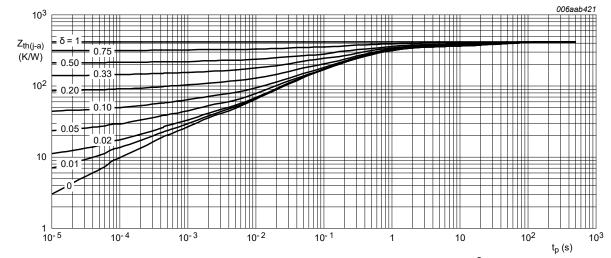
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	tor						
R <sub>th(j-a)</sub> thermal resistance from junction to ambient	thermal resistance from	in free air [1	[1]	-	-	568	K/W
		[2]	-	-	500	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	230	K/W
Per device	'						
""(J-"a)	thermal resistance from	in free air	[1]	-	-	416	K/W
	junction to ambient		[2]	-	-	313	K/W

- [1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided, 35 μm copper, tin-plated, mounting pad for collector 1 cm².



FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>

Fig. 3. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

BC847BPN

### 45 V, 100 mA NPN/PNP general-purpose transistor

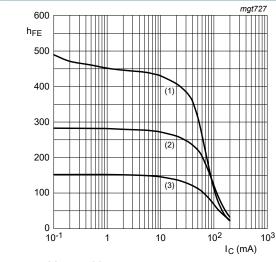
## 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	or; for the PNP transistor v	with negative polarity				
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$I_C = 100 \ \mu\text{A}; \ I_E = 0 \ \text{A}; \ T_{amb} = 25 \ ^{\circ}\text{C}$	50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	45	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	$I_C = 0 \text{ A}; I_E = 100 \mu\text{A}; T_{amb} = 25 \text{ °C}$	5	-	-	V
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	15	nA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	200	-	450	
OLSat	collector-emitter	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA; T <sub>amb</sub> = 25 °C	-	-	100	mV
	saturation voltage	$I_C$ = 100 mA; $I_B$ = 5 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; $T_{amb}$ = 25 °C	-	-	300	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	755	-	mV
$V_{BE}$	base-emitter voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	580	655	700	mV
			600	655	750	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz;	-	-	1.5	pF
		T <sub>amb</sub> = 25 °C	-	-	2.2	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	11	-	pF
			-	10	-	pF
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V; } I_{C} = 10 \text{ mA; } f = 100 \text{ MHz;}$ $T_{amb} = 25 \text{ °C}$	100	-	-	MHz

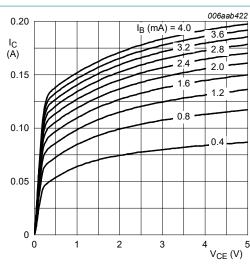
**BC847BPN Nexperia** 

#### 45 V, 100 mA NPN/PNP general-purpose transistor



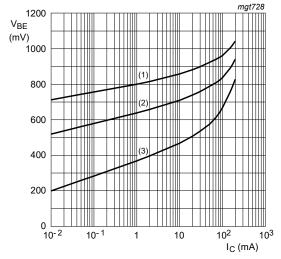
V<sub>CE</sub> = 5 V (1) T<sub>amb</sub> = 150 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = -55 °C

NPN transistor: DC current gain as a function of Fig. 4. collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 5. NPN transistor: Collector current as a function of collector-emitter voltage; typical values



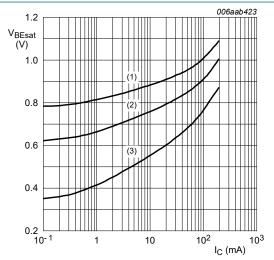
 $V_{CE} = 5 V$ 

(1)  $T_{amb}$  = -55 °C

(2) T<sub>amb</sub> = 25 °C

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

Fig. 6. Base-emitter voltage as a function of collector current; typical values



 $I_{\rm C}/I_{\rm B} = 20$ 

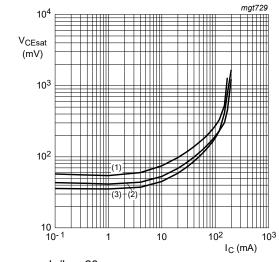
(1)  $T_{amb}$  = -55 °C

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

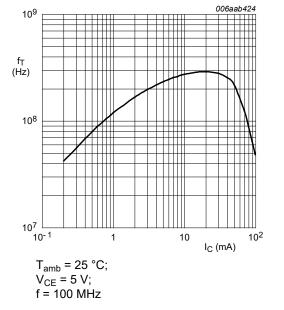
Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

#### 45 V, 100 mA NPN/PNP general-purpose transistor

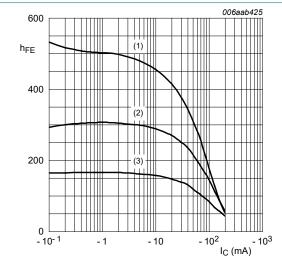


I<sub>C</sub>/I<sub>B</sub> = 20 (1) T<sub>amb</sub> = 150 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = -55 °C

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values



Transition frequency as a function of collector Fig. 9. current; typical values



 $V_{CE}$  = -5 V

(1)  $T_{amb} = 150 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = -55 \, ^{\circ}C$ 

Fig. 10. PNP transistor: DC current gain as a function of collector current; typical values

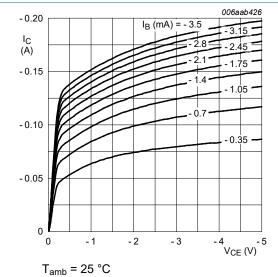


Fig. 11. PNP transistor: Collector current as a function of collector-emitter voltage; typical values

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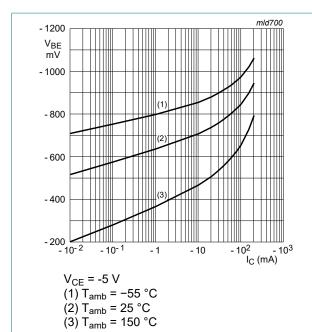
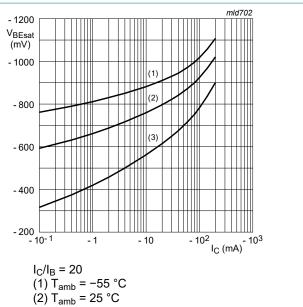


Fig. 12. PNP TR2: Base-emitter voltage as a function of collector current; typical values



 $I_{C}/I_{B} = 20$ (1)  $T_{amb} = -55 \,^{\circ}C$ (2)  $T_{amb} = 25 \,^{\circ}C$ (3)  $T_{amb} = 150 \,^{\circ}C$ 

Fig. 13. PNP TR2: Base-emitter saturation voltage as a function of collector current; typical values

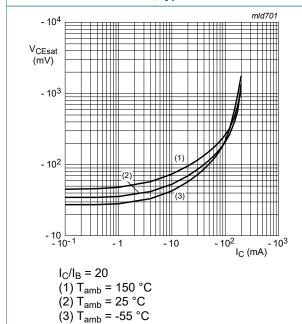
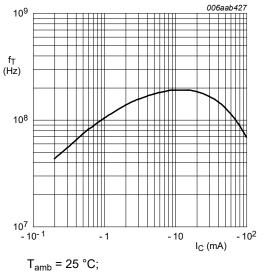


Fig. 14. PNP TR2: Collector-emitter saturation voltage as a function of collector current; typical values

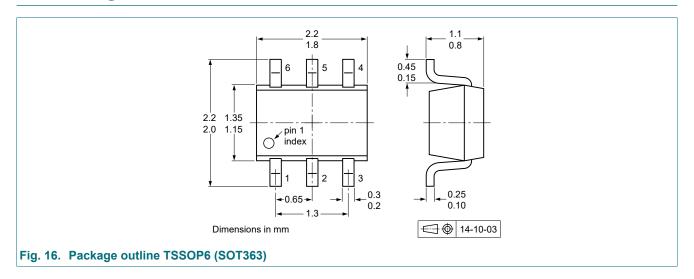


 $V_{CE} = -5 V;$ f = 100 MHz

Fig. 15. PNP transistor: Transition frequency as a function of collector current; typical values

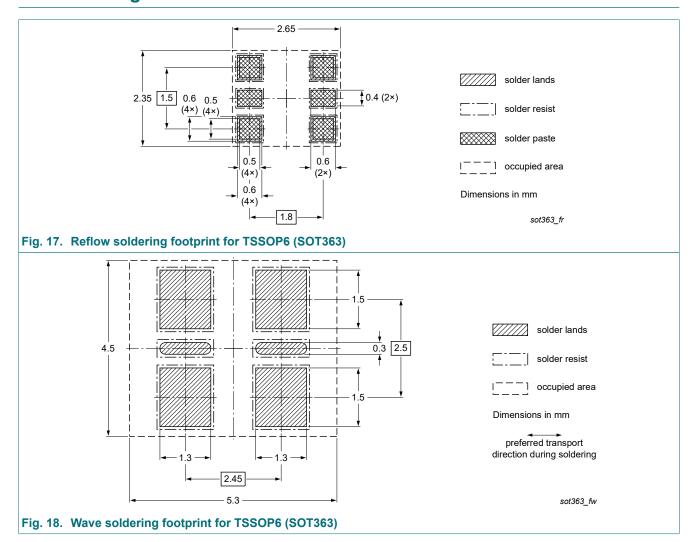
### 45 V, 100 mA NPN/PNP general-purpose transistor

# 11. Package outline



## 45 V, 100 mA NPN/PNP general-purpose transistor

## 12. Soldering



### 45 V, 100 mA NPN/PNP general-purpose transistor

# 13. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC847BPN v.5	20220701	Product data sheet	-	BC847BPN_4
Modification:	automotive (-Q	nged to non-automotive qualit l) product alternative(s). lation removed.	fication. Please refer t	o nexperia.com for
BC847BPN_4	20090218	Product data sheet	-	BC847BPN_3
BC847BPN_3	20011026	Product specification	-	BC847BPN_2
BC847BPN_2	19990426	Preliminary specification	-	BC847BPN_1
BC847BPN_1	19970709	Preliminary specification	-	-

## 14. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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	Features and benefits

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