

60 V, 1 A NPN medium power transistors Rev. 2 — 1 July 2022

### 1. General description

NPN medium power transistor series in a small SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

#### Table 1. Product overview

Type number	Package		NPN complement
	Nexperia	JEITA	
BCP55-Q	SOT223	SC73	BCP52-Q
BCP55-10-Q			BCP52-10-Q
BCP55-16-Q			BCP52-16-Q

### 2. Features and benefits

- High current •
- Three current gain selections
- High power dissipation capability
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- Linear voltage regulators
- Power management
- Low-side switches
- MOSFET drivers
- Battery-driven devices .
- Amplifiers

### 4. Quick reference data

#### Table 2. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	60	V
I <sub>C</sub>	collector current			-	-	1	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	2	А
h <sub>FE</sub>	DC current gain	·					
	BCP55-Q	$V_{CE}$ = 2 V; I <sub>C</sub> = 150 mA T <sub>amb</sub> = 25 °C	[1]	63	-	250	
	BCP55-10-Q		[1]	63	-	160	
	BCP55-16-Q		[1]	100	-	250	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# nexperia

### 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	E	emitter		B-t
4	С	collector	<u> </u>	E
				sym123

### 6. Ordering information

Table 4. Ordering information									
Type number	e number Package								
	Name	Description	Version						
BCP55-Q	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223						
BCP55-10-Q									
BCP55-16-Q									

### 7. Marking

Table 5. Marking	
Type number	Marking code
BCP55-Q	BCP55
BCP55-10-Q	BCP55 /10
BCP55-16-Q	BCP55 /16

### 8. Limiting values

#### Table 6. Limiting values

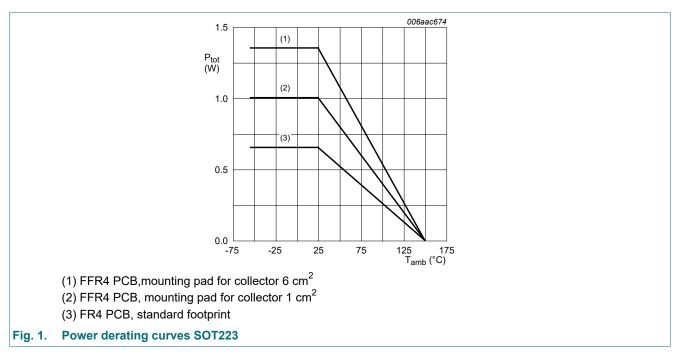
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
l <sub>C</sub>	collector current			-	1	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	2	А
I <sub>B</sub>	base current			-	0.3	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	0.3	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



### 9. Thermal characteristics

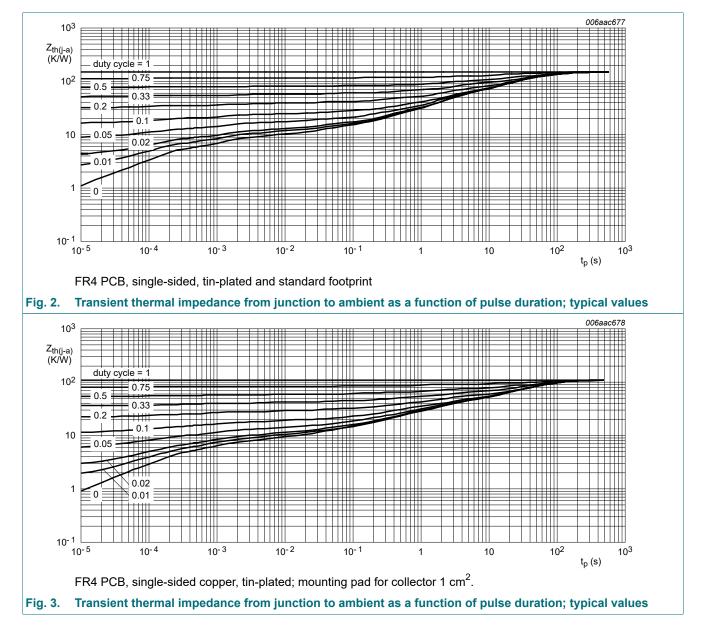
#### **Table 7. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]	-	-	125	K/W
			[3]	-	-	93	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	16	K/W

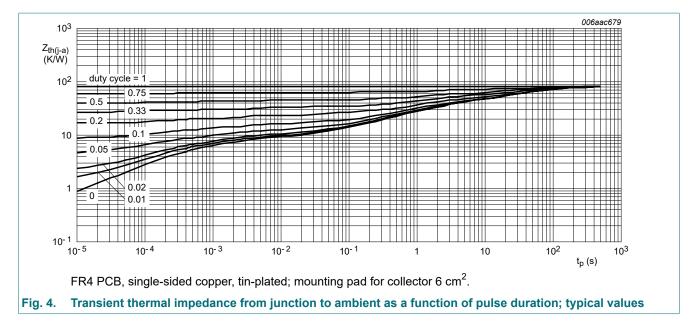
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; monting pad for collector 1 cm<sup>2</sup>

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated; monting pad for collector 6 cm<sup>2</sup>.



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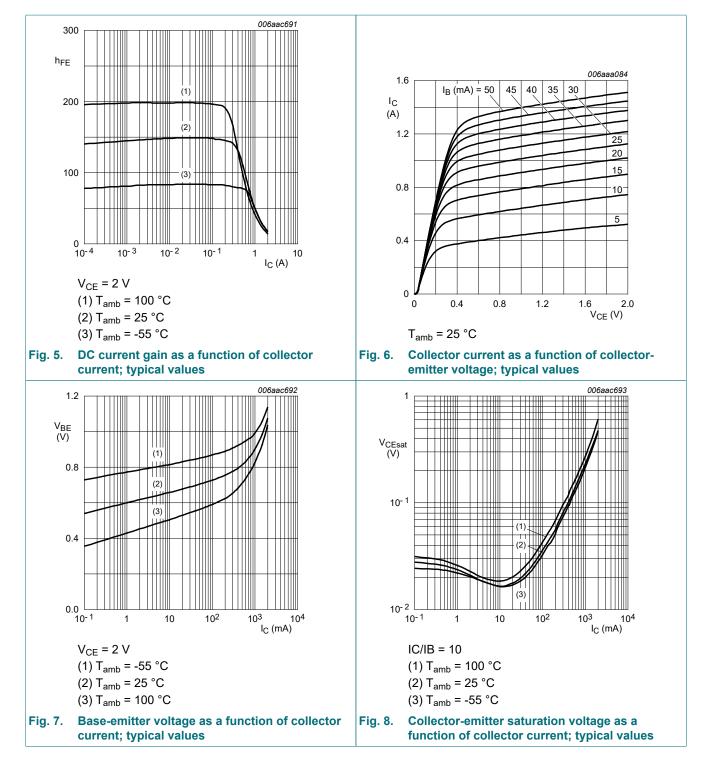
### **10. Characteristics**

#### Table 8. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 ; T <sub>amb</sub> = 25 °C		60	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_{C} = 2 \ \mu A; I_{B} = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		60	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>C</sub> = 0 A; I <sub>E</sub> = 100 μA	I <sub>C</sub> = 0 A; I <sub>E</sub> = 100 μA		-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	10	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain						
	BCP55-Q	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA; T <sub>amb</sub> = 25 °C	[1]	63	-	250	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	40	-	-	
	BCP55-10-Q	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA; T <sub>amb</sub> = 25 °C	[1]	63	-	160	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	40	-	-	
	BCP55-16-Q	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA; T <sub>amb</sub> = 25 °C	[1]	100	-	250	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	40	-	-	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C	[1]	63	-	-	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; T <sub>amb</sub> = 25 °C	[1]	-	-	0.5	V
V <sub>BE</sub>	base-emitter voltage	$V_{CE}$ = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	[1]	-	-	1	V
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; \text{ I}_{E} = \text{i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$		-	6	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		100	180	-	MHz

[1] pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

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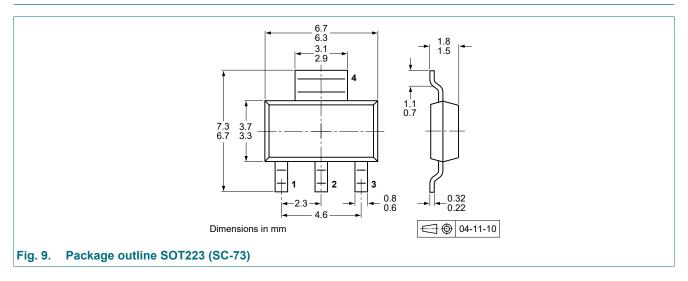


### **11. Test information**

### 11.1. Quality information

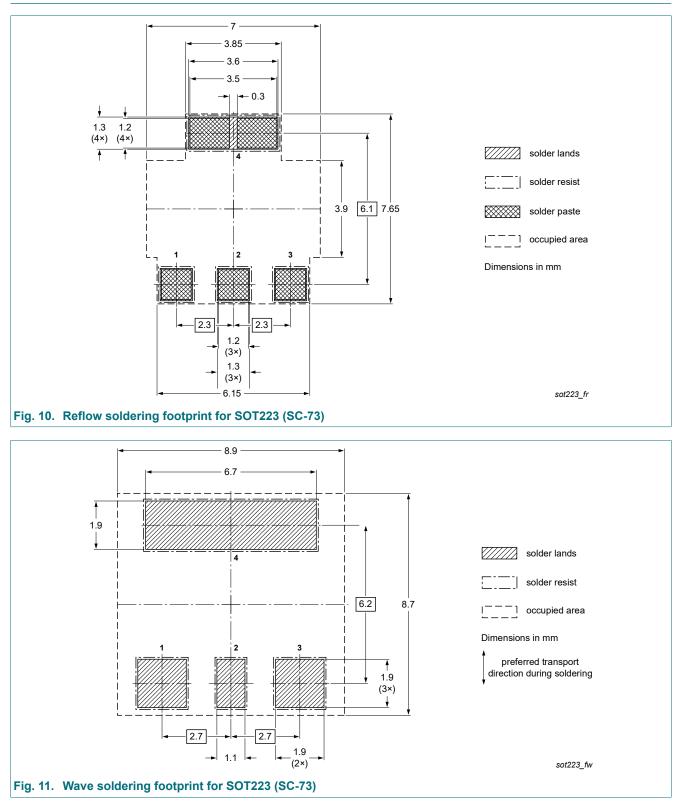
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 12. Package outline



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### 13. Soldering



### 14. Revision history

Table 9. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCP55-Q_SER v.2	20220701	Product data sheet	-	BCP55-Q_SER v.1
Modifications:	Characterist	ics at V <sub>(BR)CEO</sub> : Conditio	ons corrected	
BCP55-Q_SER v.1	20210623	Product data sheet	-	-

BCP55-Q\_SER

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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