

80 V, 1 A NPN medium power transistors Rev. 2 — 29 June 2022

1. General description

NPN medium power transistors in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		NPN comlement
	Nexperia	JEDEC	
BCP56T-Q	SOT223	SC-73	BCP53T-Q
BCP56-10T-Q			BCP53-10T-Q
BCP56-16T-Q			BCP53-16T-Q

2. Features and benefits

- + High collector current capability I_C and I_{CM}
- Three current gain selections
- High power dissipation capability
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Linear voltage regulators
- MOSFET drivers
- High-side switches
- Power management
- Amplifiers

4. Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	80	V
I _C	collector current			-	-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	2	А
h _{FE}	DC current gain						
	BCP56T-Q	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
	BCP56-10T-Q		[1]	63	-	160	
	BCP56-16T-Q		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	E	emitter		B-t
4	С	collector	<u> </u>	E
				sym123

6. Ordering information

Table 4. Ordering information								
Type number	Package	Package						
	Name	Description	Version					
BCP56T-Q	SC-73	plastic, surface-mounted package with increased heatsink;	SOT223					
BCP56-10T-Q		4 leads						
BCP56-16T-Q								

7. Marking

Table 5. Marking	
Type number	Marking code
BCP56T-Q	BCP56T
BCP56-10T-Q	P5610T
BCP56-16T-Q	P5616T

8. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		100	V
V _{CEO}	collector-emitter voltage	open base		-	80	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	А
I _B	base current			-	0.2	А
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	0.3	А
P _{tot} total power dissipation	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.6	W
			[2]	-	1	W
			[3]	-	1.3	W
			[4]	-	1.3	W
			[5]	-	1.8	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

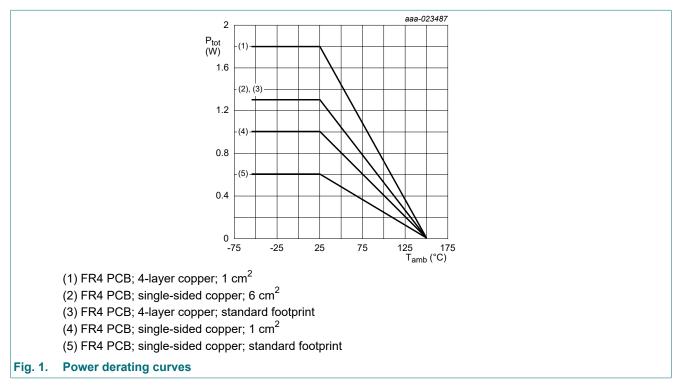
[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm².

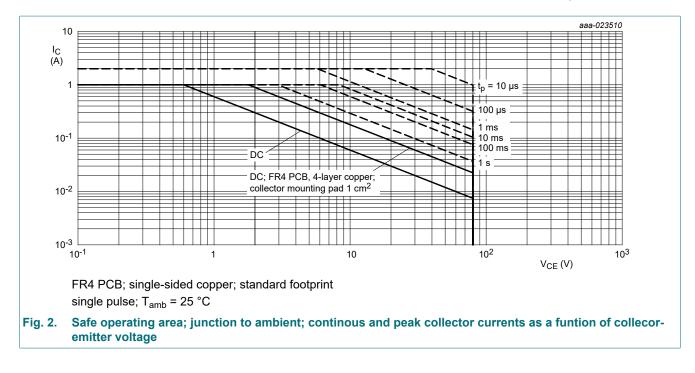
[3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².

[4] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint.

[5] Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm.²



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9. Thermal characteristics

Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	(j-a) thermal resistance from junction to ambient	in free air	[1]	-	-	209	K/W
			[2]			125	K/W
			[3]			97	K/W
			[4]	-	-	97	K/W
			[5]	-	-	70	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	18	K/W

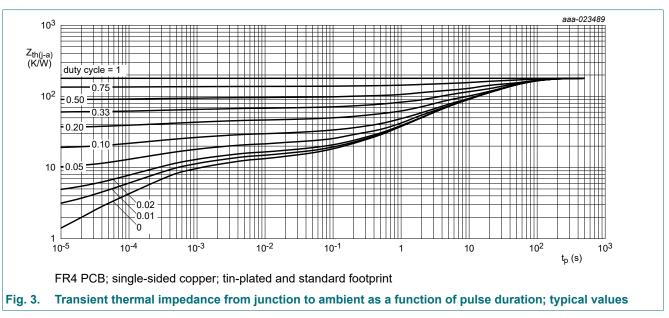
Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint. [1]

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm²₋ [2]

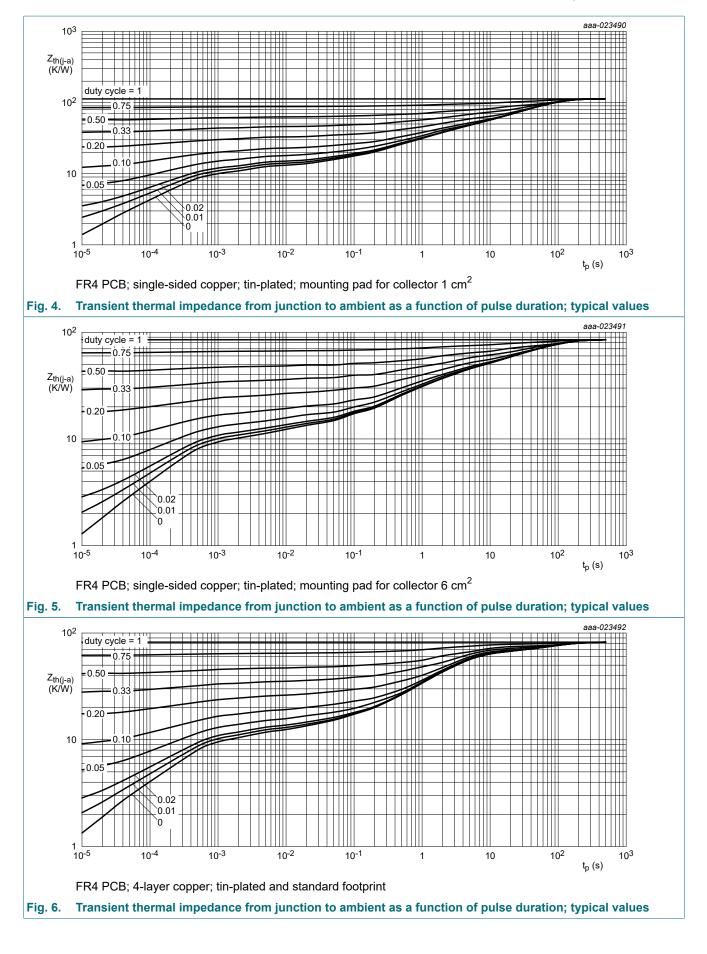
Device mounted on an FR4 Printed-Circuit-Board (PCB), single-sided copper; tin-plated; mounting pad for collector 1 cm². [3]

Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated and standard footprint. [4]

Device mounted on an FR4 Printed-Circuit-Board (PCB); 4-layer copper; tin-plated; mounting pad for collector 1 cm². [5]

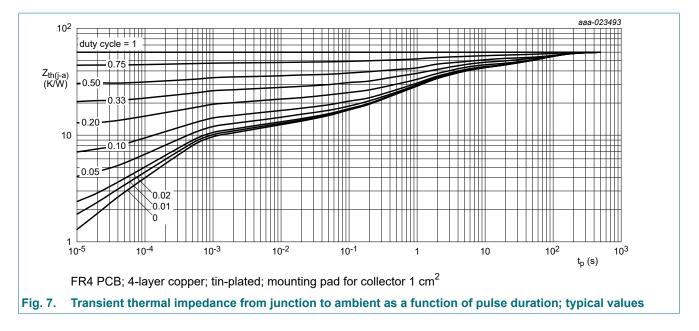


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10. Characteristics

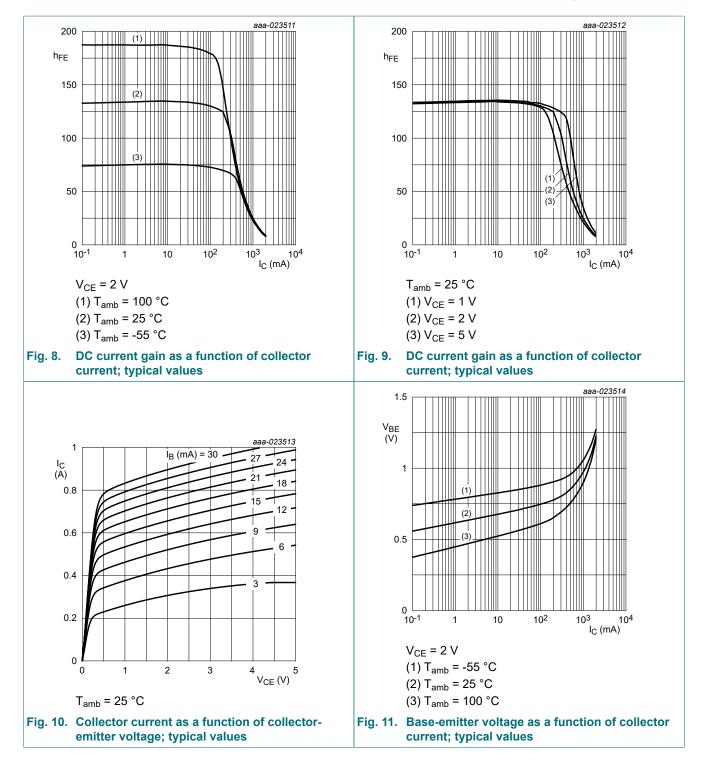
Table 8. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		100	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A		80	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A		5	-	-	V
I _{CBO}	collector-base	V _{CB} = 30 V; I _E = 0 A		-	-	100	nA
	cut-off current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	100	nA
h _{FE}	DC current gain						
	BCP56T-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCP56-10T-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	160	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
	BCP56-16T-Q	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA	[1]	100	-	250	
		V _{CE} = 2 V; I _C = 500 mA	[1]	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	500	mV
V _{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA	[1]	-	-	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	4.5	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz		100	155	-	MHz

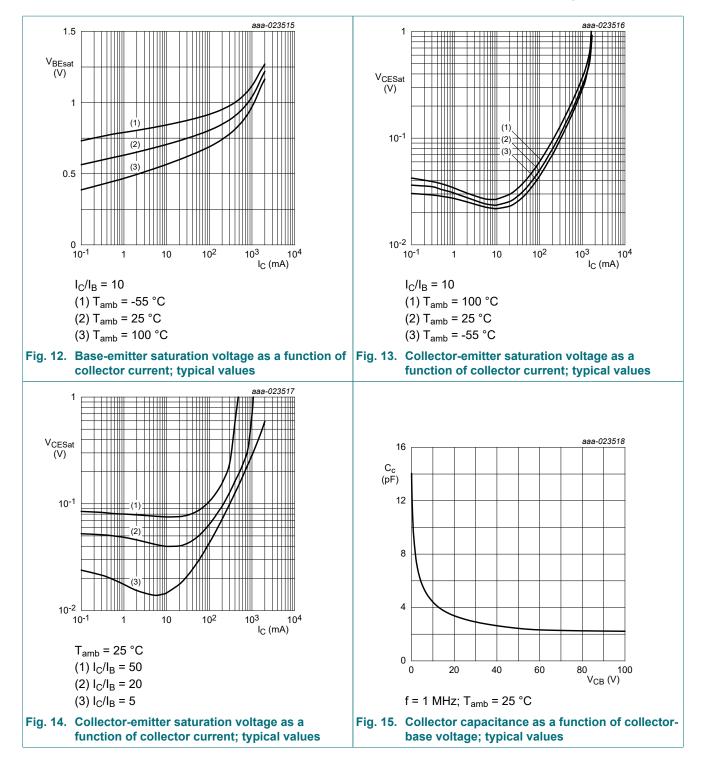
[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$

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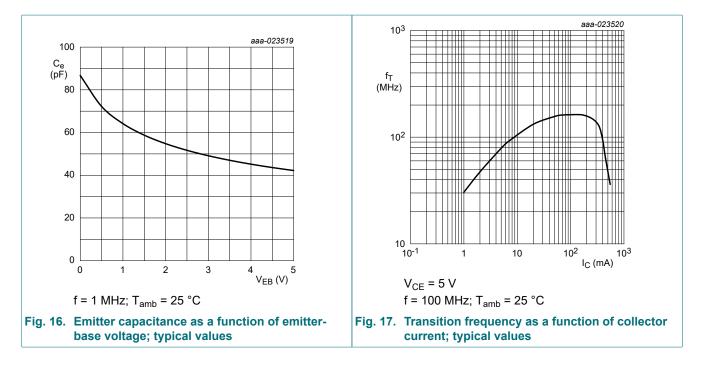


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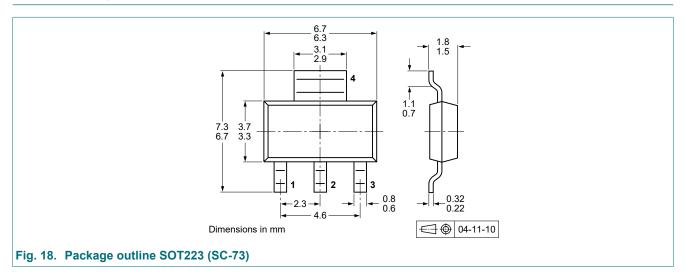


11. Test information

11.1. Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

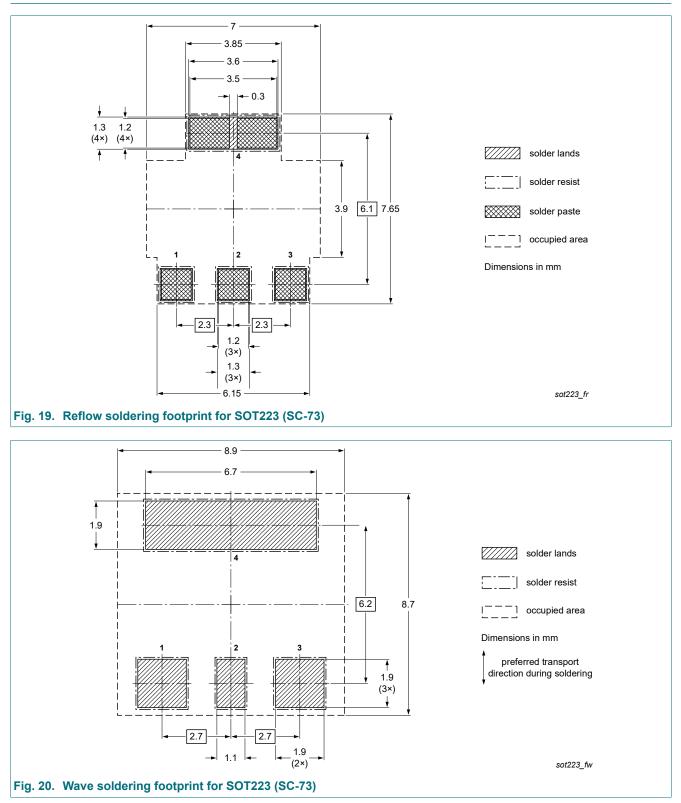
12. Package outline



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13. Soldering



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14. Revision history

Table 9. Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP56T-Q_SER v.1	20220629	Product data sheet	-	BCP56T-Q_SER v.1
Modifications:	Characteristics	at V _{(BR)CEO} : Conditions c	orrected	
BCP56T-Q_SER v.1	20210623	Product data sheet	-	-

BCP56T-Q_SER

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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