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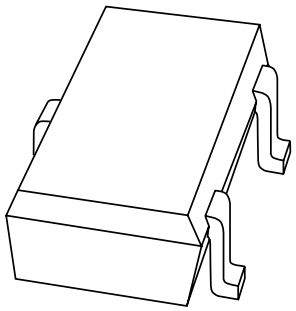
Kind regards,

Team Nexperia

DATA SHEET



PMST5088; PMST5089
NPN general purpose transistors

Product data sheet
Supersedes data of 1997 May 22

1999 Apr 22

NPN general purpose transistors PMST5088; PMST5089

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 30 V).

APPLICATIONS

- Low-noise input stages in audio equipment.

DESCRIPTION

NPN transistor in a SC-70; SOT323 plastic package.

MARKING

TYPE NUMBER	MARKING CODE(1)
PMST5088	∗1Q
PMST5089	∗1R

Note

1. ∗ = - : Made in Hong Kong.
   ∗ = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector

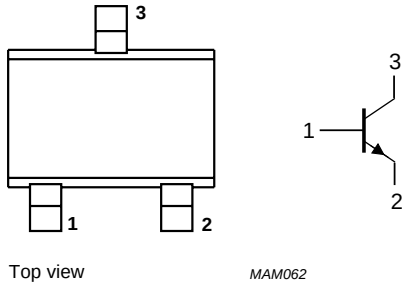


Fig.1 Simplified outline (SC-70; SOT323) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	PMST5088		–	35	V
	PMST5089		–	30	V
V_{CEO}	collector-emitter voltage	open base			
	PMST5088		–	30	V
	PMST5089		–	25	V
V_{EBO}	emitter-base voltage	open collector	–	4.5	V
I_C	collector current (DC)		–	100	mA
I_{CM}	peak collector current		–	200	mA
I_{BM}	peak base current		–	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	200	mW
T_{stg}	storage temperature		−65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		−65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

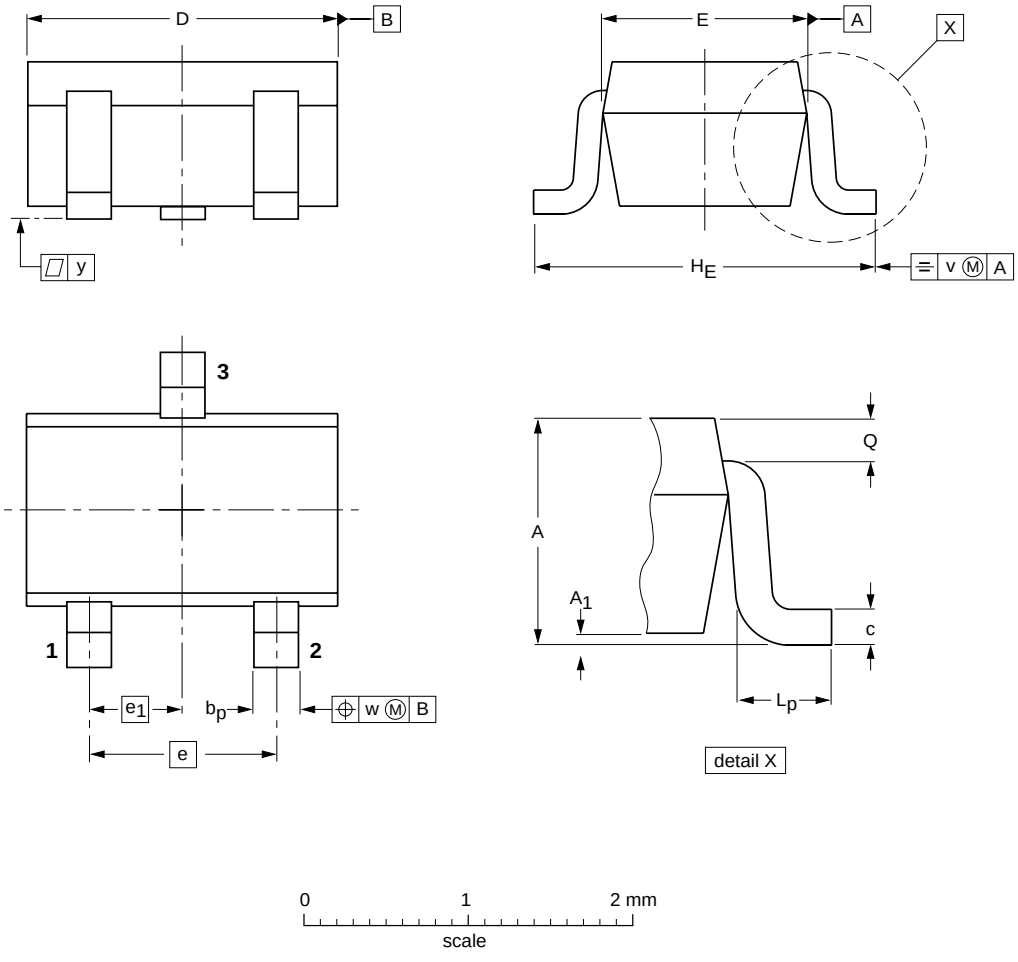
CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	I_E = 0; V_{CB} = 20 V	–	50	nA
		I_E = 0; V_{CB} = 20 V; T_j = 150 °C	–	10	μA
I_{EBO}	emitter cut-off current	I_C = 0; V_{EB} = 3 V	–	50	nA
		I_C = 0; V_{EB} = 4.5 V	–	100	nA
h_{FE}	DC current gain PMST5088	V_{CE} = 5 V			
		I_C = 0.1 mA	300	900	
		I_C = 1 mA	350	–	
		I_C = 10 mA	300	–	
	DC current gain PMST5089	V_{CE} = 5 V			
		I_C = 0.1 mA	400	1200	
		I_C = 1 mA	450	–	
		I_C = 10 mA	400	–	
V_{CEsat}	collector-emitter saturation voltage	I_C = 10 mA; I_B = 1 mA	–	500	mV
V_{BE}	base-emitter voltage	I_C = 10 mA; V_{CE} = 5 V	–	800	mV
C_c	collector capacitance	I_E = i_e = 0; V_{CB} = 5 V; f = 1 MHz	–	4	pF
C_e	emitter capacitance	I_C = i_c = 0; V_{EB} = 0.5 V; f = 1 MHz	–	12	pF
f_T	transition frequency	I_C = 10 mA; V_{CE} = 5 V; f = 100 MHz	100	–	MHz
F	noise figure PMST5088	I_C = 100 μA; V_{CE} = 5 V; R_S = 1 kΩ f = 10 Hz to 15.7 kHz	–	3	dB
	PMST5089		–	2	dB

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads **SOT323**



DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1 max	b_p	c	D	E	e	e_1	H_E	L_p	Q	v	w
mm	1.1 0.8	0.1	0.4 0.3	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT323			SC-70			97-02-28

DATA SHEET STATUS

DOCUMENT STATUS[1]	PRODUCT STATUS[2]	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NXP Semiconductors

Customer notification

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Contact information

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For sales offices addresses send e-mail to: **salesaddresses@nxp.com**

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