# **74AUP1G17**

## **Low-power Schmitt trigger**

Rev. 14 — 18 July 2023

**Product data sheet** 

### 1. General description

The 74AUP1G17 is a single buffer with Schmitt-trigger input. This device ensures very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- · High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Low static power consumption; I<sub>CC</sub> = 0.9 μA (maximum)
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 ° C to +85 ° C and -40 ° C to +125 ° C



Low-power Schmitt trigger

## 3. Ordering information

**Table 1. Ordering information** 

Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G17GW	-40 ° C to +125 ° C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G17GV	-40 ° C to +125 ° C	SC-74A	plastic surface-mounted package; 5 leads	SOT753
74AUP1G17GM	-40 ° C to +125 ° C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74AUP1G17GN	-40 ° C to +125 ° C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115
74AUP1G17GS	-40 ° C to +125 ° C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202
74AUP1G17GX	-40 ° C to +125 ° C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	SOT1226-3
74AUP1G17GX4	-40 °C to +125 °C	X2SON4	plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 × 0.6 × 0.32 mm	SOT1269-2

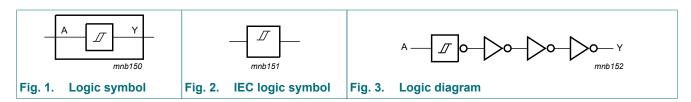
## 4. Marking

Table 2. Marking

- cabio 2: marking	
Type number	Marking code[1]
74AUP1G17GW	pJ
74AUP1G17GV	рЈ
74AUP1G17GM	рЈ
74AUP1G17GN	рЈ
74AUP1G17GS	рЈ
74AUP1G17GX	рЈ
74AUP1G17GX4	bl

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

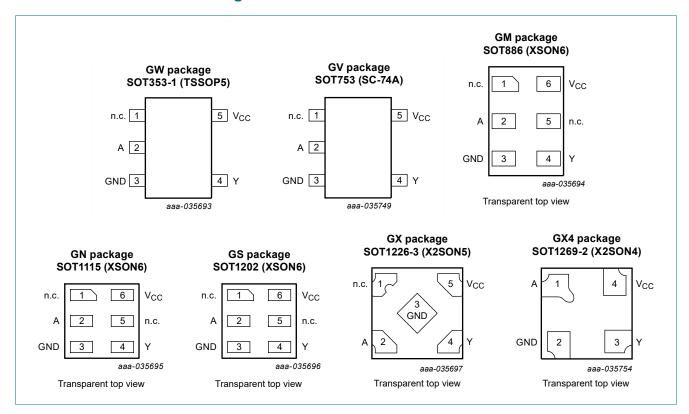
## 5. Functional diagram



Low-power Schmitt trigger

## 6. Pinning information

### 6.1. Pinning



### 6.2. Pin description

Table 3. Pin description

Symbol	Pin	Pin								
	SC-74A, TSSOP5 and X2SON5	XSON6	X2SON4							
n.c.	1	1, 5	-	not connected						
A	2	2	1	data input						
GND	3	3	2	ground (0 V)						
Υ	4	4	3	data output						
V <sub>CC</sub>	5	6	4	supply voltage						

## 7. Functional description

### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input	Output
A	Υ
L	L
Н	Н

Low-power Schmitt trigger

### 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
Io	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>		-	±20	mA
I <sub>CC</sub>	supply current			-	+50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = -40 °C to +125 °C				
		SOT353-1 (TSSOP5) SOT753 (SC-74A) SOT886 (XSON6) SOT1115 (XSON6) SOT1202 (XSON6) SOT1226-3 (X2SON5)	[2]	-	250	mW
		SOT1269-2 (X2SON4)	[3]	-	150	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SOT753 (SC-74A) package:  $P_{tot}$  derates linearly with 3.8 mW/K above 85  $^{\circ}\text{C}.$ 

For SOT886 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1226-3 (X2SON5) package: P<sub>tot</sub> derates linearly with 3.0 mW/K above 67 °C. [3] For SOT1269-2 (X2SON4) package: P<sub>tot</sub> derates linearly with 1.7 mW/K above 57 °C.

# 9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
V <sub>O</sub>	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C

<sup>[2]</sup> For SOT353-1 (TSSOP5) package: P<sub>tot</sub> derates linearly with 3.3 mW/K above 74 °C.

Low-power Schmitt trigger

## 10. Static characteristics

#### **Table 7. Static characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	5 °C		-1			
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0 \text{ V}$ to 3.6 V; $V_{CC} = 0 \text{ V}$	-	-	±0.2	μA
Δl <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.2	μA
I <sub>CC</sub>	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
Cı	input capacitance	$V_I$ = GND or $V_{CC}$ ; $V_{CC}$ = 0 V to 3.6 V	-	1.1	-	pF
Co	output capacitance	V <sub>O</sub> = GND; V <sub>CC</sub> = 0 V	-	1.7	-	pF

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -4	10 °C to +85 °C	'				
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
<b>5</b> 11		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 $V$ to 3.6 $V$	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		$I_{O}$ = -2.7 mA; $V_{CC}$ = 3.0 V	2.67	-	-	V
		$I_{O}$ = -4.0 mA; $V_{CC}$ = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		$I_{O}$ = 2.3 mA; $V_{CC}$ = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
I <sub>I</sub>	input leakage current	V <sub>I</sub> = GND to 3.6 V; V <sub>CC</sub> = 0 V to 3.6 V	-	-	±0.5	μΑ
I <sub>OFF</sub>	power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V	-	-	±0.5	μA
Δl <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.6	μA
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μA
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ

### Low-power Schmitt trigger

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -4	0 °C to +125 °C			1		
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O}$ = -20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6 × V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33 × V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		$I_{O}$ = 2.3 mA; $V_{CC}$ = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.75	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	V <sub>I</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.75	μA
I <sub>CC</sub>	supply current	$V_{I}$ = GND or $V_{CC}$ ; $I_{O}$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

# 11. Dynamic characteristics

### **Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5

Symbol	Parameter	Conditions	25 °C -40 °C to +8		o +85 °C	-40 °C to	+125 °C	Unit			
			Min	Typ[1]	Max	Min	Max	Min	Max		
C <sub>L</sub> = 5 p	F										
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 4</u> [2]									
	delay	elay V <sub>CC</sub> = 0.8 V	-	19.0	-	-	-	-	-	ns	
			V <sub>CC</sub> = 1.1 V to 1.3 V	2.6	5.7	10.6	2.5	10.9	2.5	11.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.4	4.2	6.5	2.3	7.1	2.3	7.4	ns	
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	3.6	5.5	1.9	6.1	1.9	6.3	ns	
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.9	3.0	4.2	1.8	4.6	1.8	4.8	ns	
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.8	2.7	3.6	1.5	3.8	1.5	4.0	ns	

### Low-power Schmitt trigger

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C <sub>L</sub> = 10	pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 4</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	22.5	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.9	6.6	12.4	2.7	12.9	2.7	13.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.6	4.8	7.8	2.4	8.3	2.4	8.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.5	4.2	6.3	2.4	6.8	2.4	7.1	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.3	3.5	4.8	2.1	5.3	2.1	5.6	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	3.3	4.4	2.0	4.6	2.0	4.8	ns
C <sub>L</sub> = 15	pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 4</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	26.0	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.2	7.4	14.1	3.1	14.7	3.1	14.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.1	5.4	8.7	2.8	9.5	2.8	9.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.7	4.7	7.1	2.7	7.8	2.7	8.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.6	4.0	5.6	2.5	6.0	2.5	6.3	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.5	3.7	4.9	2.2	5.2	2.2	5.5	ns
C <sub>L</sub> = 30	pF									
t <sub>pd</sub>	propagation	A to Y; see <u>Fig. 4</u> [2]								
	delay	V <sub>CC</sub> = 0.8 V	-	36.3	-	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.9	9.7	19.0	3.7	19.8	3.7	20.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.5	7.0	11.2	3.6	12.4	3.6	13.0	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.5	6.0	9.2	3.4	10.1	3.4	10.7	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	3.4	5.1	7.0	3.2	7.5	3.2	7.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	3.3	4.8	6.2	3.1	7.1	3.1	7.5	ns
$C_L = 5 p$	F, 10 pF, 15 pF	and 30 pF								
$C_{PD}$	power	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]								
	dissipation capacitance	V <sub>CC</sub> = 0.8 V	-	2.5	-	-	-	-	-	pF
	Japaonanoo	V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.5	-	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF

<sup>[1]</sup> All typical values are measured at nominal  $V_{\text{CC}}$ .

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

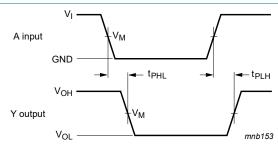
N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_0) = \text{sum of the outputs.}$ 

 <sup>[2]</sup> t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>
 [3] C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).

Low-power Schmitt trigger

### 11.1. Waveform and test circuit



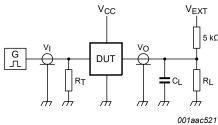
Measurement points are given in Table 9.

Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage levels that occur with the output load.

Fig. 4. The data input (A) to output (Y) propagation delays

**Table 9. Measurement points** 

Supply voltage	Input	nput					
V <sub>CC</sub>	V <sub>M</sub>	$V_{\rm M}$ $V_{\rm I}$ $t_{\rm r} = t_{\rm f}$					
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	0.5 × V <sub>CC</sub>			



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Test data is given in Table 10.

Definitions for test circuit:

R<sub>L</sub> = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

 $V_{\text{EXT}}$  = External voltage for measuring switching times.

Fig. 5. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V <sub>EXT</sub>		
V <sub>CC</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V <sub>CC</sub>

[1] For measuring enable and disable times,  $R_L$  = 5  $k\Omega.$ 

For measuring propagation delays, setup and hold times and pulse width  $R_{L}$  = 1  $\mbox{M}\Omega.$ 

Low-power Schmitt trigger

## 12. Transfer characteristics

#### **Table 11. Transfer characteristics**

Voltages are referenced to GND (ground = 0 V).

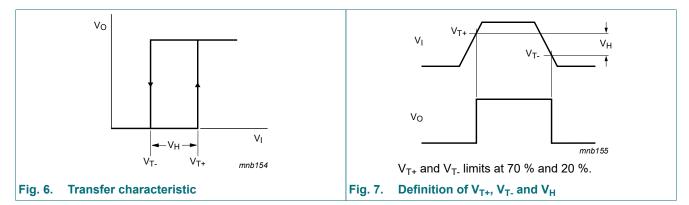
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	25 °C				1	
V <sub>T+</sub>	positive-going threshold voltage	see Fig. 6 and Fig. 7				
		V <sub>CC</sub> = 0.8 V	0.30	-	0.60	V
		V <sub>CC</sub> = 1.1 V	0.53	-	0.90	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.11	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.29	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.77	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.29	V
V <sub>T-</sub>	negative-going threshold voltage	see Fig. 6 and Fig. 7				
		V <sub>CC</sub> = 0.8 V	0.10	-	0.60	V
		V <sub>CC</sub> = 1.1 V	0.26	-	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	V
V <sub>H</sub>	hysteresis voltage	see Fig. 6, Fig. 7, Fig. 8 and Fig. 9				
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	V

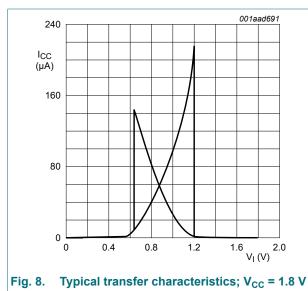
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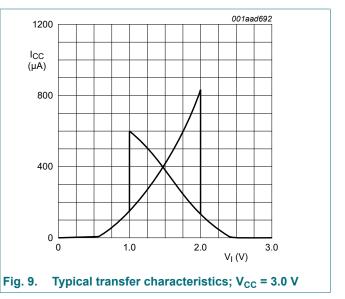
Symb	ol Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> =	-40 °C to +85 °C					
	positive-going threshold	see <u>Fig. 6</u> and <u>Fig. 7</u>				
	voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.60	V
		V <sub>CC</sub> = 1.1 V	0.53	-	0.90	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.11	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.29	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.77	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.29	V
	negative-going threshold	see Fig. 6 and Fig. 7				
	voltage	V <sub>CC</sub> = 0.8 V	0.10	-	0.60	V
		V <sub>CC</sub> = 1.1 V	0.26	-	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	V
V <sub>H</sub>	hysteresis voltage	see Fig. 6, Fig. 7, Fig. 8 and Fig. 9				
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	V
T <sub>amb</sub> =	-40 °C to +125 °C				'	
V <sub>T+</sub>	positive-going threshold	see <u>Fig. 6</u> and <u>Fig. 7</u>				
	voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.62	V
		V <sub>CC</sub> = 1.1 V	0.53	-	0.92	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.13	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.31	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.80	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.32	V
V <sub>T-</sub>	negative-going threshold	see Fig. 6 and Fig. 7				
	voltage	V <sub>CC</sub> = 0.8 V	0.10	-	0.60	V
		V <sub>CC</sub> = 1.1 V	0.26	-	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	V
V <sub>H</sub>	hysteresis voltage	see Fig. 6, Fig. 7, Fig. 8 and Fig. 9				1
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	V

Low-power Schmitt trigger

### 12.1. Waveforms transfer characteristics







Low-power Schmitt trigger

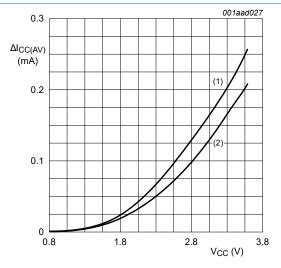
## 13. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{ad} = f_i \times (t_r \times I_{CC(AV)} + t_f \times I_{CC(AV)}) \times V_{CC}$  where:

- P<sub>ad</sub> = additional power dissipation (μW);
- f<sub>i</sub> = input frequency (MHz);
- t<sub>r</sub> = input rise time (ns); 10 % to 90 %;
- t<sub>f</sub> = input fall time (ns); 90 % to 10 %;
- I<sub>CC(AV)</sub> = average additional supply current (μA).

Average I<sub>CC</sub> differs with positive or negative input transitions, as shown in Fig. 10.



- (1) Positive-going edge.
- (2) Negative-going edge.

Linear change of V<sub>I</sub> between 0.8 V and 2.0 V. All values given are typical, unless otherwise specified.

Fig. 10. Average  $I_{CC}$  as a function of  $V_{CC}$ 

Low-power Schmitt trigger

## 14. Package outline

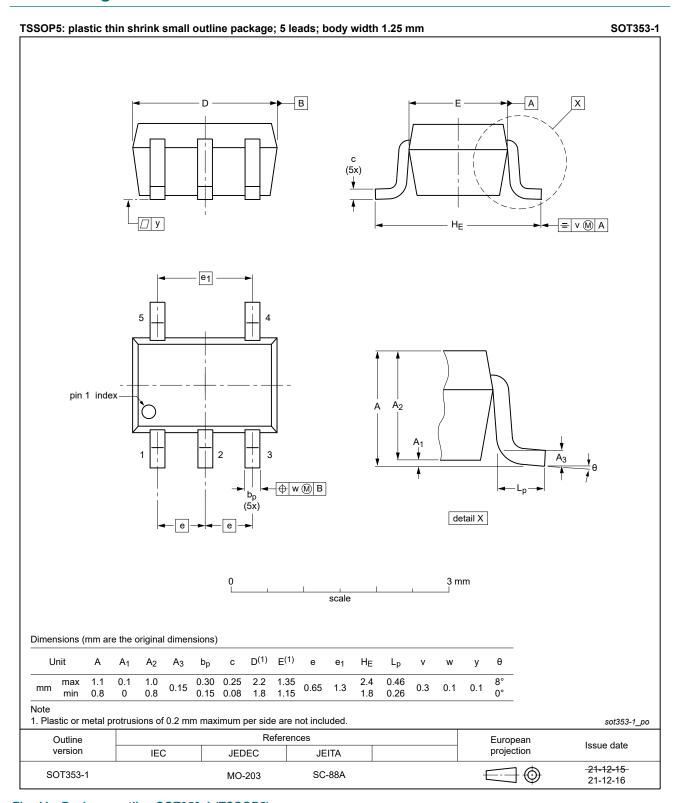


Fig. 11. Package outline SOT353-1 (TSSOP5)

### Low-power Schmitt trigger

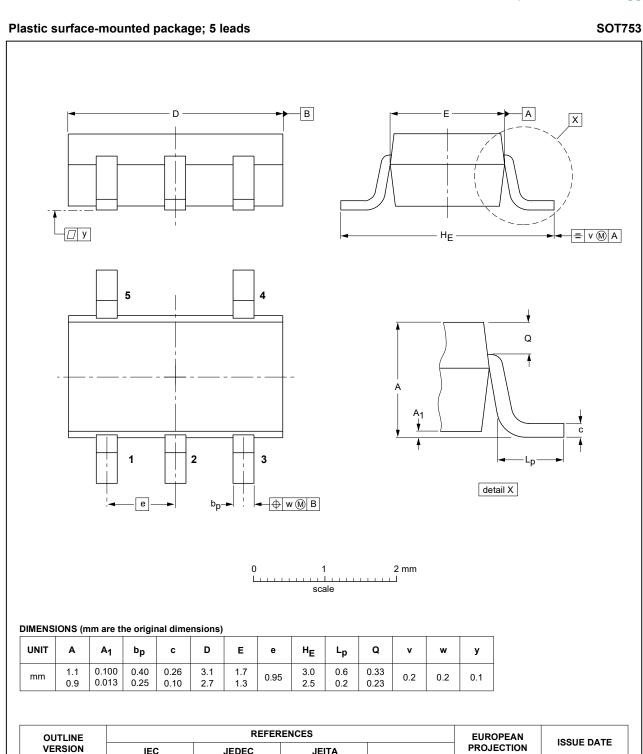


Fig. 12. Package outline SOT753 (SC-74A)

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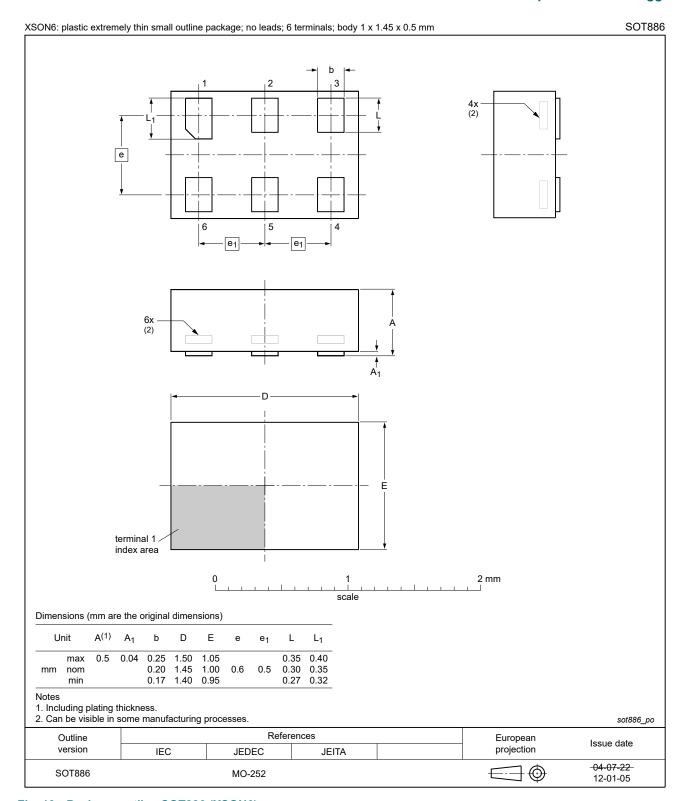


Fig. 13. Package outline SOT886 (XSON6)

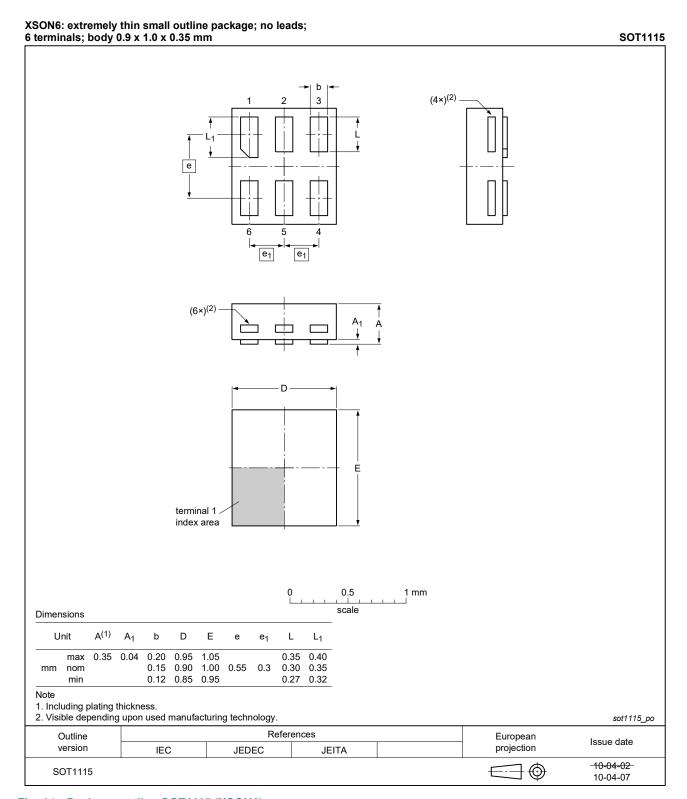


Fig. 14. Package outline SOT1115 (XSON6)

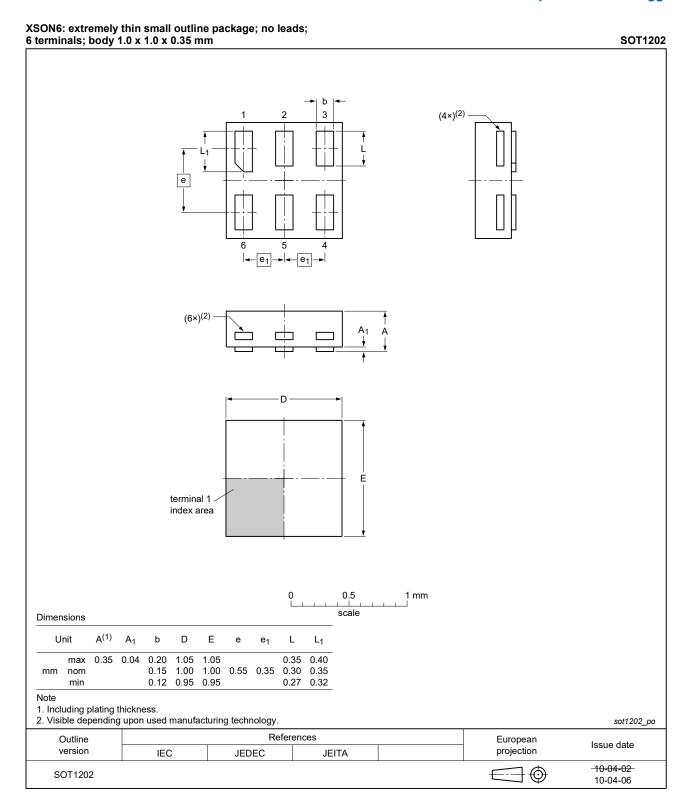


Fig. 15. Package outline SOT1202 (XSON6)

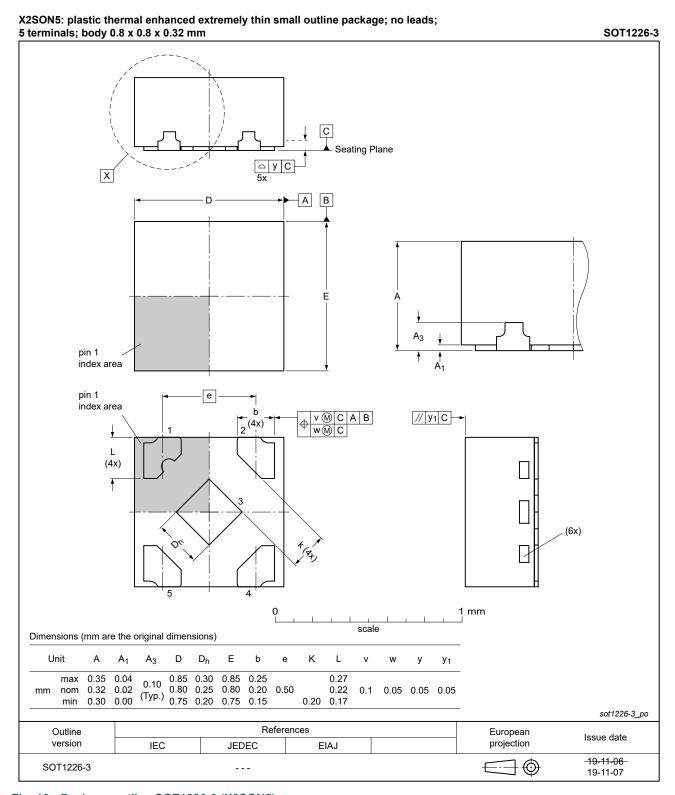


Fig. 16. Package outline SOT1226-3 (X2SON5)

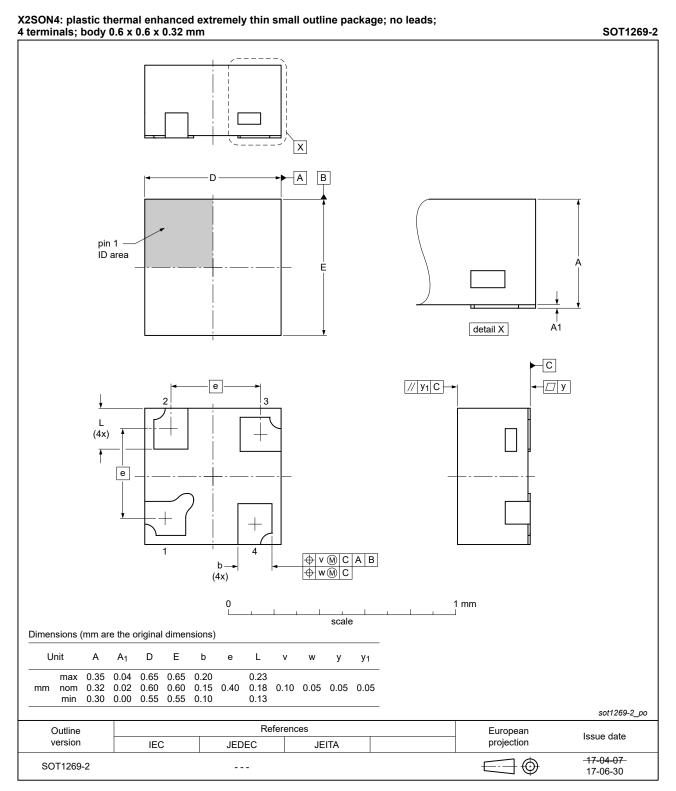


Fig. 17. Package outline SOT1269-2 (X2SON4)

Low-power Schmitt trigger

### 15. Abbreviations

#### **Table 12. Abbreviations**

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model

## 16. Revision history

### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74AUP1G17 v.14	20230713	Product data sheet	-	74AUP1G17 v.13		
Modifications:	<u>Section 2</u> : ESD specification updated according to the latest JEDEC standard.					
74AUP1G17 v.13	20220113 Product data sheet - 74AUP					
Modifications:	Fig. 11: Package outline drawing for SOT353-1 (TSSOP5) has changed.					
74AUP1G17 v.12	20210707	Product data sheet	-	74AUP1G17 v.11		
Modifications:	<ul> <li>SOT1226 (X2SON5) package changed to SOT1226-3 (X2SON5) package.</li> <li>Type number 74AUP1G17GF (SOT891/XSON6) removed.</li> <li>Table 5: Derating values for P<sub>tot</sub> total power dissipation have been updated.</li> </ul>					
74AUP1G17 v.11	20180608	Product data sheet	-	74AUP1G17 v.10		
Modifications:	Added type nur	nber 74AUP1G17GX4 (SOT	1269-2)	,		
74AUP1G17 v.10	20170519	Product data sheet	-	74AUP1G17 v.9		
Modifications:	Nexperia.	nis data sheet has been rede e been adapted to the new o		, ,		
74AUP1G17 v.9	20161104	Product data sheet	-	74AUP1G17 v.8		
Modifications:	<ul> <li>Added type nur</li> </ul>	mber 74AUP1G17GV (SOT7	53)			
74AUP1G17 v.8	20150115	Product data sheet	-	74AUP1G17 v.7		
Modifications:	Marking code   T	able 2: typo corrected in type	number 74AUP1G17GX	(.		
74AUP1G17 v.7	20120716	Product data sheet	-	74AUP1G17 v.6		
Modifications:	Package outline	e drawing of SOT1226 (Fig. 1	16) modified.			
74AUP1G17 v.6	20120412	Product data sheet	-	74AUP1G17 v.5		
Modifications:	<ul> <li>Added type number 74AUP1G17GX (SOT1226)</li> <li>Package outline drawing of SOT886 (Fig. 13) modified.</li> </ul>					
74AUP1G17 v.5	20111124	Product data sheet	-	74AUP1G17 v.4		
Modifications:	Legal pages updated.					
74AUP1G17 v.4	20100715	Product data sheet	-	74AUP1G17 v.3		
74AUP1G17 v.3	20090710	Product data sheet	-	74AUP1G17 v.2		
74AUP1G17 v.2	20060727	Product data sheet	-	74AUP1G17 v.1		

#### Low-power Schmitt trigger

### 17. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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### Low-power Schmitt trigger

### **Contents**

1. General description	1
2. Features and benefits	1
3. Ordering information	2
4. Marking	2
5. Functional diagram	
6. Pinning information	3
6.1. Pinning	
6.2. Pin description	
7. Functional description	
8. Limiting values	
Recommended operating conditions	
10. Static characteristics	
11. Dynamic characteristics	
11.1. Waveform and test circuit	
12. Transfer characteristics	
12.1. Waveforms transfer characteristics	
13. Application information	
14. Package outline	
15. Abbreviations	
16. Revision history	
17. Legal information	
- 5	

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