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PNP resistor-equipped transistor; $R1 = 2.2 \text{ k}\Omega$, $R2 = 2.2 \text{ k}\Omega$ Rev. 2 — 4 May 2012 Product data sh

Product data sheet

1. **Product profile**

1.1 General description

PNP Resistor-Equipped Transistor (RET) in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTC123EMB.

1.2 Features and benefits

- 100 mA output current capability
- Reduces component count
- Built-in bias resistors
- Reduces pick and place costs

1.3 Applications

- Low-current peripheral driver
- Control of IC inputs

- Simplifies circuit design
- AEC-Q101 qualified
- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Replaces general-purpose transistors in digital applications
- Mobile applications

1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
lo	output current		-	-	-100	mA
R1	bias resistor 1 (input)	T _{amb} = 25 °C	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		0.8	1	1.2	



PNP resistor-equipped transistor; $R1 = 2.2 \text{ k}\Omega$, $R2 = 2.2 \text{ k}\Omega$

2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)		
2	G	GND (emitter)		3
3	0	output (collector)	2 Transparent top view SOT883B (DFN1006B-3)	1 R2 2 sym003

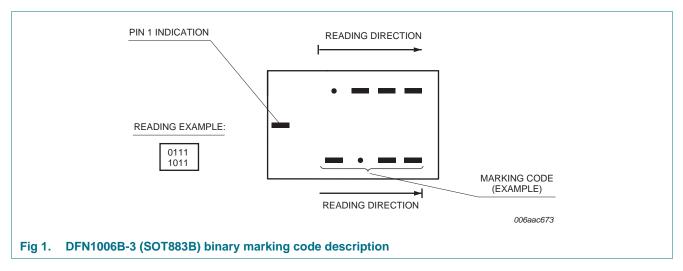
3. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
PDTA123EMB	DFN1006B-3	Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.37 mm	SOT883B				

4. Marking

Table 4.	Marking	codes
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Type number	Marking code
PDTA123EMB	0010 0010



PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

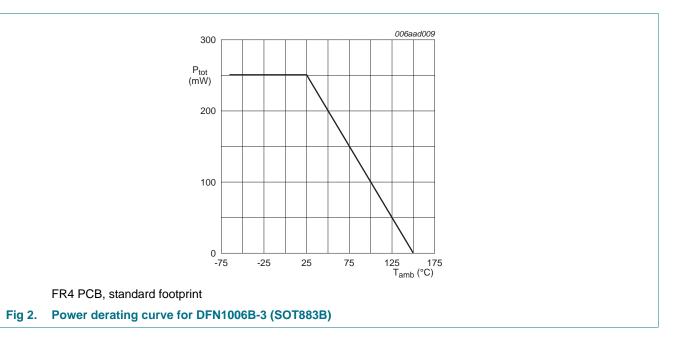
5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-50	V
V _{EBO}	emitter-base voltage	open collector		-	-10	V
VI	input voltage	positive		-	10	V
		negative		-	-12	V
lo	output current			-	-100	mA
I _{CM}	peak collector current	pulsed; t _p ≤ 1 ms		-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	<u>[1]</u>	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

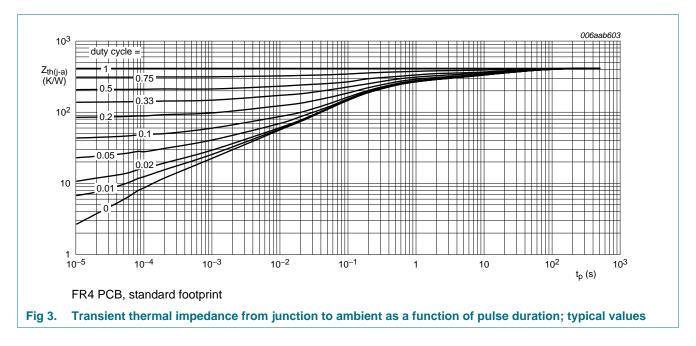


PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

6. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1]</u>	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

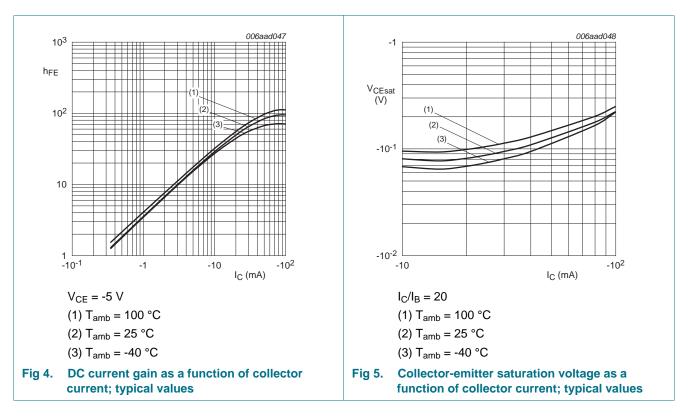


PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

7. Characteristics

Characteristics					
Parameter	Conditions	Min	Тур	Max	Unit
collector-base cut-off current	V_{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
collector-emitter cut-off	V_{CE} = -30 V; I _B = 0 A; T _{amb} = 25 °C	-	-	-1	μA
current	V_{CE} = -30 V; I _B = 0 A; T _j = 150 °C	-	-	-5	μA
emitter-base cut-off current	V_{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-2	mA
DC current gain	V_{CE} = -5 V; I _C = -20 mA; T _{amb} = 25 °C	30	-	-	
collector-emitter saturation voltage	I_{C} = -10 mA; I_{B} = -0.5 mA; T_{amb} = 25 °C	-	-	-150	mV
off-state input voltage	V_{CE} = -5 V; I _C = -1 mA; T _{amb} = 25 °C	-	-1.2	-0.5	V
on-state input voltage	V_{CE} = -0.3 V; I_{C} = -20 mA; T_{amb} = 25 $^{\circ}C$	-2	-1.6	-	V
bias resistor 1 (input)	T _{amb} = 25 °C	1.54	2.2	2.86	kΩ
bias resistor ratio		0.8	1	1.2	
collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	-	3	pF
transition frequency	$V_{CE} = -5 \text{ V}; \text{ I}_{C} = -10 \text{ mA}; \text{ f} = 100 \text{ MHz};$ [1] $T_{amb} = 25 \text{ °C}$	-	180	-	MHz
	Parametercollector-base cut-off currentcollector-emitter cut-off currentemitter-base cut-off currentDC current gain collector-emitter saturation voltageoff-state input voltage on-state input voltagebias resistor 1 (input) bias resistor ratio collector capacitance	ParameterConditionscollector-base cut-off current $V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ collector-emitter cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_j = 150 \text{ °C}$ emitter-base cut-off current $V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ DC current gain $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ collector-emitter saturation voltage $I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$ off-state input voltage $V_{CE} = -5 \text{ V}; I_C = -1 \text{ mA}; T_{amb} = 25 \text{ °C}$ on-state input voltage $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ bias resistor 1 (input) $T_{amb} = 25 \text{ °C}$ bias resistor ratio $T_{amb} = 25 \text{ °C}$ collector capacitance $V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ °C}$ transition frequency $V_{CE} = -5 \text{ V}; I_C = -10 \text{ mA}; f = 100 \text{ MHz};$	ParameterConditionsMincollector-base cut-off current $V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ -collector-emitter cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ -collector-emitter cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_j = 150 \text{ °C}$ -emitter-base cut-off current $V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ -DC current gain $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ 30collector-emitter saturation voltage $I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$ -off-state input voltage $V_{CE} = -5 \text{ V}; I_C = -1 \text{ mA}; T_{amb} = 25 \text{ °C}$ -on-state input voltage $V_{CE} = -0.3 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ -bias resistor 1 (input) $T_{amb} = 25 \text{ °C}$ 1.54bias resistor ratio0.80.8collector capacitance $V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 \text{ °C}$ -transition frequency $V_{CE} = -5 \text{ V}; I_C = -10 \text{ mA}; f = 100 \text{ MHz};$ 11	ParameterConditionsMinTypcollector-base cut-off current $V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ collector-emitter cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ emitter-base cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_j = 150 \text{ °C}$ emitter-base cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_j = 150 \text{ °C}$ DC current gain $V_{CE} = -5 \text{ V}; I_C = -0 \text{ A}; T_{amb} = 25 \text{ °C}$ 30-collector-emitter saturation voltage $I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}; T_{amb} = 25 \text{ °C}$ off-state input voltage $V_{CE} = -5 \text{ V}; I_C = -1 \text{ mA}; T_{amb} = 25 \text{ °C}$ on-state input voltage $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ bias resistor 1 (input) $T_{amb} = 25 \text{ °C}$ 1.542.2bias resistor ratio0.81collector capacitance $V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; I_B = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 \text{ °C}$ transition frequency $V_{CE} = -5 \text{ V}; I_C = -10 \text{ mA}; f = 100 \text{ MHz};$ 11-180	Parameter Conditions Min Typ Max collector-base cut-off current $V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ - - -100 collector-emitter cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ - - -1 collector-emitter cut-off current $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ - - -1 vcrent $V_{CE} = -30 \text{ V}; I_B = 0 \text{ A}; T_amb = 25 \text{ °C}$ - - -5 emitter-base cut-off current $V_{CE} = -5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ °C}$ - - -2 DC current gain $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ 30 - - collector-emitter saturation voltage $V_{CE} = -5 \text{ V}; I_C = -1 \text{ mA}; T_{amb} = 25 \text{ °C}$ - -1.2 -0.5 off-state input voltage $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ - -1.2 -0.5 on-state input voltage $V_{CE} = -5 \text{ V}; I_C = -20 \text{ mA}; T_{amb} = 25 \text{ °C}$ - -1.2 -0.5 bias resistor 1 (input) $T_{amb} = 25 \text{ °C}$ 1.54 2.2

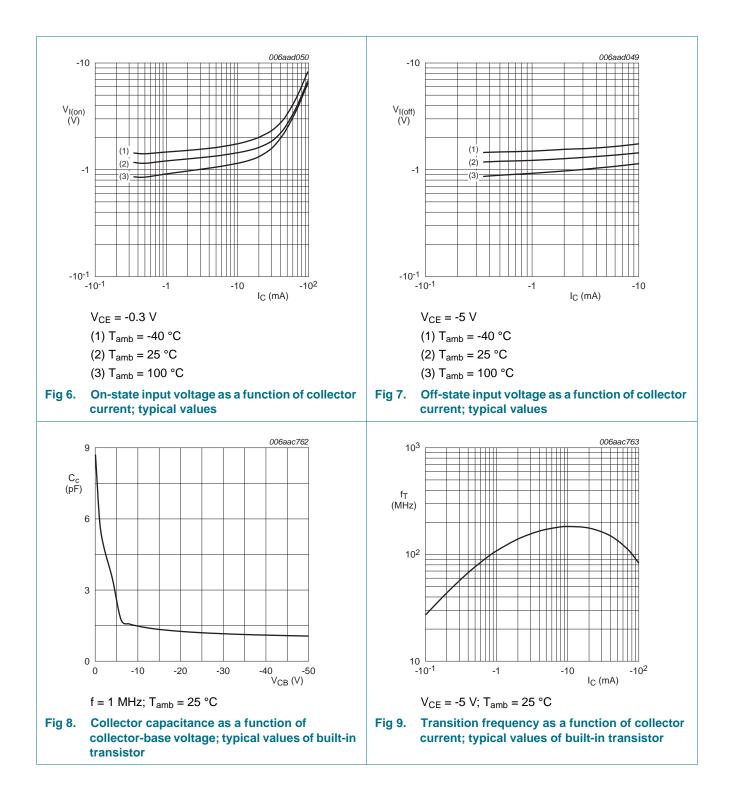
[1] Characteristics of built-in transistor.



PDTA123EMB Product data sheet

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PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω



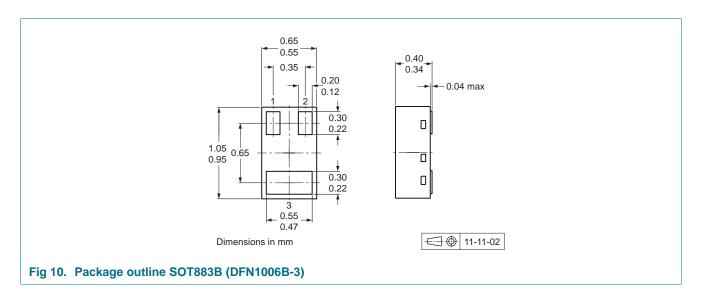
PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

8. Test information

8.1 Quality information

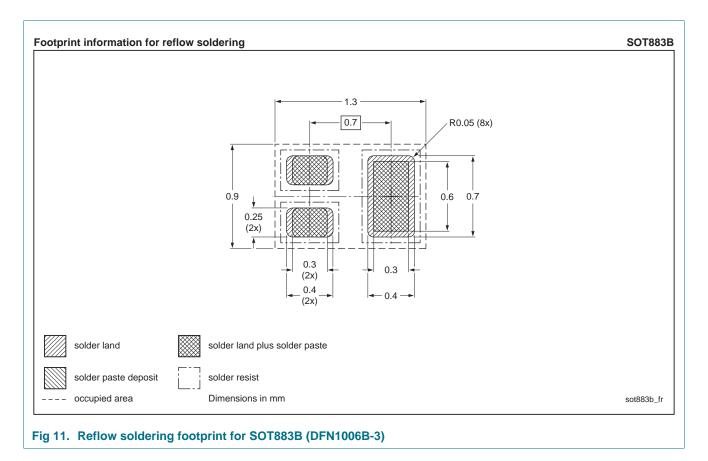
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

9. Package outline



PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

10. Soldering



PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

11. Revision history

Table 8.Revision	history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PDTA123EMB v.2	20120504	Product data sheet	-	PDTA123EMB v.1
Modifications:	• 7 "Characterist	ics": parameter name corre	cted	
PDTA123EMB v.1	20120425	Product data sheet	-	-

PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 2.2 k Ω

12. Legal information

12.1 Data sheet status

Document status ^[1] ^[2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Product data sheet

PDTA123EMB

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