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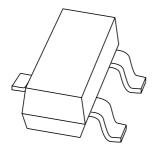
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



MMBTA92 PNP high-voltage transistor

Product specification Supersedes data of 2000 Apr 11 2004 Jan 16





PNP high-voltage transistor

MMBTA92

FEATURES

- Low current (max. 100 mA)
- High voltage (max. 300 V).

APPLICATIONS

- Telephony
- Professional communication equipment.

DESCRIPTION

PNP high-voltage transistor in a SOT23 plastic package. NPN complement: MMBTA42.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
MMBTA92	7E*	

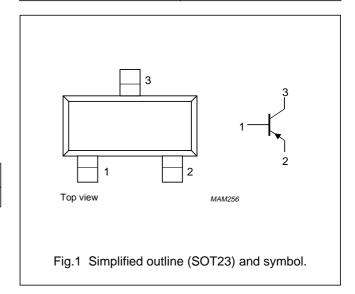
Note

1. * = p: Made in Hong Kong.

* = t : Made in Malaysia. * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE		
NUMBER	NAME	AME DESCRIPTION VERSION	
MMBTA92	_	plastic surface mounted package; 3 leads	SOT23

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS		MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-300	V
V_{CEO}	collector-emitter voltage	open base	_	-300	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

Philips Semiconductors Product specification

PNP high-voltage transistor

MMBTA92

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 $^{\circ}C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0$; $V_{CB} = -200 \text{ V}$	_	-250	nA
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -3 \text{ V}$	_	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; note 1			
		$I_C = -1 \text{ mA}$	25	_	
		$I_C = -10 \text{ mA}$	40	_	
		$I_{\rm C} = -30 \; {\rm mA}$	25	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -20 \text{ mA}; I_B = -2 \text{ mA}$	_	-500	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -20 \text{ mA}; I_B = -2 \text{ mA}$	_	-900	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -20 \text{ V}$; $f = 1 \text{ MHz}$	_	6	pF
f _T	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -20 \text{ V};$ f = 100 MHz	50	_	MHz

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

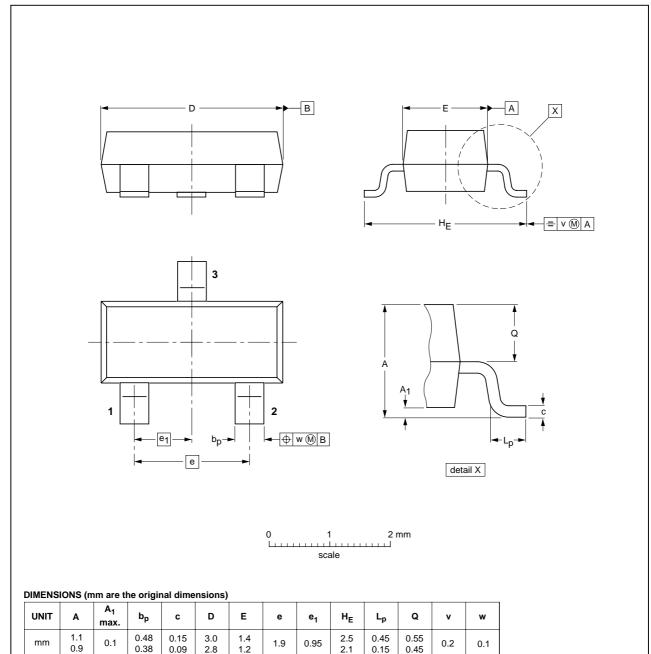
PNP high-voltage transistor

MMBTA92

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE	REFERENCES		EUROPEAN	ICCUE DATE			
VERSION	IEC JEDEC EIAJ		EIAJ		PROJECTION	ISSUE DATE	
SOT23		TO-236AB				97-02-28 99-09-13	

Philips Semiconductors Product specification

PNP high-voltage transistor

MMBTA92

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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