

NX3020NAK

30 V, single N-channel Trench MOSFET

29 October 2013

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Very fast switching
- Trench MOSFET technology
- ESD protection
- Low threshold voltage

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V_{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	200	mA
Static charact	teristics						
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_{D} = 100 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; \ T_{j}$ = 25 °C		-	2.7	4.5	Ω

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	<u> </u>	D I
2	S	source		
3	D	drain	1	G S 017aaa255

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
NX3020NAK	TO-236AB	plastic surface-mounted package; 3 leads	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code [1]
NX3020NAK	%CU

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V_{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	200	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	125	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \mu s$		-	800	mA
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	300	mW
			[1]	-	360	mW

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Symbol	Parameter	Conditions		Min	Max	Unit
		T _{sp} = 25 °C		-	1060	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode	,			'	
Is	source current	T _{amb} = 25 °C	[1]	-	200	mA

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

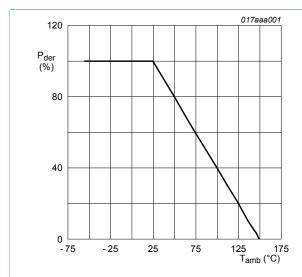


Fig. 1. Normalized total power dissipation as a function of ambient temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

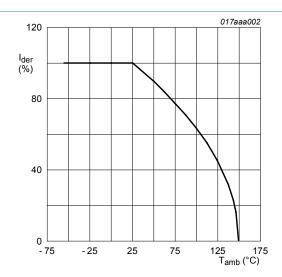


Fig. 2. Normalized continuous drain current as a function of ambient temperature

$$I_{der} = \frac{I_D}{I_{DQ5^{\circ}C}} \times 100 \%$$

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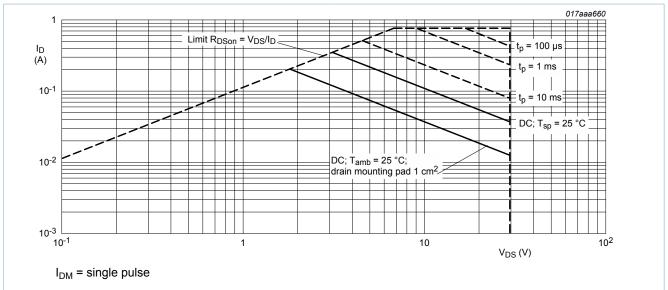


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
fror	thermal resistance	to	[1]	-	350	400	K/W
	from junction to ambient		[2]	-	300	340	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	115	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

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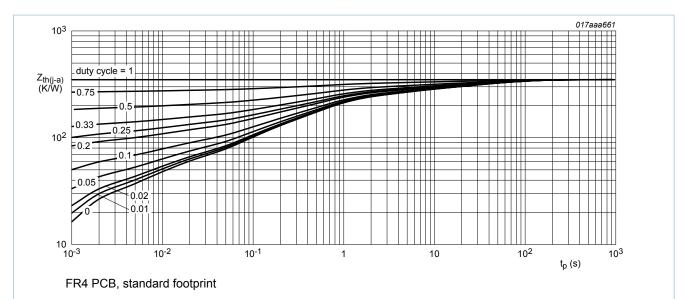


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

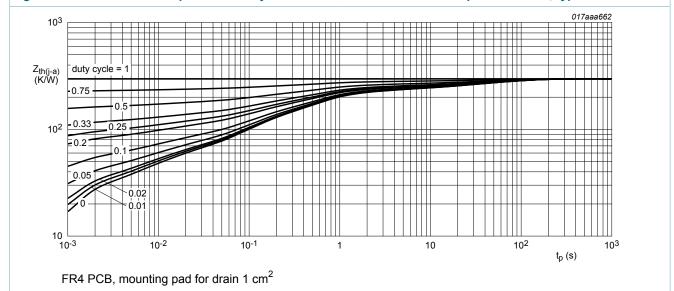


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	acteristics		<u> </u>				
$V_{(BR)DSS}$	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$		30	-	-	V
V_{GSth}	gate-source threshold voltage	$I_D = 250 \text{ A}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$		8.0	1.2	1.5	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V; T _j = 25 °C		-	-	1	μΑ
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 150 °C		-	-	10	μΑ
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	3.5	μΑ
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-3.5	μA
		$V_{GS} = 10 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	1	μA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-1	μA
		$V_{GS} = 4.5 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	-	0.5	μΑ
		V_{GS} = -4.5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-0.5	μA
R _{DSon} drain-source on-state resistance		V_{GS} = 10 V; I_{D} = 100 mA; pulsed; $t_{p} \le 300$ μs; $\delta \le 0.02$; T_{j} = 25 °C	-	2.7	4.5	Ω
		V_{GS} = 10 V; I_{D} = 100 mA; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{j} = 150 °C	-	5.5	9.2	Ω
		V_{GS} = 4.5 V; I_D = 100 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_j = 25 °C	-	3	5.2	Ω
	V_{GS} = 2.5 V; I_{D} = 10 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; \ T_{j}$ = 25 °C	-	4	13	Ω	
g fs	forward transconductance	V_{DS} = 10 V; I_{D} = 150 mA; pulsed; $t_{p} \le$ 300 µs; $\delta \le$ 0.02 ; T_{j} = 25 °C	320	-	-	mS
Dynamic cl	haracteristics			-		
Q _{G(tot)}	total gate charge	V_{DS} = 15 V; I_{D} = 150 mA; V_{GS} = 4.5 V;	-	0.34	0.44	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.11	-	nC
Q_{GD}	gate-drain charge		-	0.06	-	nC
C _{iss}	input capacitance	V_{DS} = 10 V; f = 1 MHz; V_{GS} = 0 V;	-	13	20	pF
C _{oss}	output capacitance	T _j = 25 °C	-	2.6	-	pF
C _{rss}	reverse transfer capacitance		-	1.1	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 20 V; R_{L} = 250 Ω ; V_{GS} = 10 V;	-	5	10	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega$; $T_j = 25 °C$	-	5	-	ns
t _{d(off)}	turn-off delay time	1	-	34	68	ns
t _f	fall time	1	-	17	-	ns
Source-dra	in diode			1	1	
V _{SD}	source-drain voltage	I_S = 115 mA; V_{GS} = 0 V; T_j = 25 °C	0.47	0.7	1.2	V

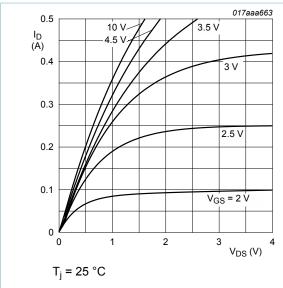


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

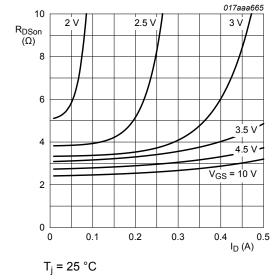


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

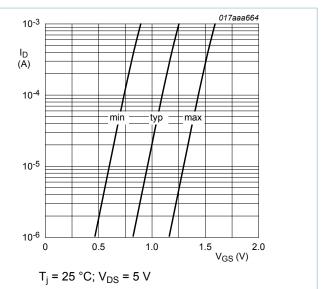


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

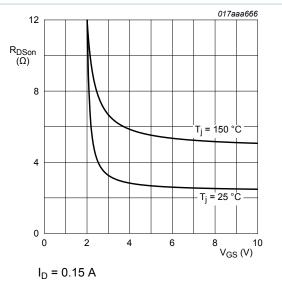


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

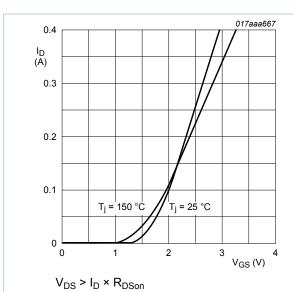


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

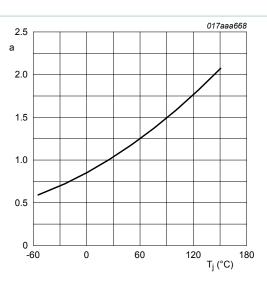


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$

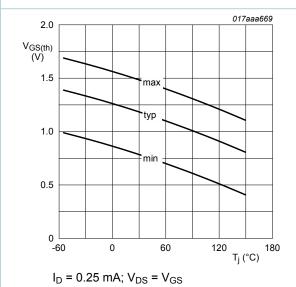
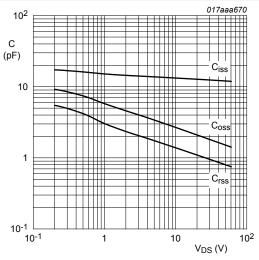


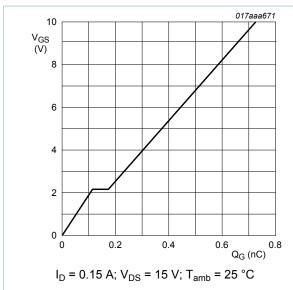
Fig. 12. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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V_{GS}(pl)

V_{GS}(th)

V_{GS}(th)

Q_{GS1} Q_{GS2}

Q_{GG}(tot)

017aaa137

Fig. 15. Gate charge waveform definitions

Fig. 14. Gate-source voltage as a function of gate charge; typical values

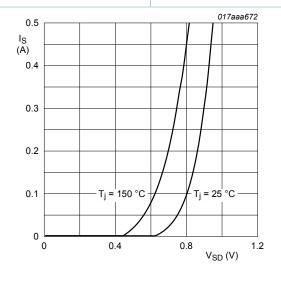
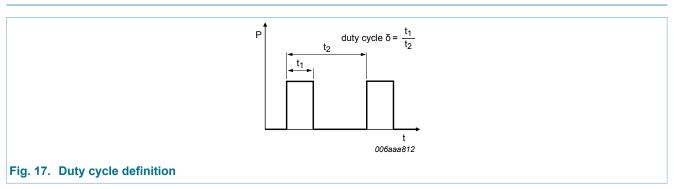


Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

 $V_{GS} = 0 V$

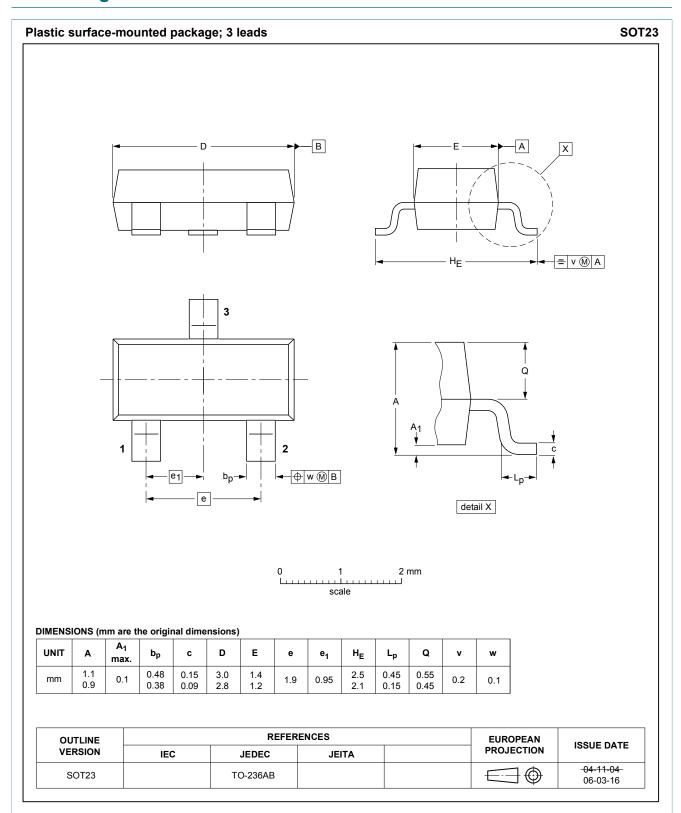


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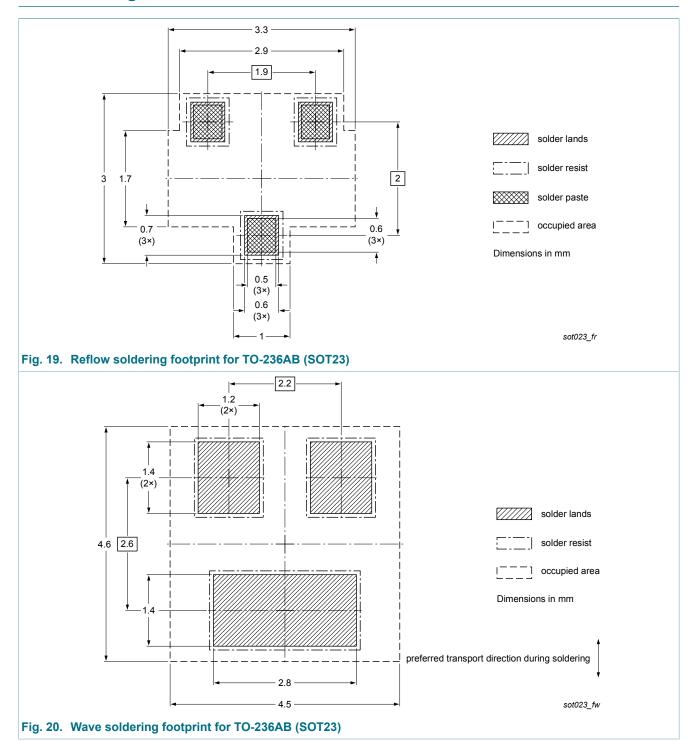
12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
NX3020NAK v.2	20131029	Product data sheet	-	NX3020NAK v.1		
Modifications:	 3D package outline added Table 7 values of capacitance parameters corrected Figure 13 corrected 					
NX3020NAK v.1	20121002	Product data sheet	-	-		

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