



NX3020NAKV

30 V, 200 mA dual N-channel Trench MOSFET

29 October 2013

Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Very fast switching
- Trench MOSFET technology
- ESD protection
- Low threshold voltage

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

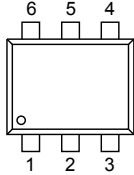
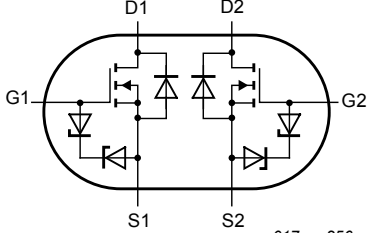
Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--|----------------------------------|---|-----|-----|-----|----------|
| Per transistor | | | | | | |
| V_{DS} | drain-source voltage | $T_j = 25\text{ °C}$ | - | - | 30 | V |
| V_{GS} | gate-source voltage | | -20 | - | 20 | V |
| I_D | drain current | $V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}$ | [1] | - | 200 | mA |
| Static characteristics (per transistor) | | | | | | |
| R_{DSon} | drain-source on-state resistance | $V_{GS} = 10\text{ V}; I_D = 100\text{ mA}; T_j = 25\text{ °C}$ | - | 2.7 | 4.5 | Ω |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm^2 .

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|---|--|
| 1 | S1 | source TR1 |  <p>SOT666</p> |  <p>017aaa256</p> |
| 2 | G1 | gate TR1 | | |
| 3 | D2 | drain TR2 | | |
| 4 | S2 | source TR2 | | |
| 5 | G2 | gate TR2 | | |
| 6 | D1 | drain TR1 | | |

6. Ordering information

Table 3. Ordering information

| Type number | Package | | |
|-------------|---------|--|---------|
| | Name | Description | Version |
| NX3020NAKV | SOT666 | plastic surface-mounted package; 6 leads | SOT666 |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| NX3020NAKV | GB |

8. Limiting values

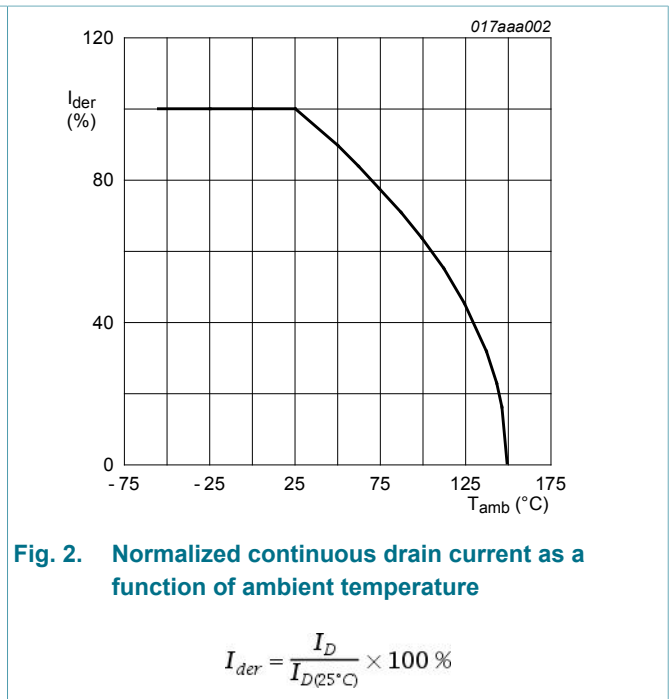
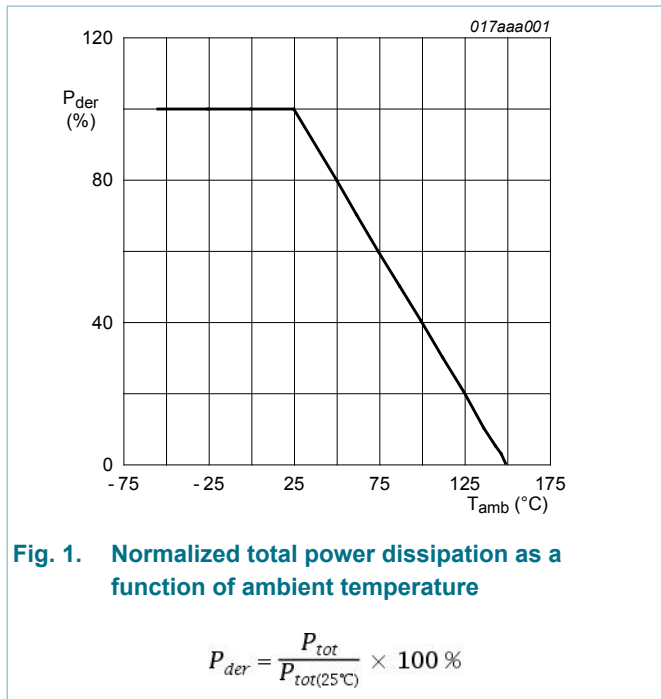
Table 5. Limiting values

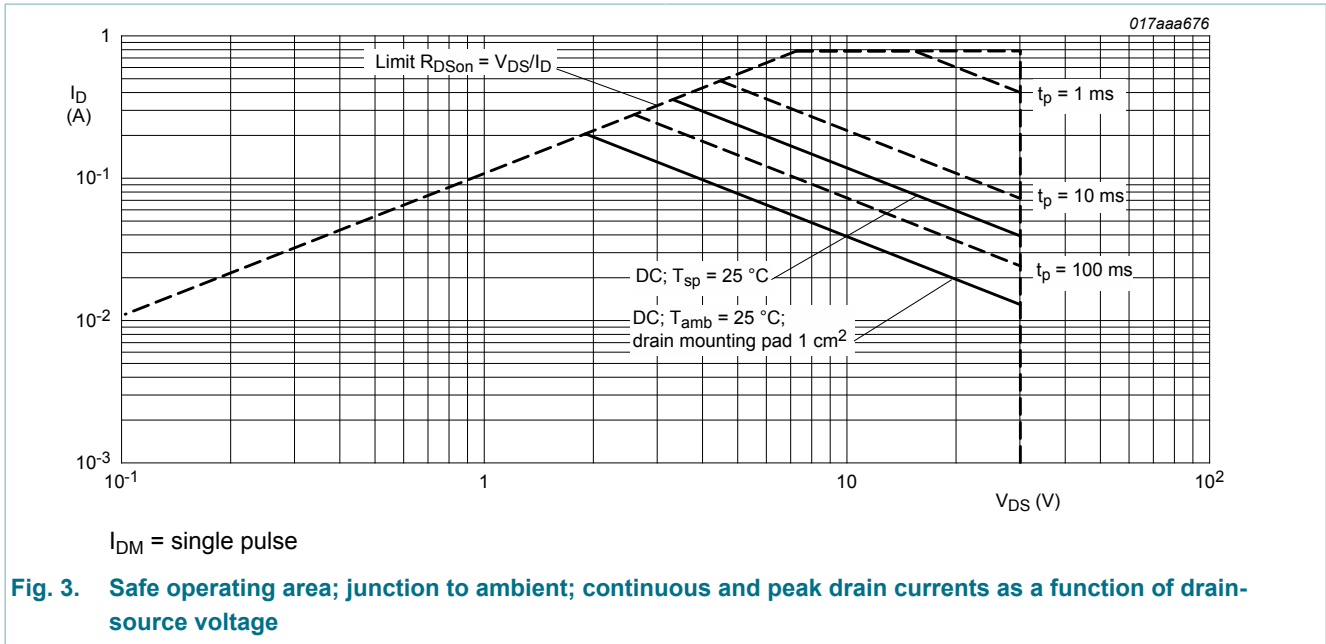
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit | |
|-----------------------|-------------------------|---|-----|-----|------|----|
| Per transistor | | | | | | |
| V_{DS} | drain-source voltage | $T_j = 25\text{ °C}$ | - | 30 | V | |
| V_{GS} | gate-source voltage | | -20 | 20 | V | |
| I_D | drain current | $V_{GS} = 4.5\text{ V}; T_{amb} = 25\text{ °C}$ | [1] | - | 200 | mA |
| | | $V_{GS} = 4.5\text{ V}; T_{amb} = 100\text{ °C}$ | [1] | - | 120 | mA |
| I_{DM} | peak drain current | $T_{amb} = 25\text{ °C}; \text{single pulse}; t_p \leq 10\text{ }\mu\text{s}$ | | - | 800 | mA |
| P_{tot} | total power dissipation | $T_{amb} = 25\text{ °C}$ | [2] | - | 260 | mW |
| | | | [1] | - | 370 | mW |
| | | $T_{sp} = 25\text{ °C}$ | | - | 1100 | mW |

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|---------------------------|-------------------------|--------------------------|-----|-----|-----|------|
| Source-drain diode | | | | | | |
| I _S | source current | T _{amb} = 25 °C | | - | 200 | mA |
| Per device | | | | | | |
| P _{tot} | total power dissipation | T _{amb} = 25 °C | [2] | - | 375 | mW |
| T _j | junction temperature | | | -55 | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.





9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|-----------------------|--|-------------|-----|-----|-----|-----|------|
| Per transistor | | | | | | | |
| R _{th(j-a)} | thermal resistance from junction to ambient | in free air | [1] | - | 410 | 480 | K/W |
| | | | [2] | - | 290 | 340 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | | - | - | 105 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².

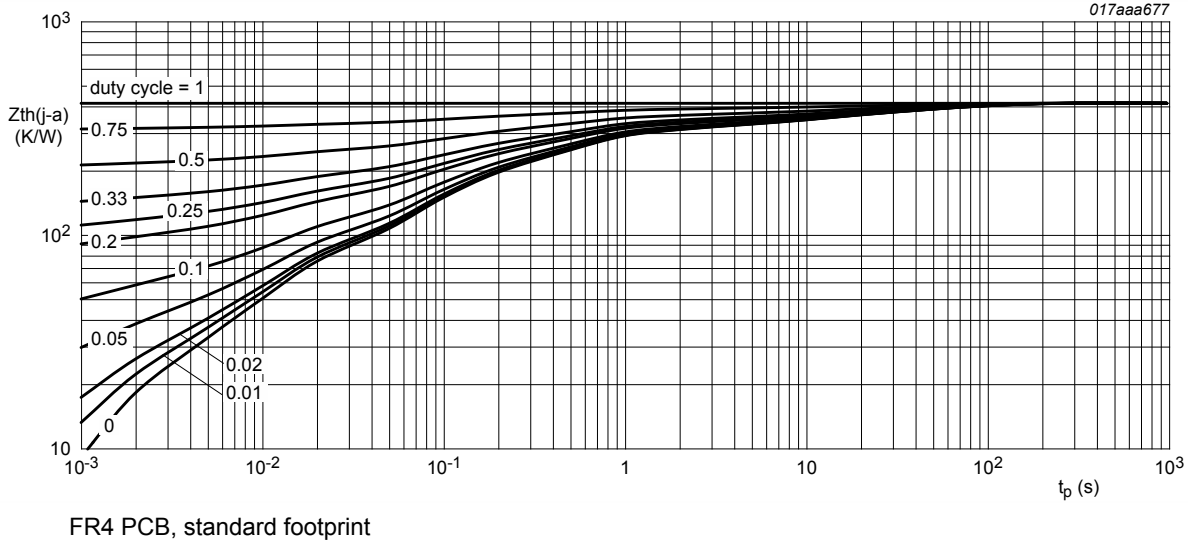


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

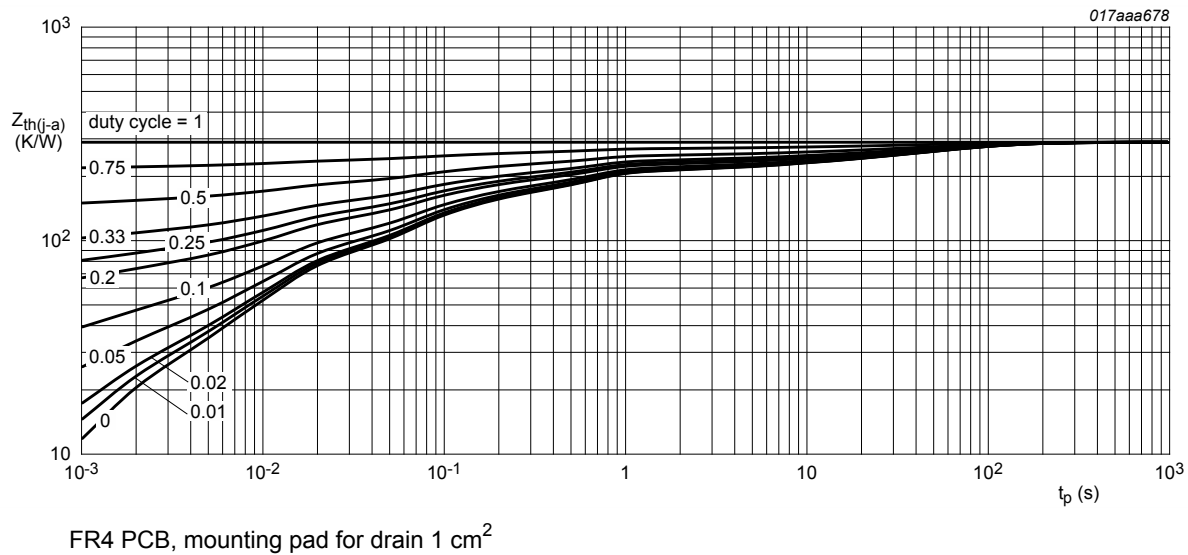


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--|--------------------------------|---|-----|-----|-----|---------|
| Static characteristics (per transistor) | | | | | | |
| $V_{(BR)DSS}$ | drain-source breakdown voltage | $I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 \text{ }^\circ C$ | 30 | - | - | V |
| V_{GSth} | gate-source threshold voltage | $I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ }^\circ C$ | 0.8 | 1.2 | 1.5 | V |
| I_{DSS} | drain leakage current | $V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25 \text{ }^\circ C$ | - | - | 1 | μA |
| | | $V_{DS} = 30 V; V_{GS} = 0 V; T_j = 150 \text{ }^\circ C$ | - | - | 10 | μA |

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|----------------------------------|---|------|------|------|------|
| I _{GSS} | gate leakage current | V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C | - | - | 3.5 | μA |
| | | V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C | - | - | 3.5 | μA |
| | | V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C | - | - | 1 | μA |
| | | V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C | - | - | 1 | μA |
| | | V _{GS} = 4.5 V; V _{DS} = 0 V; T _j = 25 °C | - | - | 0.5 | μA |
| | | V _{GS} = -4.5 V; V _{DS} = 0 V; T _j = 25 °C | - | - | 0.5 | μA |
| R _{DSon} | drain-source on-state resistance | V _{GS} = 10 V; I _D = 100 mA; T _j = 25 °C | - | 2.7 | 4.5 | Ω |
| | | V _{GS} = 10 V; I _D = 100 mA; T _j = 150 °C | - | 5.5 | 9.2 | Ω |
| | | V _{GS} = 4.5 V; I _D = 100 mA; T _j = 25 °C | - | 3 | 5.2 | Ω |
| | | V _{GS} = 2.5 V; I _D = 10 mA; T _j = 25 °C | - | 4 | 13 | Ω |
| g _{fs} | forward transconductance | V _{DS} = 10 V; I _D = 150 mA; T _j = 25 °C | - | 320 | - | mS |
| Dynamic characteristics (per transistor) | | | | | | |
| Q _{G(tot)} | total gate charge | V _{DS} = 15 V; I _D = 150 mA; V _{GS} = 4.5 V; T _j = 25 °C | - | 0.34 | 0.44 | nC |
| Q _{GS} | gate-source charge | | - | 0.11 | - | nC |
| Q _{GD} | gate-drain charge | | - | 0.06 | - | nC |
| C _{iss} | input capacitance | V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V; T _j = 25 °C | - | 13 | 20 | pF |
| C _{oss} | output capacitance | | - | 2.6 | - | pF |
| C _{rss} | reverse transfer capacitance | | - | 1.1 | - | pF |
| t _{d(on)} | turn-on delay time | V _{DS} = 20 V; R _L = 250 Ω; V _{GS} = 10 V; R _{G(ext)} = 6 Ω; T _j = 25 °C | - | 5 | 10 | ns |
| t _r | rise time | | - | 5 | - | ns |
| t _{d(off)} | turn-off delay time | | - | 34 | 68 | ns |
| t _f | fall time | | - | 17 | - | ns |
| Source-drain diode (per transistor) | | | | | | |
| V _{SD} | source-drain voltage | I _S = 115 mA; V _{GS} = 0 V; T _j = 25 °C | 0.47 | 0.7 | 1.2 | V |

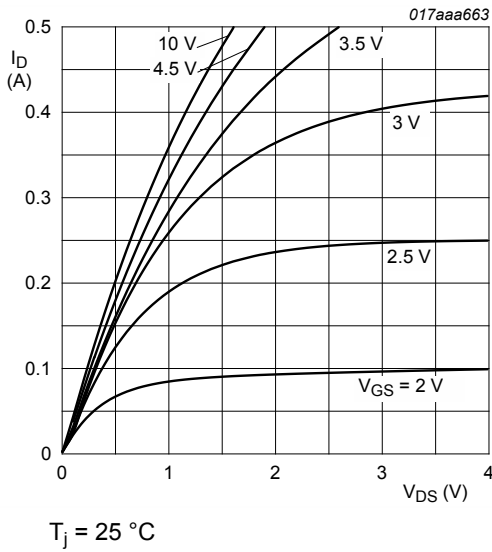


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

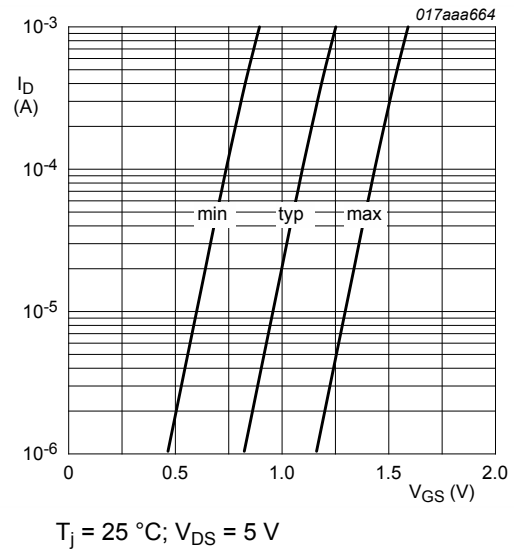


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

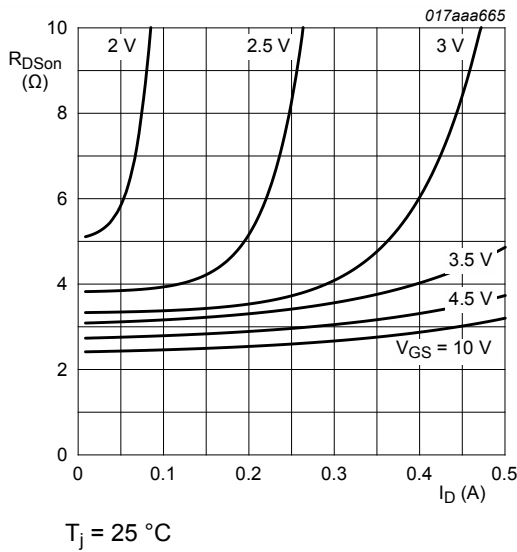


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

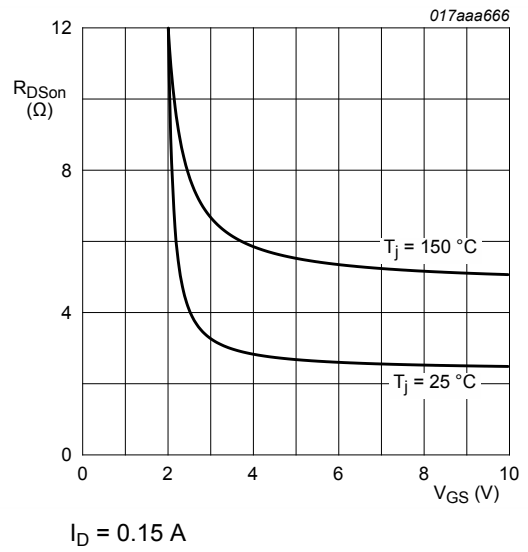
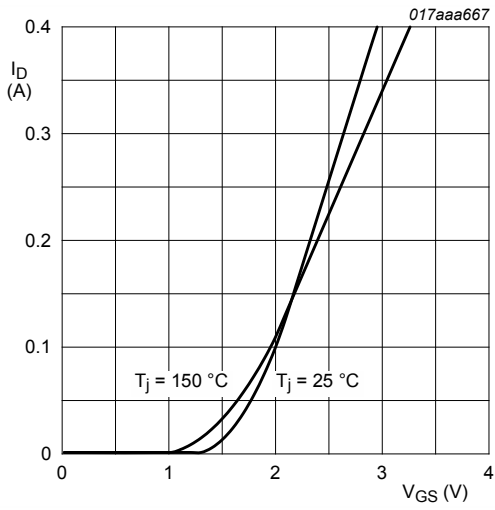


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values



$$V_{DS} > I_D \times R_{DSon}$$

Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

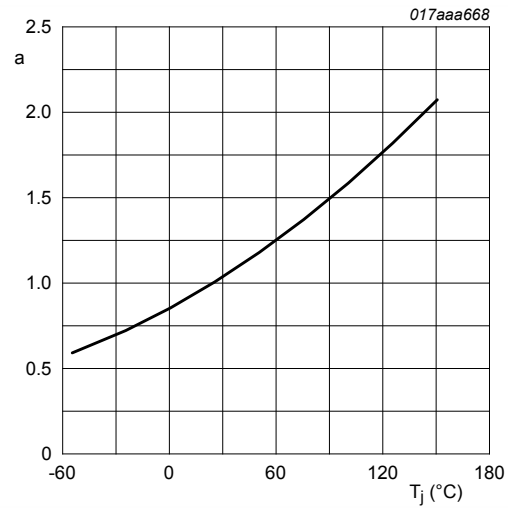
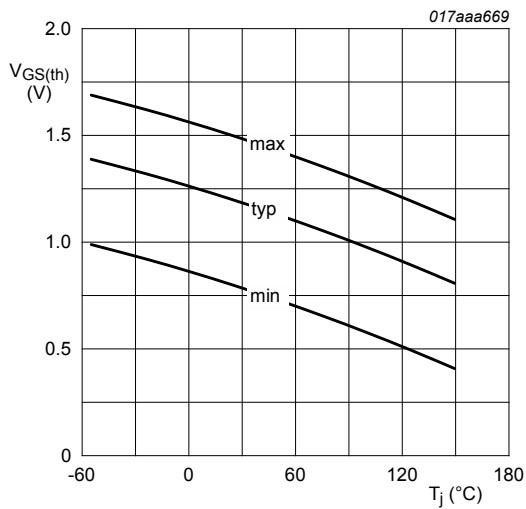


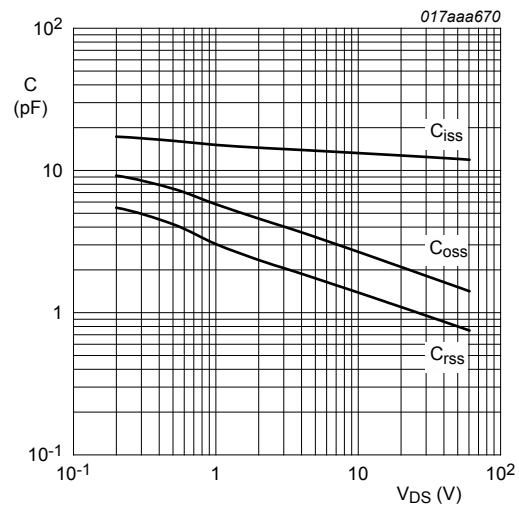
Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon(25^\circ C)}}$$



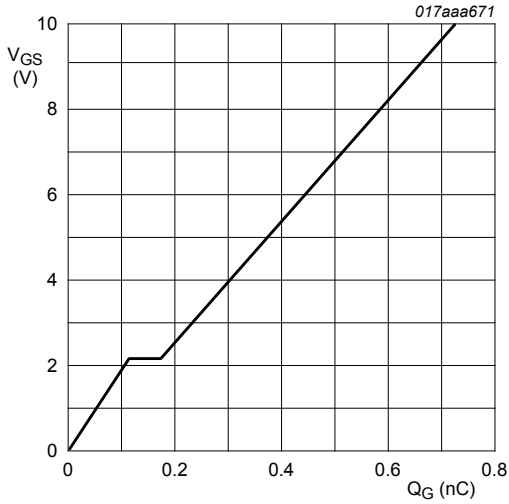
$$I_D = 0.25 \text{ mA}; V_{DS} = V_{GS}$$

Fig. 12. Gate-source threshold voltage as a function of junction temperature



$$f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}$$

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$I_D = 0.15$ A; $V_{DS} = 15$ V; $T_{amb} = 25$ °C

Fig. 14. Gate-source voltage as a function of gate charge; typical values

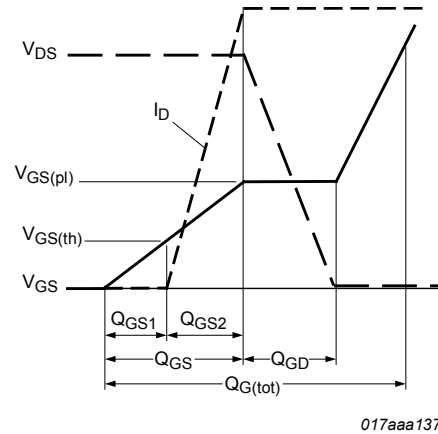
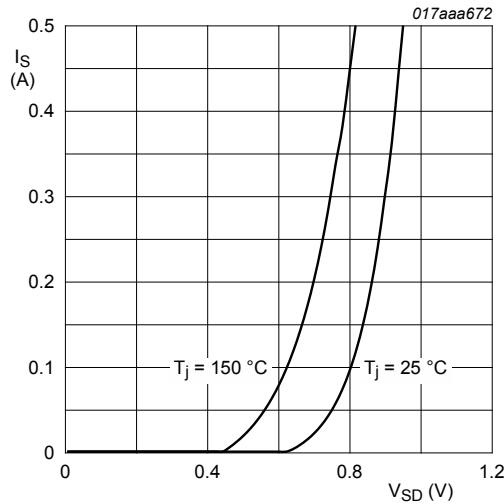


Fig. 15. Gate charge waveform definitions



$V_{GS} = 0$ V

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

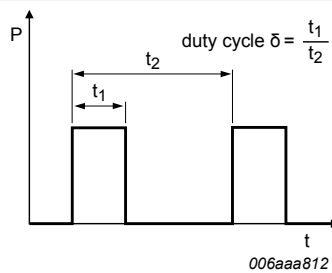


Fig. 17. Duty cycle definition

12. Package outline

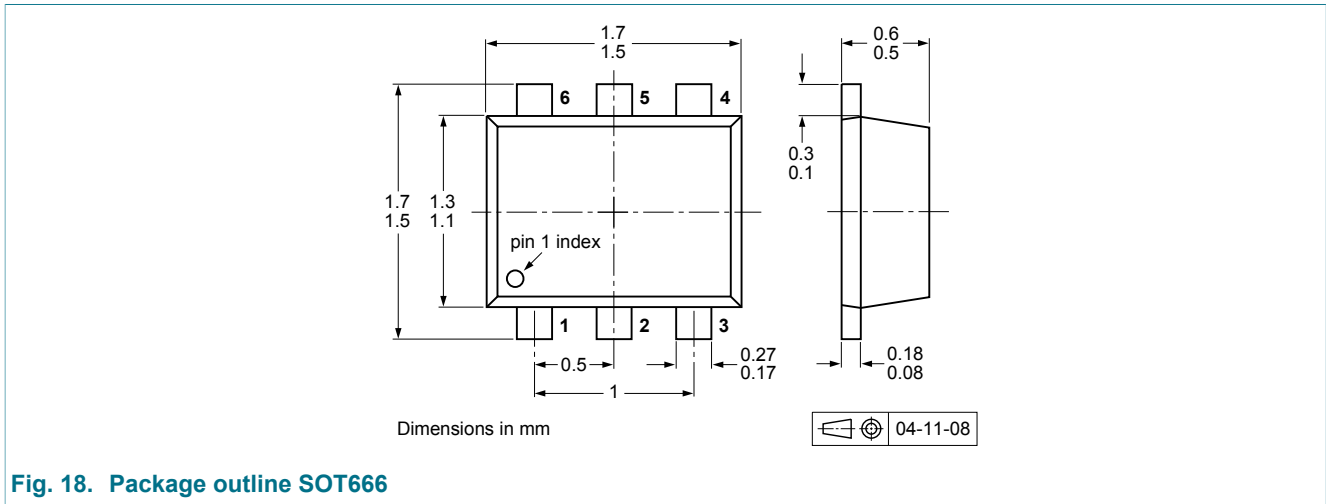


Fig. 18. Package outline SOT666

13. Soldering

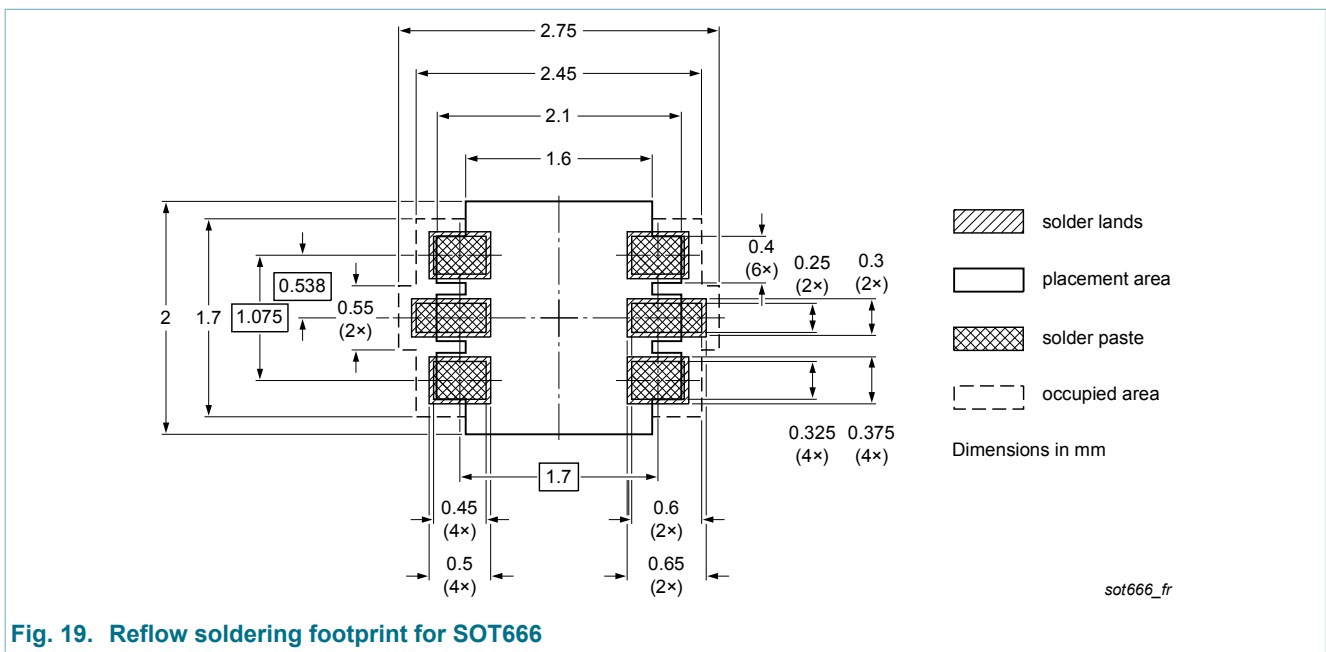


Fig. 19. Reflow soldering footprint for SOT666

14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|---|--------------------|---------------|----------------|
| NX3020NAKV v.2 | 20131029 | Product data sheet | - | NX3020NAKV v.1 |
| Modifications: | <ul style="list-style-type: none">• 3D package outline added• Table 7 values of capacitance parameters corrected• Figure 13 corrected | | | |
| NX3020NAKV v.1 | 20120706 | Product data sheet | - | - |

15. Legal information

15.1 Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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- [2] The term 'short data sheet' is explained in section "Definitions".
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