

600 V, 0.5 A PNP high-voltage low VCEsat (BISS) transistor12 August 2014Product data sheet

1. General description

PNP high-voltage low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBHV8560Z

2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C
- High collector current gain h_{FE} at high I_C
- AEC-Q101 qualified

3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- LCD backlighting
- HID front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-600	V
I _C	collector current			-	-	-0.5	А
h _{FE}	DC current gain	V_{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C		70	130	-	

5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	2, 4
2	С	collector		1-1
3	E	emitter		· •
4	С	collector	☐1 ∐2 ∐3 SC-73 (SOT223)	3 sym028

6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PBHV9560Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223

7. Marking

Table 4. Marking codes	
Type number	Marking code
PBHV9560Z	HV956Z

8. Limiting values

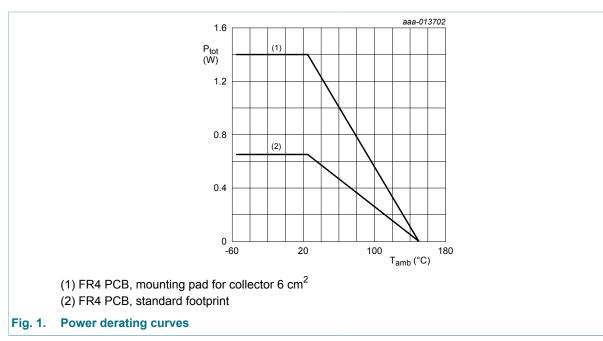
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-600	V
V _{CEO}	collector-emitter voltage	open base		-	-600	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-600	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-0.5	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.4	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



PBHV9560Z

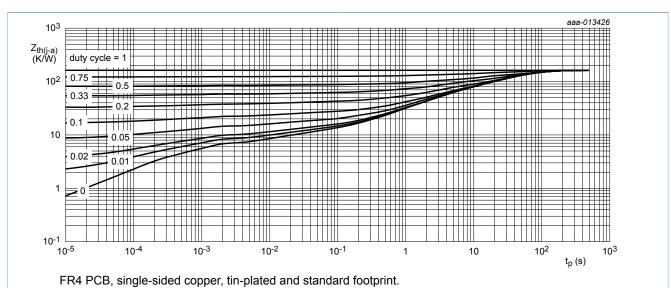
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9. Thermal characteristics

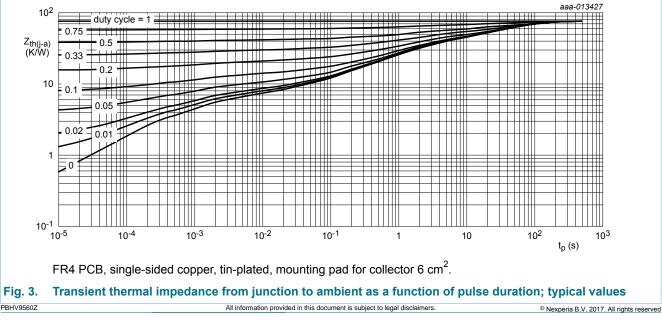
Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	-	190	K/W
	from junction to ambient		[2]	-	-	89	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².





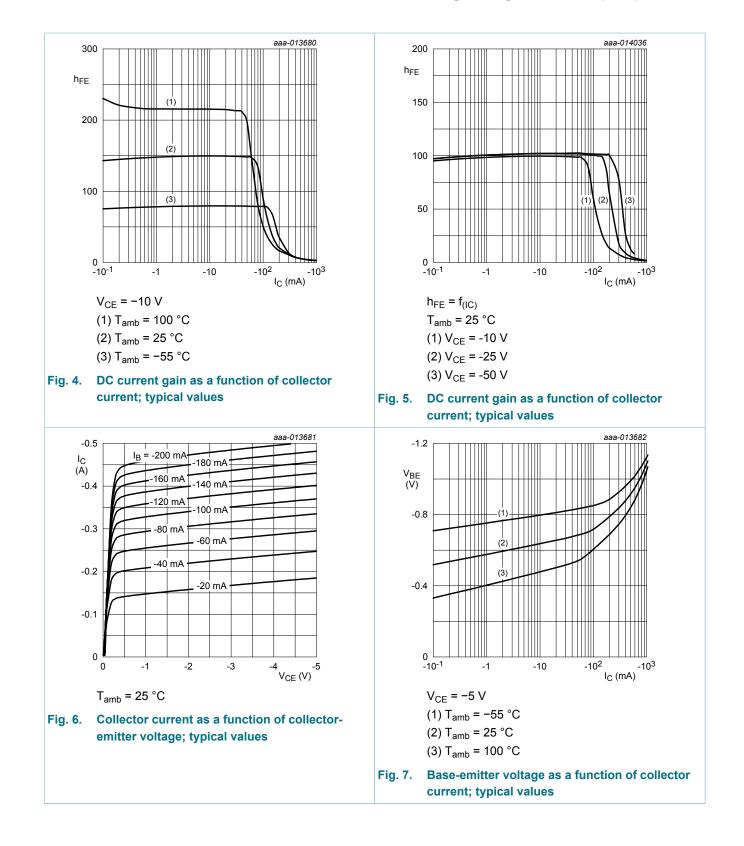


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10. Characteristics

Symbol	Parameter	Conditions	Ν	Min	Тур	Max	Unit
Symbol	Falalletei		n	VIIII	тур	WIAX	Unit
I _{CBO}	collector-base cut-off	V_{CB} = -400 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-	-100	nA
	current	V_{CB} = -400 V; I _E = 0 A; T _j = 150 °C	-	-	-	-10	μA
I _{CES}	collector-emitter cut-off current	V_{CE} = -400 V; V_{BE} = 0 V; T_{amb} = 25 °C	-	-	-	-100	nA
I _{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-	-100	nA
h _{FE}	DC current gain	V_{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C	7	70	130	-	
		$\begin{split} V_{CE} &= -10 \text{ V}; \text{ I}_{C} = -100 \text{ mA}; \text{ pulsed}; \\ t_{p} &\leq 300 \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 ^{\circ}\text{C} \end{split}$	Ę	50	90	-	
V _{CEsat}	collector-emitter	$I_{\rm C}$ = -50 mA; $I_{\rm B}$ = -5 mA; $T_{\rm amb}$ = 25 °C	-	-	-150	-250	mV
saturation voltage	saturation voltage	I_{C} = -100 mA; I_{B} = -20 mA; pulsed; $t_{p} \le 300$ μs; δ ≤ 0.02; T_{amb} = 25 °C	-	_	-140	-250	mV
V _{BEsat}	base-emitter saturation voltage	$\begin{split} I_C &= -50 \text{ mA}; I_B = -5 \text{ mA}; \text{ pulsed}; \\ t_p &\leq 300 \mu\text{s}; \delta \leq 0.02; T_{\text{amb}} = 25 ^\circ\text{C} \end{split}$	-	-	-	-900	mV
f _T	transition frequency	V _{CE} = -10 V; I _C = -30 mA; f = 100 MHz	-	-	38	-	MHz
C _c	collector capacitance	V _{CB} = -20 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	_	12	-	pF
C _e	emitter capacitance	V _{EB} = -0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	_	390	-	pF

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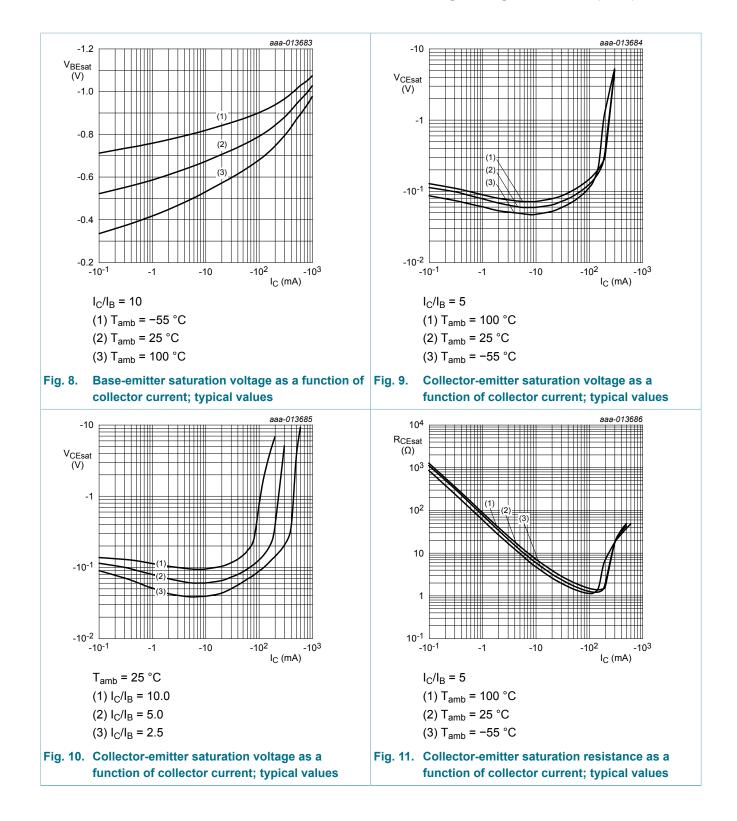
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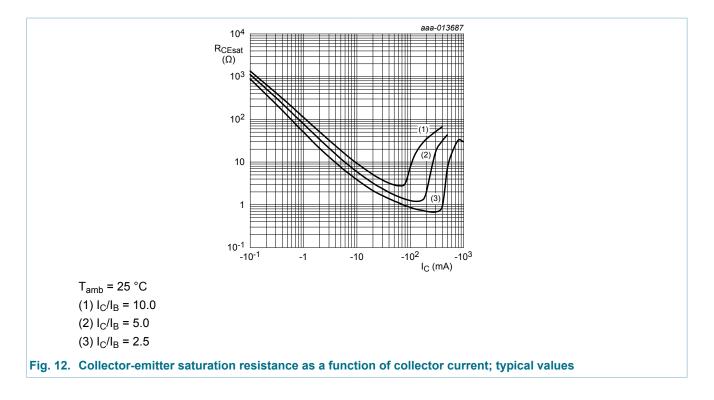
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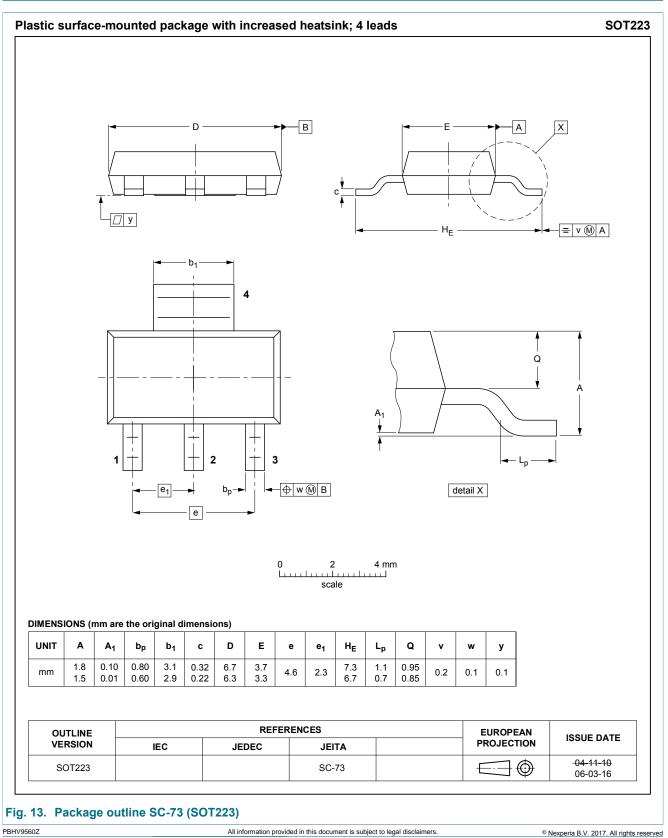


11. Test information

11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

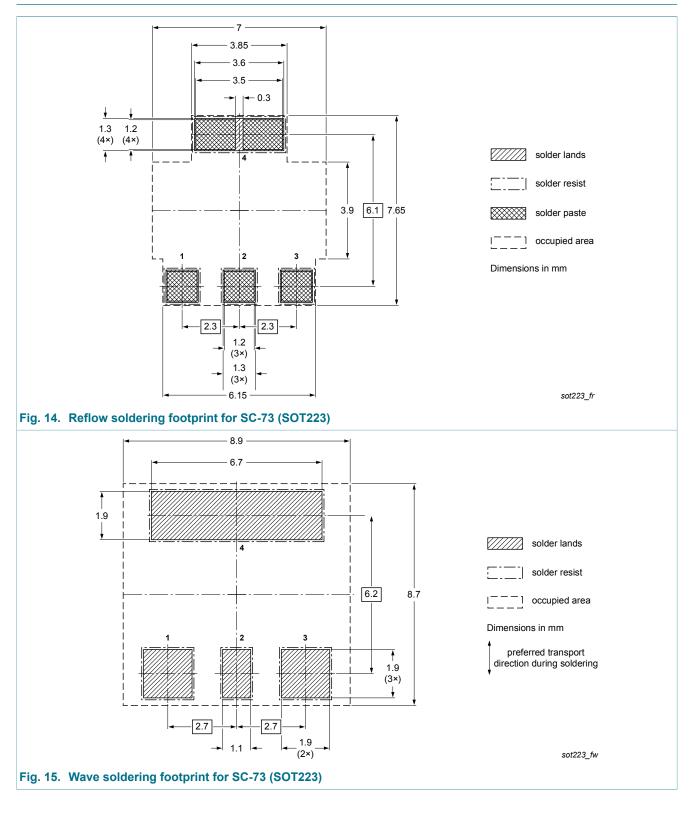
12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBHV9560Z v.1	20140812	Product data sheet	-	-

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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