

# PBSS3515MB

# 15 V, 0.5 A PNP low VCEsat (BISS) transistor Rev. 1 — 6 March 2012

Product data sheet

#### 1. **Product profile**

## 1.1 General description

PNP low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a leadless ultra small SOT883B Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS2515MB.

#### 1.2 Features and benefits

- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Low collector-emitter saturation voltage V<sub>CFsat</sub>
- High collector current capability I<sub>C</sub> and  $I_{CM}$
- High efficiency due to less heat generation
- AEC-Q101 qualified
- Reduced Printed-Circuit Board (PCB) requirements

## 1.3 Applications

- DC-to-DC conversion
- Supply line switching
- Battery charger

- LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)

#### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-15	V
I <sub>C</sub>	collector current		-	-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	-1	Α
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C$ = -500 mA; $I_B$ = -50 mA; pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb} = 25 \ ^{\circ}C$	-	-	500	mΩ



# 2. Pinning information

#### Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		
2	Е	emitter	1	3 
3	С	collector	2 3	1 —
			Transparent top view	2
			SOT883B	sym013

# 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS3515MB	-	Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.37 mm	SOT883B

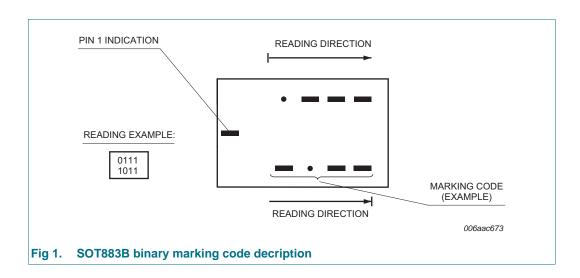
# 4. Marking

Table 4. Marking codes

Type number	Marking code <sup>[1]</sup>
PBSS3515MB	0001 0011

[1] For SOT883B binary marking code description see Figure 1

# 4.1 Binary marking code description



PBSS3515MB

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# 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-15	V
$V_{CEO}$	collector-emitter voltage	open base		-	-15	V
$V_{EBO}$	emitter-base voltage	open collector		-	-6	V
I <sub>C</sub>	collector current			-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-1	Α
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1][2]	-	250	mW
			[3][2]	-	590	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

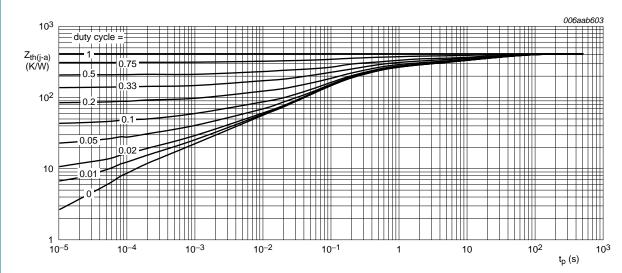
<sup>[3]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

# 6. Thermal characteristics

Table 6. Thermal characteristics

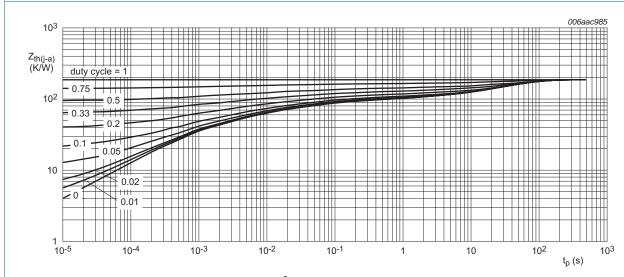
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance	in free air	[1][2]	-	-	500	K/W
	from junction to ambient		[3][2]	-	-	212	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Reflow soldering is the only recommented soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>

Fig 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

PBSS3515MB

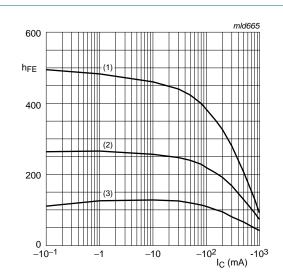
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# 7. Characteristics

#### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = -15 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-100	nΑ
	current	V <sub>CB</sub> = -15 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -2 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	200	-	-	
		$V_{CE} = -2 \text{ V; } I_{C} = -100 \text{ mA; pulsed;} $ $t_{p} \le 300 \text{ µs; } \delta \le 0.02 \text{ ; } T_{amb} = 25 \text{ °C}$	150	-	-	
		$V_{CE} = -2 \text{ V; } I_{C} = -500 \text{ mA; pulsed;} $ $t_{p} \le 300 \text{ µs; } \delta \le 0.02 \text{ ; } T_{amb} = 25 \text{ °C}$	90	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C$ = -10 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-	-25	mV
		$I_C = -200 \text{ mA}$ ; $I_B = -10 \text{ mA}$ ; pulsed; $t_p \le 300 \text{ µs}$ ; $\delta \le 0.02$ ; $T_{amb} = 25 \text{ °C}$	-	-	-150	mV
		$I_{C}$ = -500 mA; $I_{B}$ = -50 mA; pulsed; $t_{p} \le 300 \text{ µs}; \delta \le 0.02 ; T_{amb} = 25 ^{\circ}C$	-	-	-250	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_{C}$ = -500 mA; $I_{B}$ = -50 mA; pulsed; $t_{p} \le 300  \mu\text{s}; \ \delta \le 0.02 ; T_{amb} = 25 ^{\circ}\text{C}$	-	-	500	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C}$ = -500 mA; $I_{B}$ = -50 mA; pulsed; $t_{p} \le 300 \text{ µs}; \delta \le 0.02; T_{amb} = 25 ^{\circ}C$	-	-	-1.1	V
$V_{BEon}$	base-emitter turn-on voltage	$V_{CE} = -2 \text{ V; } I_{C} = -100 \text{ mA; pulsed;} $ $t_{p} \le 300 \text{ µs; } \delta \le 0.02 \text{ ; } T_{amb} = 25 \text{ °C}$	-	-	-0.9	V
f <sub>T</sub>	transition frequency	$V_{CE} = -5 \text{ V}; I_{C} = -100 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	100	280	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-	-	10	pF



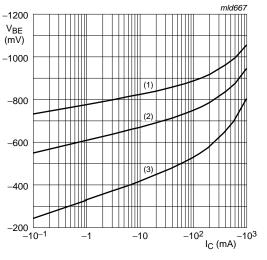
$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Fig 4. DC current gain as a function of collector current; typical values



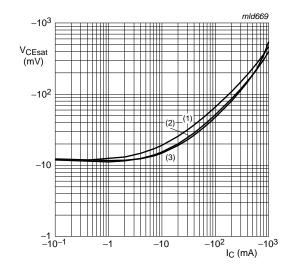
$$V_{CE} = -2 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig 5. Base-emitter voltage as a function of collector current; typical values



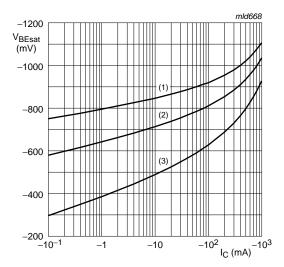
$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Fig 6. Collector-emitter saturation voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig 7. Base-emitter saturation voltage as a function of collector current; typical values

PBSS3515MB

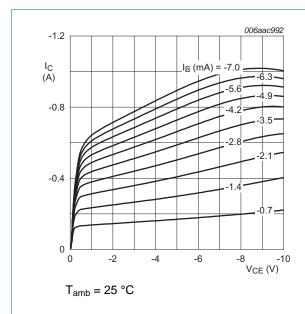
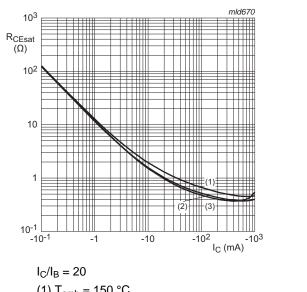


Fig 8. Collector current as a function of collector-emitter voltage; typical values



(1)  $T_{amb} = 150 \text{ °C}$ (2)  $T_{amb} = 25 \text{ °C}$ (3)  $T_{amb} = -55 \text{ °C}$ 

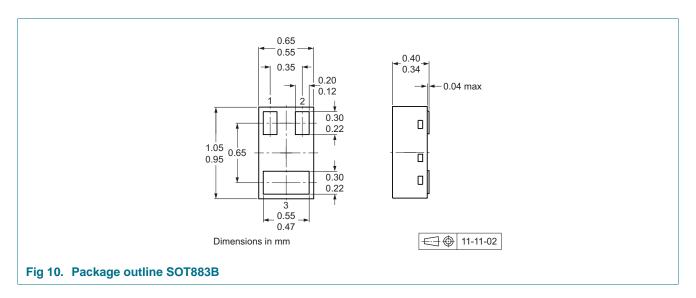
Fig 9. Collector-emitter equivalent on-resistance as a function of collector current; typical values

## 8. Test information

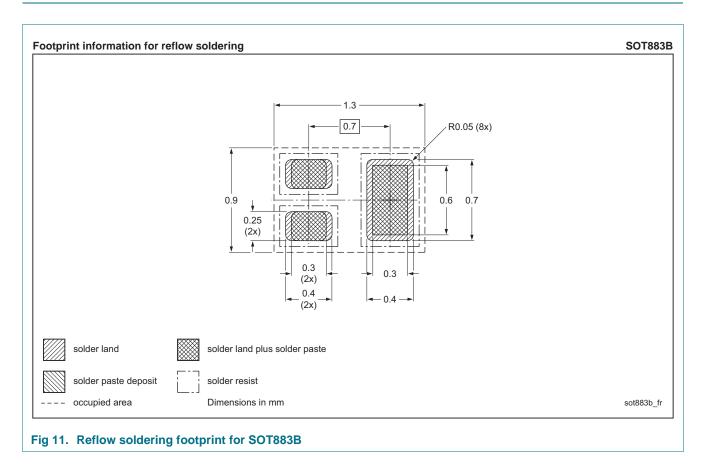
## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 9. Package outline



# 10. Soldering



# 11. Revision history

## Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PBSS3515MB v.1	20120306	Product data sheet	-	-

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Document status[1] [2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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# 14. Contents

Product profile	1
General description	1
Features and benefits	1
Applications	1
Quick reference data	1
Pinning information	2
Ordering information	2
Marking	2
Limiting values	3
Thermal characteristics	4
Characteristics	5
Test information	7
Quality information	7
Package outline	8
Soldering	8
Revision history	9
Legal information	10
Data sheet status	10
Definitions	10
Disclaimers	10
Trademarks	11
Contact information	11
	Features and benefits Applications Quick reference data Pinning information Ordering information Marking. Limiting values. Thermal characteristics Characteristics. Test information Quality information Package outline Soldering Revision history Legal information. Data sheet status Definitions. Disclaimers Trademarks.

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