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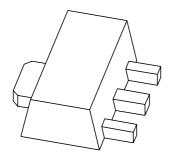
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Team Nexperia

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DATA SHEET



PBSS5350X 50 V, 3 A PNP low V_{CEsat} (BISS) transistor

Product data sheet Supersedes data of 2003 Nov 21 2004 Nov 04



50 V, 3 A PNP low V_{CEsat} (BISS) transistor

PBSS5350X

FEATURES

- SOT89 (SC-62) package
- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}
- · Higher efficiency leading to less heat generation
- Reduced printed-circuit board requirements.

APPLICATIONS

- Power management
 - DC/DC converters
 - Supply line switching
 - Battery charger
 - LCD backlighting.
- · Peripheral drivers
 - Driver in low supply voltage applications (e.g. lamps and LEDs).
 - Inductive load driver (e.g. relays, buzzers and motors).

DESCRIPTION

PNP low V_{CEsat} transistor in a SOT89 plastic package. NPN complement: PBSS4350X.

MARKING

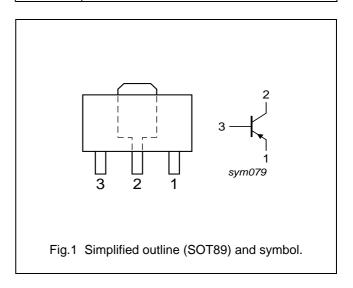
TYPE NUMBER	MARKING CODE
PBSS5350X	S46

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage	-50	V
I _C	collector current (DC)	-3	Α
I _{CM}	peak collector current -5		Α
R _{CEsat}	equivalent on-resistance	135	mΩ

PINNING

PIN	DESCRIPTION	
1	emitter	
2	collector	
3	base	



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ORDERING INFORMATION

TYPE NUMBER	PACKAGE				
TIPE NOWIBER	NAME	NAME DESCRIPTION VERSION			
PBSS5350X	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89		

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

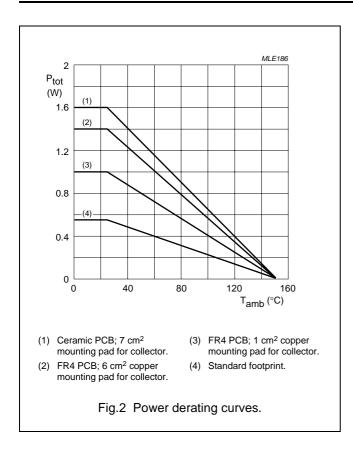
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-50	V
V _{CEO}	collector-emitter voltage	open base	_	-50	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)	note 4	_	-3	А
I _{CM}	peak collector current	limited by T _{j(max)}	_	-5	Α
I _B	base current (DC)		_	-0.5	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
		note 1	_	550	mW
		note 2	_	1	W
		note 3	_	1.4	W
		note 4	_	1.6	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Notes

- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- 3. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 6 cm².
- 4. Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.

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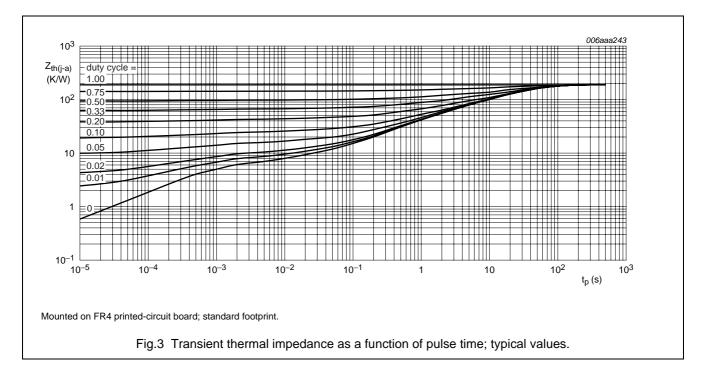
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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	in free air		
		note 1	225	K/W
		note 2	125	K/W
		note 3	90	K/W
		note 4	80	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		16	K/W

Notes

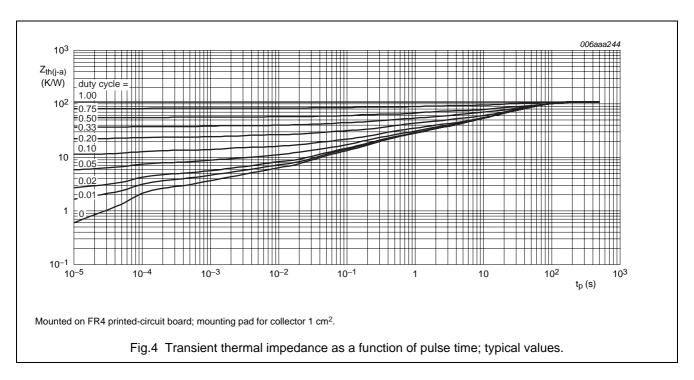
- 1. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; standard footprint.
- 2. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- 3. Device mounted on a FR4 printed-circuit board; single-sided copper; tin-plated; mounting pad for collector 6 cm².
- 4. Device mounted on a ceramic printed-circuit board 7 cm², single-sided copper, tin-plated.

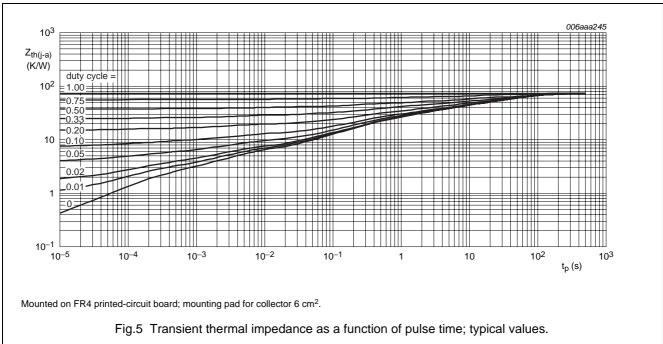


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CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}$	_	_	-100	nA
		$V_{CB} = -50 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	_	_	-50	μΑ
I _{CES}	collector-emitter cut-off current	$V_{CE} = -50 \text{ V}; V_{BE} = 0 \text{ V}$	-	_	-100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}$	-	_	-100	nA
h _{FE}	DC current gain	V _{CE} = −2 V				
		$I_C = -0.1 \text{ A}$	200	_	_	
		$I_{\rm C} = -0.5 \text{ A}$	200	_	_	
		I _C = −1 A; note 1	200	_	450	
		I _C = −2 A; note 1	130	_	-	
		$I_{C} = -3 \text{ A}$; note 1	80	_	_	
V _{CEsat}	collector-emitter saturation	$I_C = -0.5 \text{ A}; I_B = -50 \text{ mA}$	-	_	-90	mV
	voltage	$I_C = -1 \text{ A}; I_B = -50 \text{ mA}$	-	_	-180	mV
		$I_C = -2 \text{ A}; I_B = -100 \text{ mA}$	-	_	-320	mV
		$I_C = -2 \text{ A}$; $I_B = -200 \text{ mA}$; note 1	-	_	-270	mV
		$I_C = -3 \text{ A}$; $I_B = -300 \text{ mA}$; note 1	-	_	-390	mV
R _{CEsat}	equivalent on-resistance	$I_C = -2 \text{ A}$; $I_B = -200 \text{ mA}$; note 1	-	90	135	mΩ
V _{BEsat}	base-emitter saturation voltage	$I_C = -2 \text{ A}; I_B = -100 \text{ mA}$	-	_	-1.1	V
		$I_C = -3 \text{ A}$; $I_B = -300 \text{ mA}$; note 1	-	_	-1.2	V
V _{BEon}	base-emitter turn-on voltage	er turn-on voltage $V_{CE} = -2 \text{ V}; I_{C} = -1 \text{ A}$		_	_	V
f _T	transition frequency	$I_C = -100 \text{ mA}; V_{CE} = -5 \text{ V};$ f = 100 MHz	100	_	_	MHz
C _c	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}$	_	_	35	pF

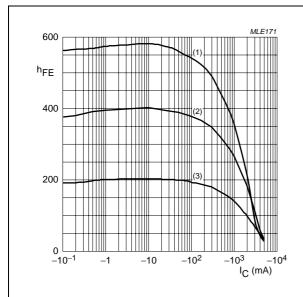
Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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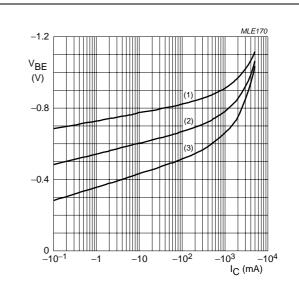
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 $V_{CE} = -2 V$.

- (1) $T_{amb} = 100 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

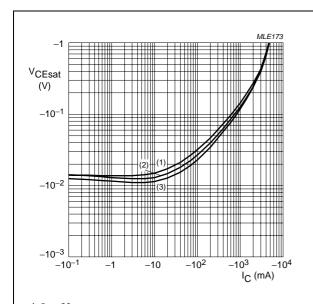
Fig.6 DC current gain as a function of collector current; typical values.



 $V_{CE} = -2 V$.

- (1) $T_{amb} = -55 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) T_{amb} = 100 °C.

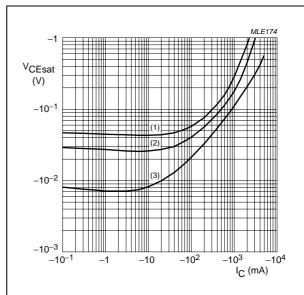
Fig.7 Base-emitter voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B}=20.$

- (1) T_{amb} = 100 °C.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -55 \, ^{\circ}C$.

Fig.8 Collector-emitter saturation voltage as a function of collector current; typical values.



 $T_{amb} = 25 \, ^{\circ}C.$

- (1) $I_C/I_B = 100$
- (2) $I_C/I_B = 50$.
- (3) $I_C/I_B = 10$.

Fig.9 Collector-emitter saturation voltage as a function of collector current; typical values.

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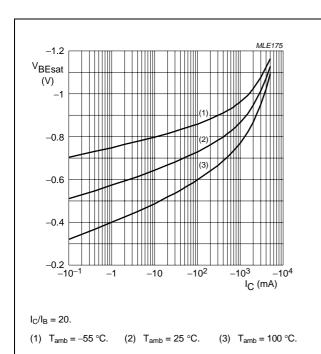


Fig.10 Base-emitter saturation voltage as a function of collector current; typical values.

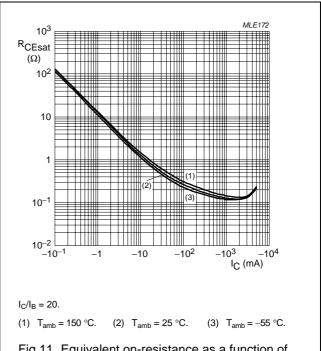
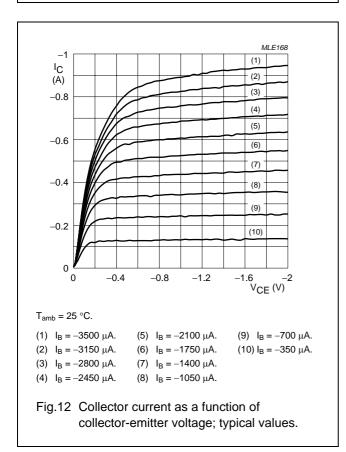
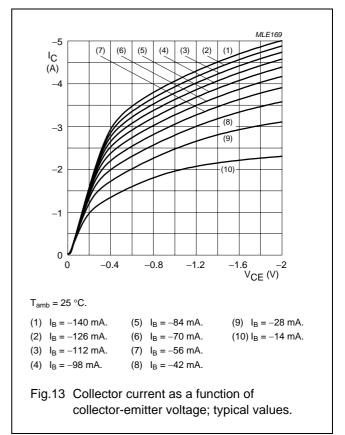


Fig.11 Equivalent on-resistance as a function of collector current; typical values.





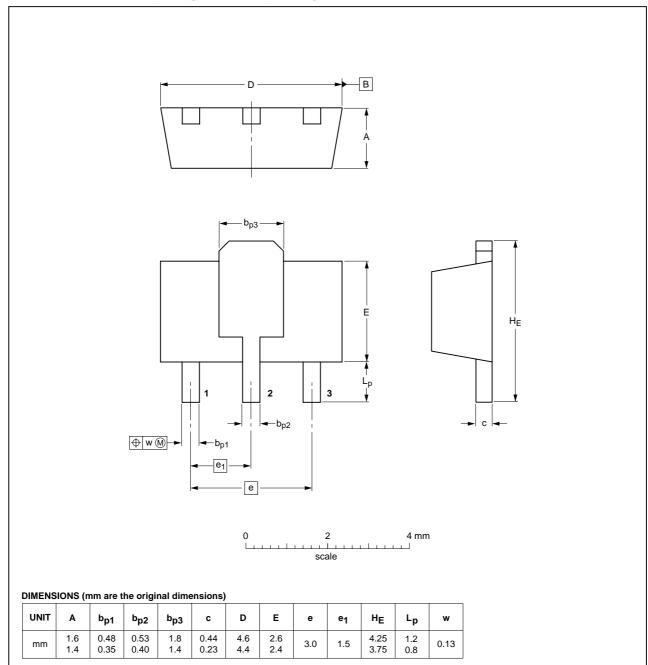
50 V, 3 A PNP low V_{CEsat} (BISS) transistor

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PACKAGE OUTLINE

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE	TLINE REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	1330E DATE
SOT89		TO-243	SC-62			-04-08-03- 06-03-16

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NXP Semiconductors

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This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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