

40 V low VCEsat PNP transistor 20 September 2019

**Product data sheet** 

### 1. General description

PNP low V<sub>CEsat</sub> transistor in a SOT223 plastic package. NPN complement: PBSS4540Z.

#### 2. Features and benefits

- Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation.
- AEC-Q101 qualified •

#### 3. Applications

- Supply line switching circuits
- Battery management applications •
- DC/DC converter applications
- · Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers)
- MOSFET driver applications.

#### 4. Quick reference data

#### Table 1. Quick reference data

| Symbol             | Parameter                    | Conditions   |  | Min | Тур | Max | Unit |
|--------------------|------------------------------|--|--|-----|-----|-----|------|
| V <sub>CEO</sub>   | collector-emitter<br>voltage | open base  |  | -   | -   | -40 | V    |
| I <sub>C</sub>     | collector current            |  |  | -   | -   | -5  | А    |
| I <sub>CM</sub>    | peak collector current       | single pulse; t <sub>p</sub> ≤ 1 ms  |  | -   | -   | -10 | A    |
| R <sub>CEsat</sub> |                              | $I_{C}$ = -2 A; $I_{B}$ = -200 mA; $t_{p} \le 300 \ \mu$ s;<br>pulsed; δ ≤ 0.02; $T_{amb}$ = 25 °C |  | -   | 55  | 80  | mΩ   |

### 5. Pinning information

| Pin | Symbol | Description | Simplified outline            | Graphic symbol |
|-----|--------|-------------|-------------------------------|----------------|
| 1   | В      | base        | 4                             | Ç              |
| 2   | С      | collector   |                               | вщ             |
| 3   | E      | emitter     |                               |                |
| 4   | С      | collector   | []1 []2 []3<br>SC-73 (SOT223) | E<br>sym132    |



#### 6. Ordering information

| Table 3. Ordering information |         |  |         |  |  |
|-------------------------------|---------|--|---------|--|--|
| Type number                   | Package |  |         |  |  |
|                               | Name    | Description  | Version |  |  |
| PBSS5540Z                     | SC-73   | plastic surface-mounted package with increased heatsink; 4 leads | SOT223  |  |  |

#### 7. Marking

| Table 4. Marking codes |              |
|------------------------|--------------|
| Type number            | Marking code |
| PBSS5540Z              | PB5540       |

#### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                 | Conditions                          |     | Min | Max  | Unit |
|------------------|---------------------------|-------------------------------------|-----|-----|------|------|
| V <sub>CBO</sub> | collector-base voltage    | open emitter                        |     | -   | -40  | V    |
| V <sub>CEO</sub> | collector-emitter voltage | open base                           |     | -   | -40  | V    |
| V <sub>EBO</sub> | emitter-base voltage      | open collector                      |     | -   | -6   | V    |
| I <sub>C</sub>   | collector current         |                                     |     | -   | -5   | А    |
| I <sub>CM</sub>  | peak collector current    | single pulse; t <sub>p</sub> ≤ 1 ms |     | -   | -10  | А    |
| I <sub>BM</sub>  | peak base current         |                                     |     | -   | -2   | А    |
| P <sub>tot</sub> | total power dissipation   | T <sub>amb</sub> ≤ 25 °C            | [1] | -   | 1.35 | W    |
|                  |                           |                                     | [2] | -   | 2    | W    |
| Tj               | junction temperature      |                                     |     | -   | 150  | °C   |
| T <sub>amb</sub> | ambient temperature       |                                     |     | -65 | 150  | °C   |
| T <sub>stg</sub> | storage temperature       |                                     |     | -65 | 150  | °C   |

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup> [1]

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>. [2]

#### 9. Thermal characteristics

#### Table 6. Thermal characteristics Symbol Parameter Conditions

| Symbol                                 | Parameter               | Conditions  |     | Min | Тур | Max | Unit |
|--|-------------------------|-------------|-----|-----|-----|-----|------|
| ······································ | thermal resistance from | in free air | [1] | -   | -   | 92  | K/W  |
|  | junction to ambient     |             | [2] | -   | -   | 62  | K/W  |

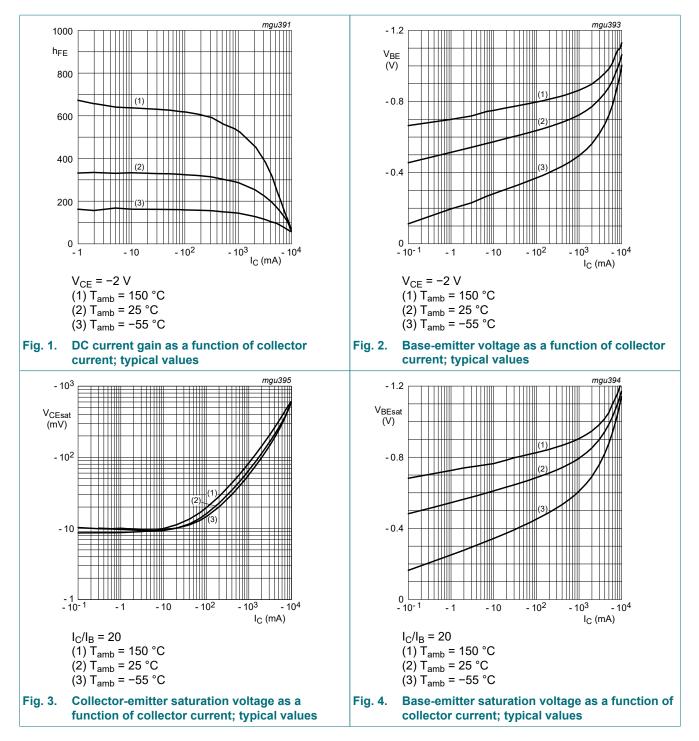
Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup> [1]

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>. [2]

# **10. Characteristics**

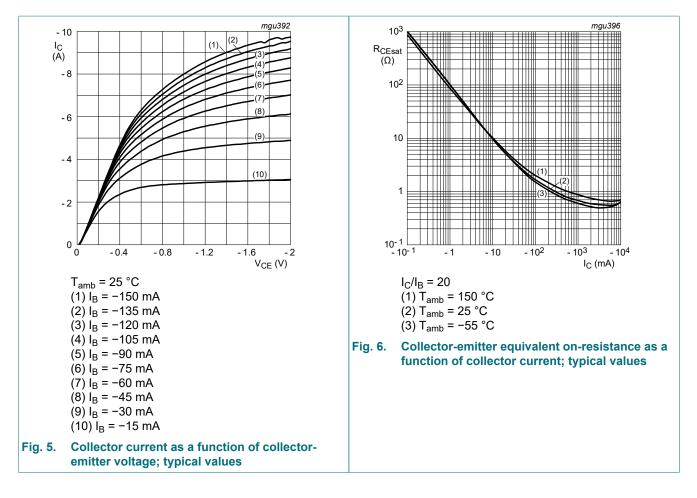
| Symbol               | Parameter   | Conditions  | Min | Тур  | Мах   | Unit |
|----------------------|---|---|-----|------|-------|------|
| V <sub>(BR)CBO</sub> | collector-base<br>breakdown voltage                   | I <sub>C</sub> = -100 μA; I <sub>E</sub> = 0 A  | -40 | -    | -     | V    |
| V <sub>(BR)CEO</sub> | collector-emitter<br>breakdown voltage                | I <sub>C</sub> = -10 mA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C   | -40 | -    | -     | V    |
| V <sub>(BR)EBO</sub> | emitter-base<br>breakdown voltage<br>(collector open) | I <sub>E</sub> = -100 μA; I <sub>B</sub> = 0 mA; T <sub>amb</sub> = 25 °C   | -6  | -    | -     | V    |
| I <sub>CBO</sub>     | collector-base cut-off                                | V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C   | -   | -    | -100  | nA   |
|                      | current   | V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C  | -   | -    | -50   | μA   |
| I <sub>EBO</sub>     | emitter-base cut-off current                          | $V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}; \text{ T}_{amb} = 25 \text{ °C}$                                 | -   | -    | -100  | nA   |
| h <sub>FE</sub>      | DC current gain                                       | V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA; T <sub>amb</sub> = 25 °C  | 250 | 350  | -     |      |
|                      |   | $V_{CE}$ = -2 V; I <sub>C</sub> = -1 A; t <sub>p</sub> ≤ 300 µs;<br>pulsed; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C | 200 | 300  | -     |      |
|                      |   | $V_{CE}$ = -2 V; I <sub>C</sub> = -2 A; t <sub>p</sub> ≤ 300 µs;<br>pulsed; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C | 150 | 250  | -     |      |
|                      |   |   | 50  | 150  | -     |      |
| V <sub>CEsat</sub>   | collector-emitter                                     | $I_{C}$ = -500 mA; $I_{B}$ = -5 mA; $T_{amb}$ = 25 °C   | -   | -80  | -120  | mV   |
|                      | saturation voltage                                    | I <sub>C</sub> = -1 A; I <sub>B</sub> = -10 mA; T <sub>amb</sub> = 25 °C  | -   | -120 | -170  | mV   |
|                      |   | $I_{C}$ = -2 A; $I_{B}$ = -200 mA; $T_{amb}$ = 25 °C  | -   | -110 | -160  | mV   |
|                      |   | I <sub>C</sub> = -5 A; I <sub>B</sub> = -500 mA; T <sub>amb</sub> = 25 °C   | -   | -250 | -375  | mV   |
| R <sub>CEsat</sub>   | collector-emitter saturation resistance               | $I_C$ = -2 A; $I_B$ = -200 mA; $t_p$ ≤ 300 μs;<br>pulsed; δ ≤ 0.02; $T_{amb}$ = 25 °C                                 | -   | 55   | 80    | mΩ   |
| V <sub>BEsat</sub>   | base-emitter saturation voltage                       | I <sub>C</sub> = -5 A; I <sub>B</sub> = -500 mA; T <sub>amb</sub> = 25 °C   | -   | -    | -1.3  | V    |
| V <sub>BEon</sub>    | base-emitter turn-on voltage                          | $V_{CE}$ = -2 V; I <sub>C</sub> = -2 A; T <sub>amb</sub> = 25 °C  | -   | -0.8 | -1.25 | V    |
| f <sub>T</sub>       | transition frequency                                  | $V_{CE}$ = -10 V; I <sub>C</sub> = -100 mA; f = 100 MHz;<br>T <sub>amb</sub> = 25 °C                                  | 60  | 120  | -     | MHz  |
| C <sub>c</sub>       | collector capacitance                                 | V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A;<br>f = 1 MHz; T <sub>amb</sub> = 25 °C           | -   | 90   | 105   | pF   |

#### 40 V low VCEsat PNP transistor



**Product data sheet** 

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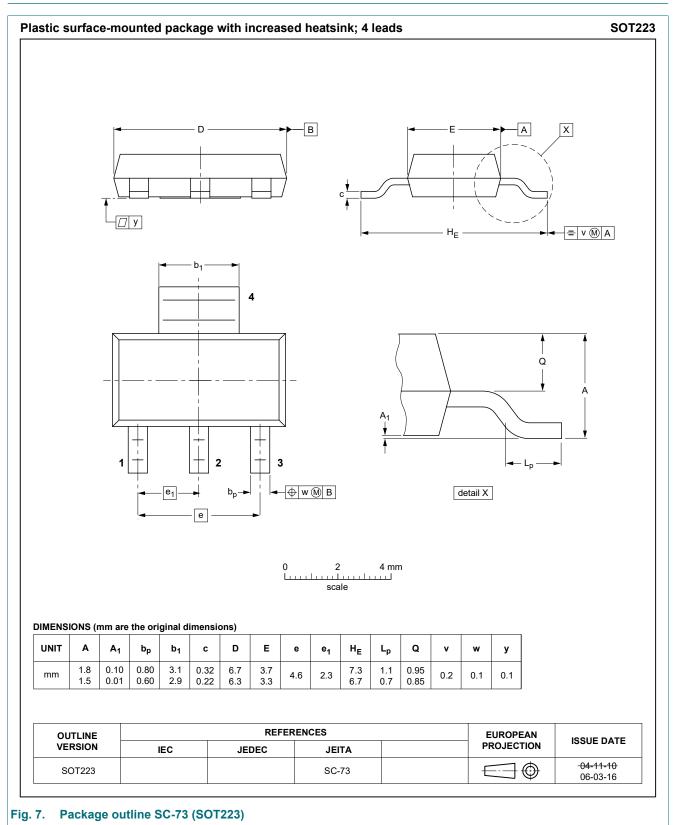
### **11. Test information**

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

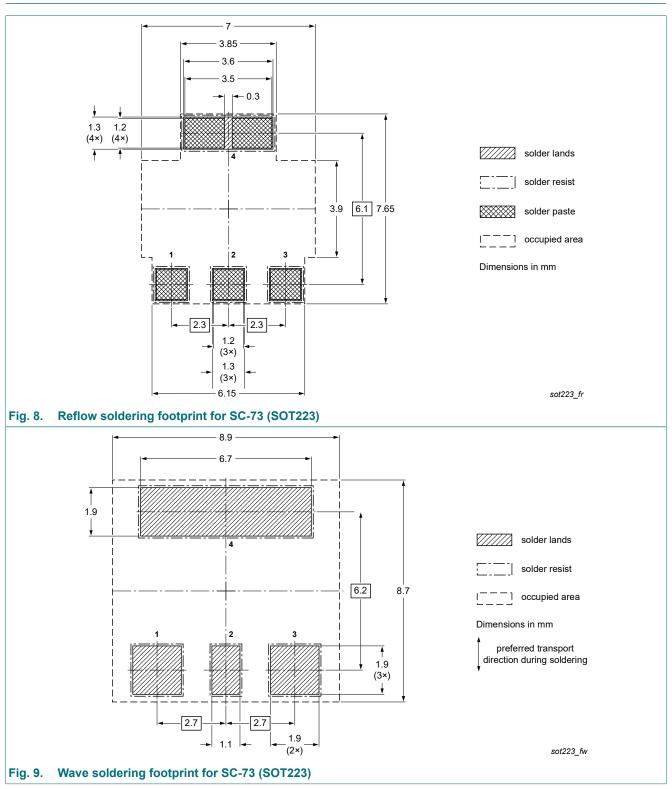
#### 40 V low VCEsat PNP transistor

# 12. Package outline



#### 40 V low VCEsat PNP transistor

### 13. Soldering



PBSS5540Z

# 14. Revision history

| Data sheet ID  | Release date   | Data sheet status  | Change notice | Supersedes     |  |  |
|----------------|--|--------------------|---------------|----------------|--|--|
| PBSS5540Z v.3  | 20190920   | Product data sheet | -             | PBSS5540Z v.2  |  |  |
| Modifications: | <ul> <li>Automotive AEC-Q101 qualification added in sections "features and benefits", "Test information" and "Legal information"</li> <li>Characteristics: breakdown voltages added</li> <li>The format of this data sheet has been redesigned to comply with the identity guidelines Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul> |                    |               |                |  |  |
|                | 00040004   | Product data sheet | _             | PBSS5540Z v.1  |  |  |
| PBSS5540Z v.2  | 20010921   | FIDUUCI UAIA SHEEL |               | 1 00000402 0.1 |  |  |

**Product data sheet** 

40 V low VCEsat PNP transistor

### 15. Legal information

#### **Data sheet status**

| Document status<br>[1][2]         | Product<br>status [3] | Definition  |
|-----------------------------------|-----------------------|---|
| Objective [short]<br>data sheet   | Development           | This document contains data from<br>the objective specification for<br>product development. |
| Preliminary [short]<br>data sheet | Qualification         | This document contains data from the preliminary specification.                             |
| Product [short]<br>data sheet     | Production            | This document contains the product specification.   |

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Product data sheet

20 September 2019

#### Contents

| 1.  | General description     | .1  |
|-----|-------------------------|-----|
| 2.  | Features and benefits   | . 1 |
| 3.  | Applications            | . 1 |
| 4.  | Quick reference data    | .1  |
| 5.  | Pinning information     | .1  |
| 6.  | Ordering information    | .2  |
| 7.  | Marking                 | . 2 |
| 8.  | Limiting values         | . 2 |
| 9.  | Thermal characteristics | . 2 |
| 10. | Characteristics         | . 3 |
| 11. | Test information        | . 5 |
| 12. | Package outline         | . 6 |
|     | Soldering               |     |
| 14. | Revision history        | .8  |
|     | Legal information       |     |
|     |                         |     |

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