

## 1. General description

Ultra low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in a DFN1006-2 (SOD882) leadless ultra small Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

## 2. Features and benefits

- Bidirectional ESD protection of one line
- High reverse standoff voltage V<sub>RWM</sub> = 30 V
- Ultra-low diode capacitance C<sub>d</sub> = 0.27 pF
- Ultra small plastic package 1.0 x 0.6 x 0.48 mm
- ESD protection up to 12 kV; IEC 61000-4-2
- AEC-Q101 qualified

## 3. Applications

- NFC antenna protection
- Protection of high-speed data lines

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit	
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	30	V	
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	-	1	A	
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; T <sub>amb</sub> = 25 °C	[1]	-	6.5	-	V	

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

## 5. Pinning information

Table 2. Pinning information					
Pin	Symbol	Description	Simplified outline	Graphic symbol	
1	K1	cathode (diode 1)			
2	K2	cathode (diode 2)		sym045	
			Transparent top view		
			DFN1006-2 (SOD882)		



## 6. Ordering information

Table 3. Ordering information						
Type number	Package	Package				
	Name	Description	Version			
PESD30VF1BL	DFN1006-2	DFN1006-2: leadless ultra small plastic package; 2 terminals	SOD882			

### 7. Marking

Table 4. Marking codes				
Type number	Marking code			
PESD30VF1BL	L7			

## 8. Limiting values

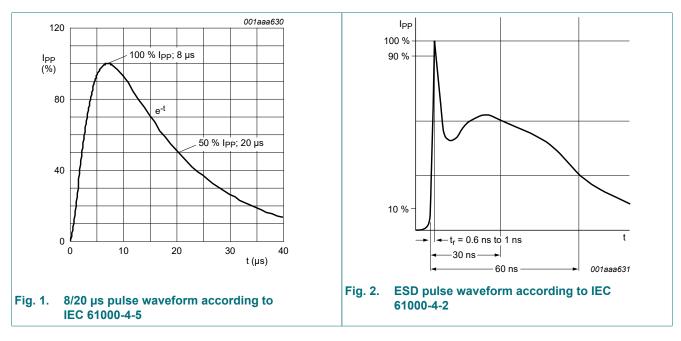
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	1	A
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximum	ratings					
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2]	-	12	kV

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

[2] Device stressed with ten non-repetitive ESD pulses.

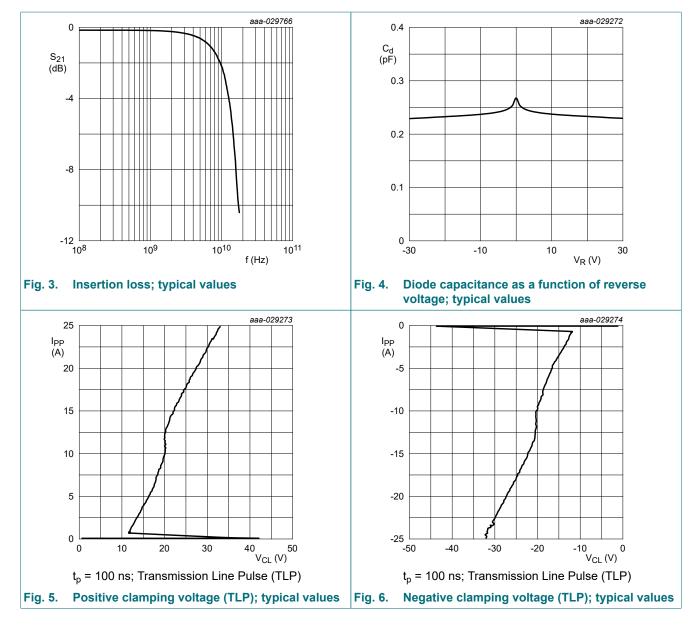


## 9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	30	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C		31	34	39	V
I <sub>RM</sub>	reverse leakage current	V <sub>R</sub> = 30 V; T <sub>amb</sub> = 25 °C		-	0.1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	0.27	0.4	pF
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; T <sub>amb</sub> = 25 °C	[1]	-	6.5	-	V
		I <sub>PP</sub> = 16 A; T <sub>amb</sub> = 25 °C	[2]	-	23	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 7.5 A; T <sub>amb</sub> = 25 °C	[2]	-	0.7	-	Ω

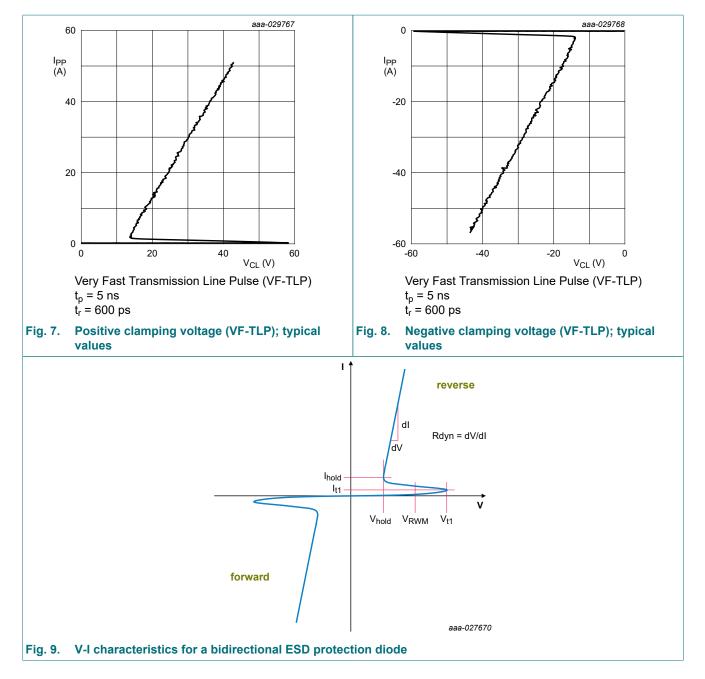
[1]

Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5. Non-repetitive current pulse, Transmission Line Pulse (TLP) tp = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008. [2]



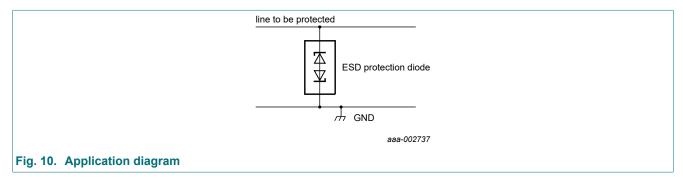
# PESD30VF1BL

#### **Bidirectional ESD protection diode**



## **10.** Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

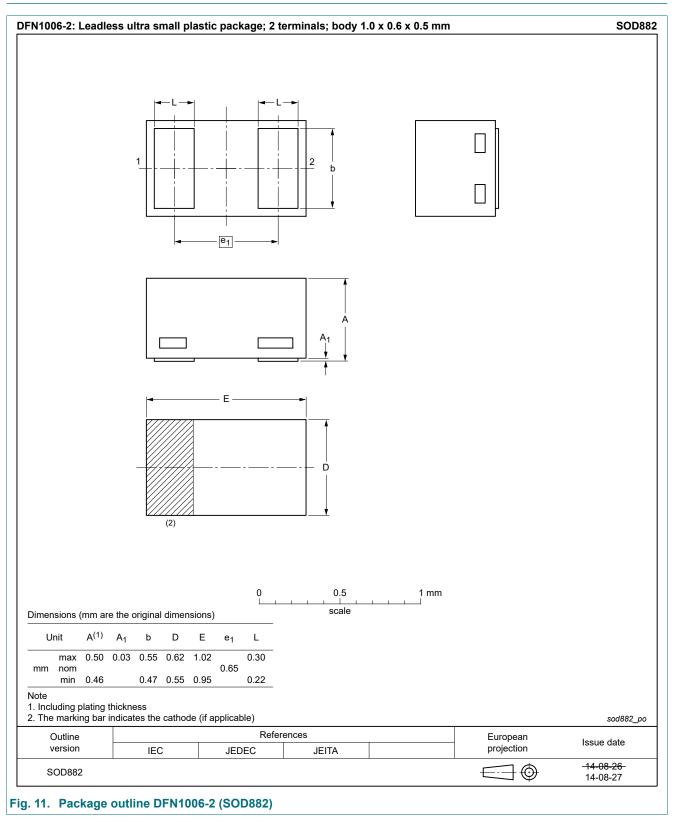
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## **11. Test information**

### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

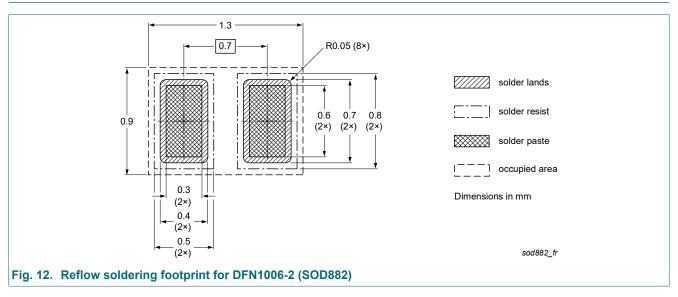
## 12. Package outline



# PESD30VF1BL

### **Bidirectional ESD protection diode**

# 13. Soldering



# 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD30VF1BL v.2	20190408	Product data sheet	-	PESD30VF1BL v.1	
Modifications:	<ul> <li>Change document status to "Product data sheet"</li> <li>Added AEC-Q101 qualification status</li> <li>Section Characteristics: updated and enhanced according to latest measurements</li> </ul>				
PESD30VF1BL v.1	20181211	Preliminary data sheet	-	-	

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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#### **Bidirectional ESD protection diode**

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# Contents

1.	General description	.1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	.1
5.	Pinning information	.1
6.	Ordering information	.2
7.	Marking	. 2
8.	Limiting values	2
9.	Characteristics	. 3
10.	Application information	5
11.	Test information	. 5
12.	Package outline	. 6
13.	Soldering	. 7
14.	Revision history	.8
15.	Legal information	.9

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