Product data sheet

1. General description

4-fold bidirectional ElectroStatic Discharge (ESD) protection array designed to protect up to four lines from the damage caused by ESD and other transients.

The device is housed in a leadless extremely thin small DFN1308-6 (SOT8006) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- · Bidirectional ESD protection of up to 4 lines
- Very high surge robustness; I_{PP} = 6 A for 8/20 μs pulse
- Very low clamping voltage: V_{CL} = 7.3 V typ. for 6 A 8/20 μs pulse
- · ESD protection up to 20 kV
- Very low dynamic resistance $R_{dyn} = 0.2 \Omega (TLP)$

3. Applications

ESD protection for low-speed lines in portable communication, consumer devices and computing devices.

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|------------------|--------------------------|--|-----|-----|-----|-----|------|
| V _{RWM} | reverse standoff voltage | T _{amb} = 25 °C | | - | - | 3.3 | V |
| I _{PPM} | rated peak pulse current | $t_p = 8/20 \ \mu s; T_{amb} = 25 \ ^{\circ}C$ | [1] | - | - | 6 | Α |
| V _{t1} | trigger voltage | T _{amb} = 25 °C | | - | 6.7 | - | V |

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------------|---------------------|----------------|
| 1 | K1 | cathode (diode 1) | 1 2 3 | |
| 2 | CC | common cathode | | K1 - |
| 3 | K2 | cathode (diode 2) | 6 5 4 | cc cc |
| 4 | K3 | cathode (diode 3) | DFN1308-6 (SOT8006) | к2 |
| 5 | CC | common cathode | | aaa-030022 |
| 6 | K4 | cathode (diode 4) | | |

6. Ordering information

Table 3. Ordering information

| Type number Package | | | | | |
|---------------------|------|--|---------|--|--|
| | Name | Description | Version | | |
| PESD3V3L4BHC | | DFN1308-6, plastic, leadless extremely thin small package; 6 terminals; body 1.3 x 0.8 x 0.38 mm | SOT8006 | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|--------------|--------------|
| PESD3V3L4BHC | L4 |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134)

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|--------------------------|--|-----|-----|-----|------|
| I _{PPM} | rated peak pulse current | t _p = 8/20 μs; T _{amb} = 25 °C | [1] | - | 6 | Α |
| Tj | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |
| ESD maximum i | ratings | | | | | |
| V _{ESD} | electrostatic discharge | IEC 61000-4-2 (contact discharge) | [2] | _ | 20 | kV |
| | voltage | IEC 61000-4-2 (air discharge) | [2] | - | 20 | kV |

^[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.

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^[2] Device stressed with ten non-repetitive ESD pulses.

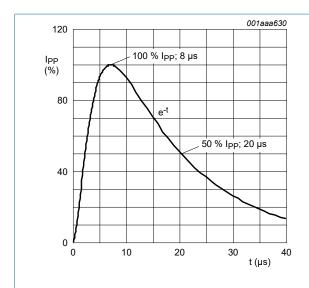


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5

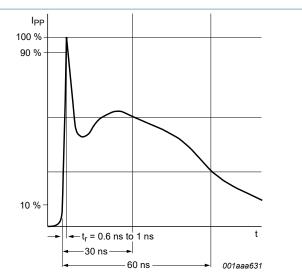


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|------------------|--------------------------|---|-----|-----|-----|-----|------|
| V_{RWM} | reverse standoff voltage | T _{amb} = 25 °C | | - | - | 3.3 | V |
| I _{RM} | reverse leakage current | V _{RWM} = 3.3 V; T _{amb} = 25 °C | | - | 3 | 100 | nA |
| C _d | diode capacitance | f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C | | - | 7.2 | 9 | pF |
| V _{CL} | clamping voltage | I _{PPM} = 1 A; t _p = 8/20 μs; T _{amb} = 25 °C | [1] | - | 5.9 | - | V |
| | | $I_{PPM} = 6 \text{ A}; t_p = 8/20 \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$ | [1] | - | 7.3 | 8.5 | V |
| R _{dyn} | dynamic resistance | I _R = 10 A; T _{amb} = 25 °C | [2] | - | 0.2 | - | Ω |
| V _{t1} | trigger voltage | T _{amb} = 25 °C | | - | 6.7 | - | V |
| V _h | holding voltage | | | 4 | - | - | V |

^[1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.

^[2] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI/ESD STM5.5.1-2008.

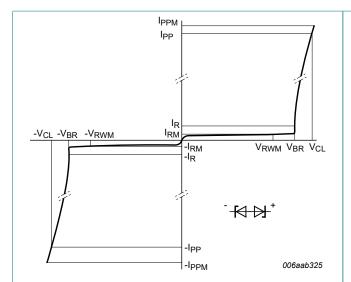


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

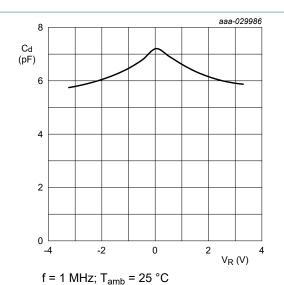


Fig. 4. Diode capacitance as a function of reverse

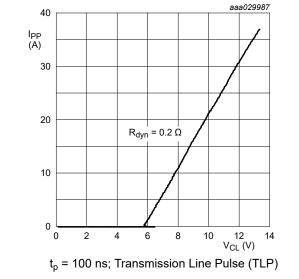


Fig. 5. Positive clamping voltage (TLP); typical values

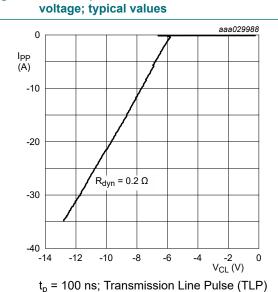
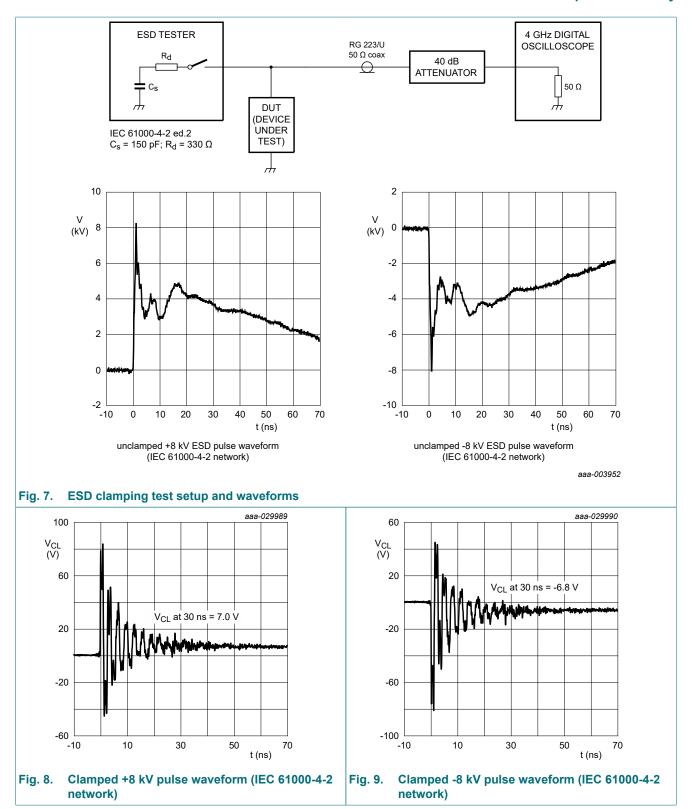
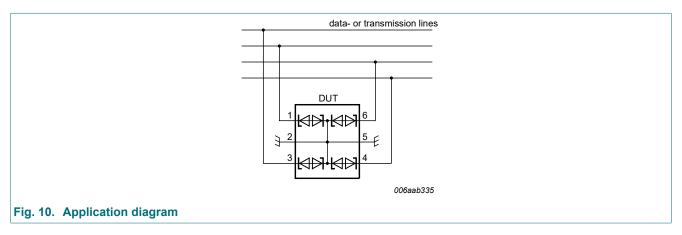


Fig. 6. Negative clamping voltage (TLP); typical values



10. Application information

The device is designed for protection of up to 4 bidirectional data lines from the damage caused by ESD and surge pulses. The device is suitable on lines where the signal polarities are above or below ground.

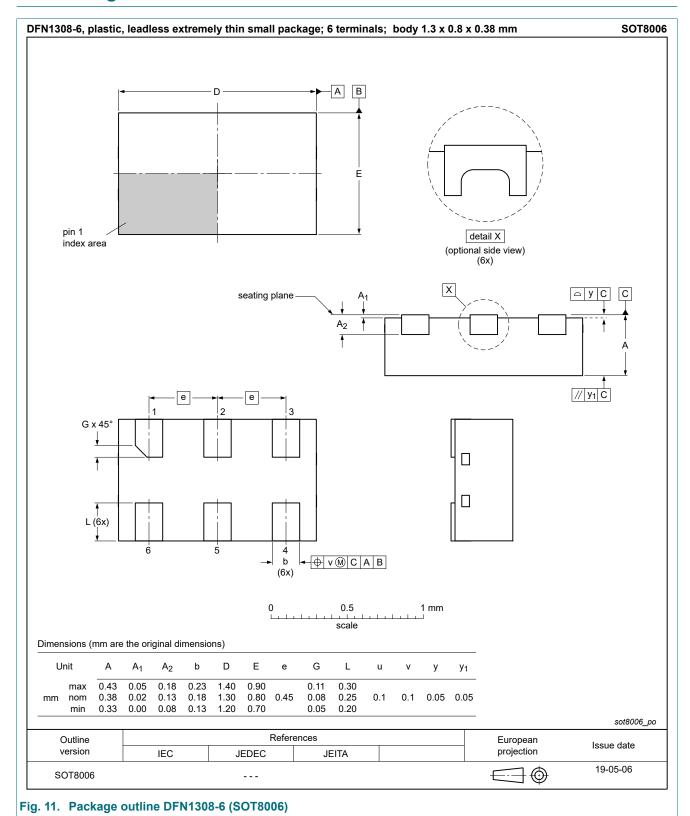


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

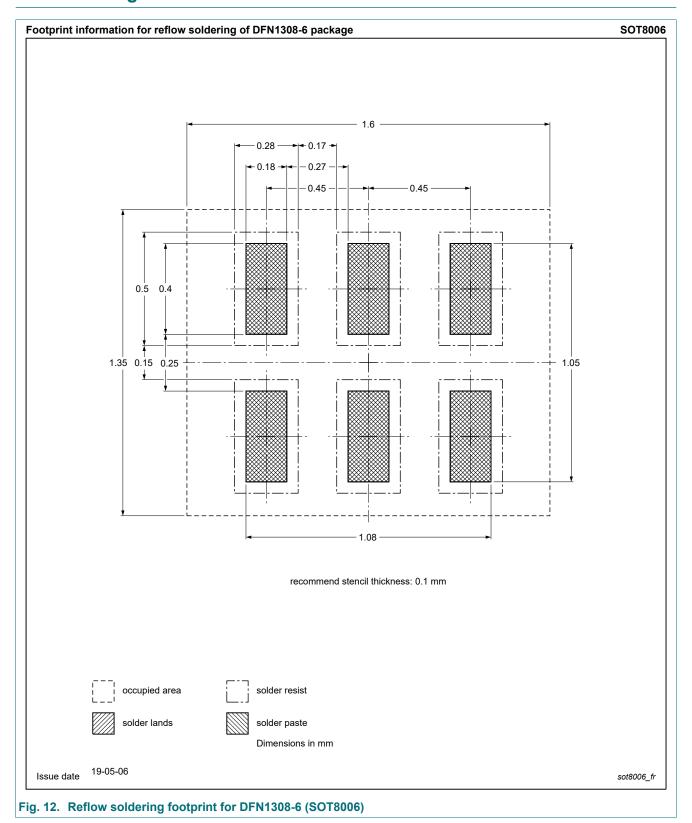
- Place the device as close to the input terminal or connector as possible
- · Minimize the path length between the device and the protected line.
- · Keep parallel signal paths to a minimum.
- Avoid running protected conductors in parallel with unprotected conductors.
- Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- · Minimize the length of the transient return path to ground.
- · Avoid using shared transient return paths to a common ground point.
- Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline



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12. Soldering



13. Revision history

Table 7. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|------------------|--------------|--------------------|---------------|------------|
| PESD3V3L4BHC v.1 | 20190607 | Product data sheet | - | - |

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14. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- Please consult the most recently issued document before initiating or completing a design.
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