

# PESD5V0V2BM

Very low capacitance bidirectional ESD protection diodes

14 August 2015

Product data sheet

## 1. General description

Two bidirectional ElectroStatic Discharge (ESD) protection diodes designed to protect two signal lines from damage caused by ESD and other transients. The device is housed in a DFN1006-3 (SOT883) leadless ultra small Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Bidirectional ESD protection of two lines
- Ultra small SMD plastic package
- ESD protection up to 30 kV; IEC 61000-4-2
- I<sub>PPM</sub> = 9 A; IEC 61000-4-5 (surge)
- Ultra low leakage current: I<sub>RM</sub> = 1 nA
- AEC-Q101 qualified

## 3. Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Communication systems
- Portable electronics

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	18	20	pF



## **Pinning information**

#### Table 2. **Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode	1 🔲	1 1
2	K2	cathode	2 3	2 51 51
3	K3	common cathode	Transparent top view	006aab331
			DFN1006-3 (SOT883)	

#### **Ordering information** 6.

#### Ordering information Table 3.

Tubio 0. Ordoning information					
Type number	Package				
	Name	Description	Version		
PESD5V0V2BM	DFN1006-3	DFN1006-3: leadless ultra small plastic package; 3 solder lands	SOT883		

#### **Marking 7**.

#### Table 4. **Marking codes**

Type number	Marking code
PESD5V0V2BM	M2

#### **Limiting values** 8.

#### **Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1][2]	-	9	Α
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximun	n ratings					
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[1][3]	-	30	kV
		IEC 61000-4-2; air discharge	[1][3]	-	30	kV
		MIL-STD-883; human body model	[1]	-	10	kV

- Measured from pin 1 or 2 to pin 3. According to IEC 61000-4-5 and IEC 61643-321.
- Device stressed with ten non-repetitive ESD pulses.

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Nexperia PESD5V0V2BM

### Very low capacitance bidirectional ESD protection diodes

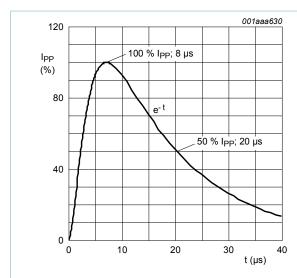


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5 and IEC 61643-321

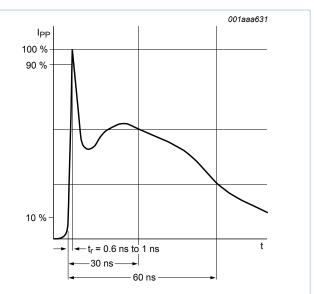


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

### 9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5 V; T <sub>amb</sub> = 25 °C	[1]	-	1	10	nA
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C	[1]	5.5	6.8	7.8	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	18	20	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C; t <sub>p</sub> = 8/20 μs	[1][2]	-	8	9.5	V
		$I_{PPM}$ = 9 A; $T_{amb}$ = 25 °C; $t_p$ = 8/20 µs	[1][2]	-	11	12.5	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[1][3]	-	0.15	-	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- [2] According to IEC 61000-4-5 and IEC 61643-321.
- [3] Non-repetitive current pulse, Transmission Line Pulse (TLP) t<sub>p</sub> = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.

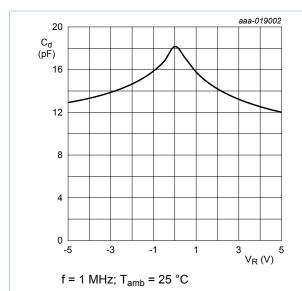


Fig. 3. Diode capacitance as a function of reverse voltage; typical values

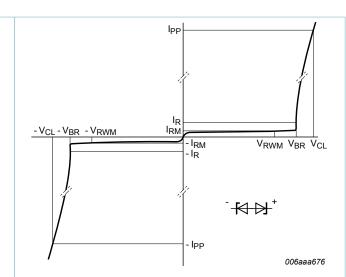


Fig. 4. V-I characteristics for a bidirectional ESD protection diode

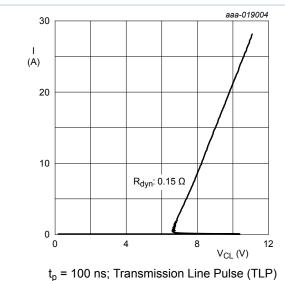
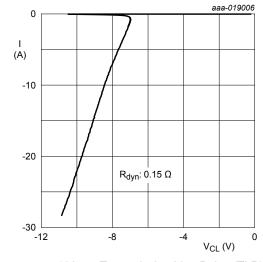
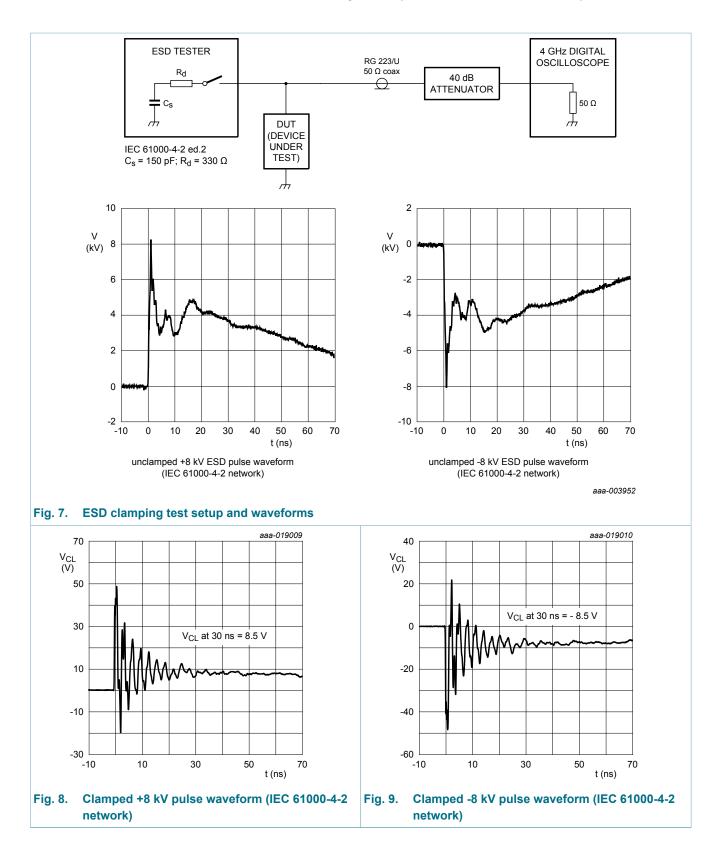


Fig. 5. Dynamic resistance



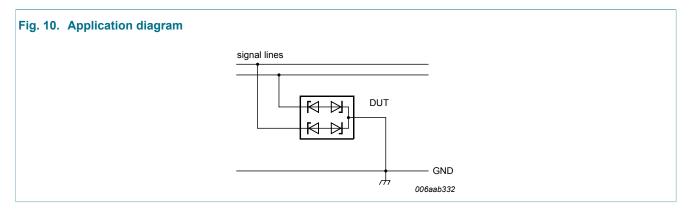
 $t_p$  = 100 ns; Transmission Line Pulse (TLP)

Fig. 6. Dynamic resistance



## 10. Application information

The device is designed for the protection of up to two bidirectional data lines from surge pulses and ESD damage.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

#### 11. Test information

#### 11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

## 12. Package outline

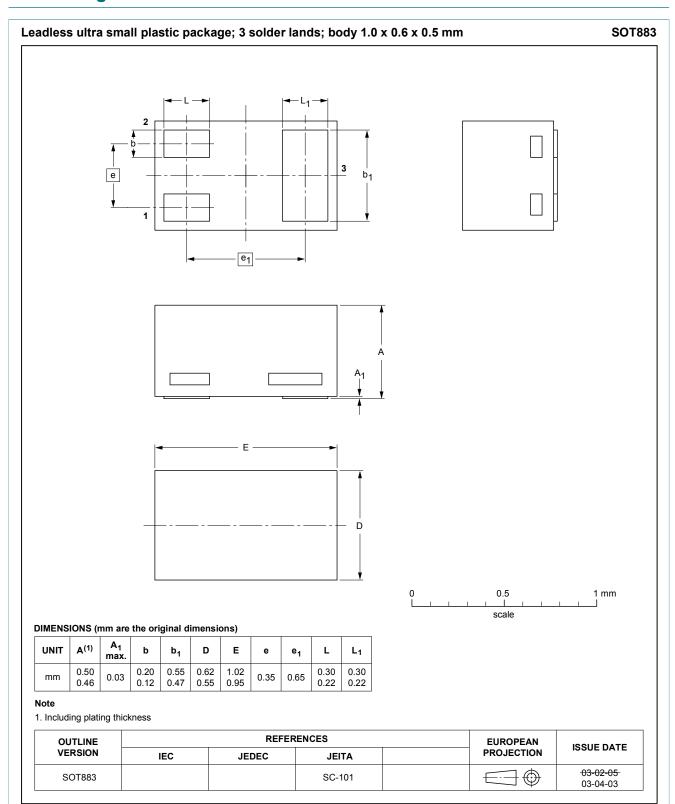
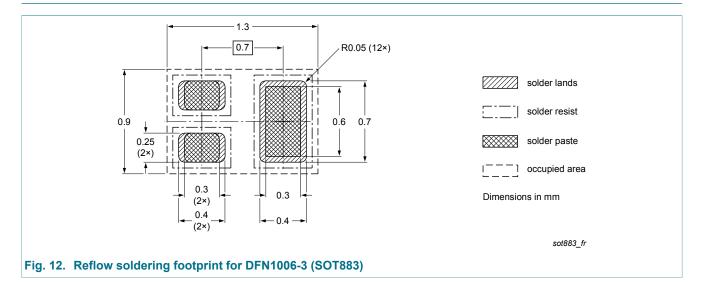


Fig. 11. Package outline DFN1006-3 (SOT883)

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## 13. Soldering



## 14. Revision history

#### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0V2BM v.1	20150814	Product data sheet	-	-

## 15. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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