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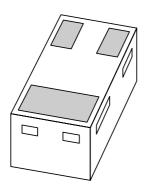
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Kind regards,

Team Nexperia

## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# PESDxL2UM series Low capacitance double ESD protection diode

Product data sheet Supersedes data of 2003 Aug 05 2005 May 23



# Low capacitance double ESD protection diode

### **PESDxL2UM** series

#### **FEATURES**

- Uni-directional ESD protection of two lines or bi-directional ESD protection of one line
- Reverse standoff voltage 3.3 and 5 V
- · Low diode capacitance
- · Ultra low leakage current
- Leadless ultra small SOT883 surface mount package  $(1 \times 0.6 \times 0.5 \text{ mm})$
- Board space 1.17 mm<sup>2</sup> (approx. 10% of SOT23)
- ESD protection >15 kV
- IEC 61000-4-2; level 4 (ESD); 15 kV (air) or 8 kV (contact).

#### **APPLICATIONS**

- · Cellular handsets and accessories
- Portable electronics
- · Computers and peripherals
- · Communication systems
- · Audio and video equipment.

#### **MARKING**

TYPE NUMBER	MARKING CODE
PESD3V3L2UM	F2
PESD5V0L2UM	F1

#### **DESCRIPTION**

Low capacitance ESD protection diode in a three pad SOT883 leadless ultra small plastic package designed to protect up to two transmission or data lines from ElectroStatic Discharge (ESD) damage.

#### **PINNING**

PIN	DESCRIPTION	
1	cathode 1	
2	cathode 2	
3	common anode	

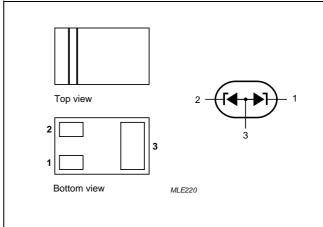


Fig.1 Simplified outline (SOT883) and symbol.

# Low capacitance double ESD protection diode

### PESDxL2UM series

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
I <sub>pp</sub>	peak pulse current	8/20 μs pulse; notes 1, 2 and 3			
	PESD3V3L2UM		_	3	Α
	PESD5V0L2UM		_	2.5	Α
P <sub>pp</sub>	peak pulse power	8/20 μs pulse; notes 1, 2 and 3	_	30	W
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> = 1 ms; square pulse	_	3.5	Α
I <sub>ZSM</sub>	non-repetitive peak reverse current	t <sub>p</sub> = 1 ms; square pulse			
	PESD3V3L2UM		_	0.9	Α
	PESD5V0L2UM		_	0.8	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C; note 4	_	250	mW
P <sub>ZSM</sub>	non-repetitive peak reverse power dissipation	t <sub>p</sub> = 1 ms; square pulse; see Fig.4	_	6	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
ESD	electrostatic discharge	IEC 61000-4-2 (contact discharge)	15	-	kV
		HBM MIL-Std 883	10	_	kV

#### **Notes**

- 1. Non-repetitive current pulse 8/20 μs exponential decay waveform; see Fig.5.
- 2. Pins 1 and 3 or 2 and 3.
- 3. Pins 1 and 2.
- 4. Device mounted on standard printed-circuit board.

#### **ESD** standards compliance

IEC 61000-4-2, level 4 (ESD)	>15 kV (air); >8 kV (contact)
HBM MIL-Std 883, class 3	>4 kV

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	all diodes loaded; note 1	500	K/W
		one diode loaded; note 2	290	K/W

#### Notes

- 1. Refer to SOT883 standard mounting conditions (footprint), FR4 with 60  $\mu m$  copper strip line.
- 2. FR4 single-sided copper 1 cm<sup>2</sup>.

# Low capacitance double ESD protection diode

## PESDxL2UM series

#### **ELECTRICAL CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per diode		<u> </u>			•	•
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 200 mA	_	1	1.2	V
V <sub>RWM</sub>	reverse stand-off voltage					
	PESD3V3L2UM		_	_	3.3	V
	PESD5V0L2UM		_	_	5	V
I <sub>RM</sub>	reverse leakage current					
	PESD3V3L2UM	V <sub>R</sub> = 3.3 V	_	75	300	nA
	PESD5V0L2UM	$V_R = 5 V$	_	5	25	nA
V <sub>(CL)R</sub>	clamping voltage	8/20 μs pulse				
	PESD3V3L2UM	$I_{pp} = 1 A$ ; notes 1 and 2	_	_	8	V
		$I_{pp} = 3 A$ ; notes 1 and 2	_	_	12	V
		$I_{pp} = 1 A$ ; notes 1 and 3	_	_	9	V
		$I_{pp} = 3 \text{ A}$ ; notes 1 and 3	_	_	13	V
	PESD5V0L2UM	$I_{pp} = 1 \text{ A}$ ; notes 1 and 2	_	_	10	V
		$I_{pp} = 2.5 \text{ A}$ ; notes 1 and 2	_	_	13	V
		$I_{pp} = 1 A$ ; notes 1 and 3	_	_	11	V
		$I_{pp} = 2.5 \text{ A}$ ; notes 1 and 3	_	_	15	V
$V_{BR}$	breakdown voltage	$I_Z = 1 \text{ mA}$				
	PESD3V3L2UM		5.32	5.6	5.88	V
	PESD5V0L2UM		6.46	6.8	7.14	V
Sz	temperature coefficient	$I_Z = 1 \text{ mA}$				
	PESD3V3L2UM		_	1.3	_	mV/K
	PESD5V0L2UM		_	2.9	_	mV/K
r <sub>diff</sub>	differential resistance	I <sub>R</sub> = 1 mA				
	PESD3V3L2UM		_	_	200	Ω
	PESD5V0L2UM		_	_	100	Ω
C <sub>d</sub>	diode capacitance					
	PESD3V3L2UM	$f = 1 \text{ MHz}; V_R = 0$	_	22	28	pF
		f = 1 MHz; V <sub>R</sub> = 5	_	12	17	pF
	PESD5V0L2UM	$f = 1 \text{ MHz}; V_R = 0$	_	16	19	pF
		f = 1 MHz; V <sub>R</sub> = 5	_	8	11	pF

#### Notes

- 1. Non-repetitive current pulse 8/20  $\mu s$  exponential decay waveform; see Fig.5.
- 2. Pins 1 and 3 or 2 and 3.
- 3. Pins 1 and 2.

# Low capacitance double ESD protection diode

### PESDxL2UM series

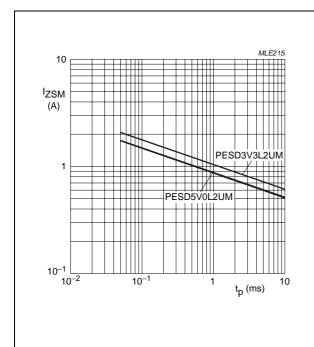
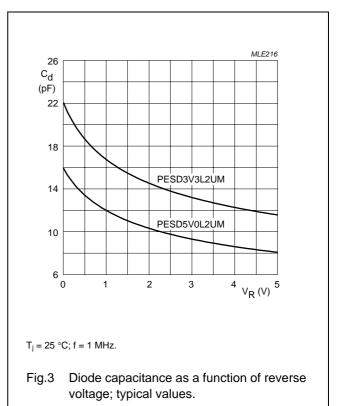
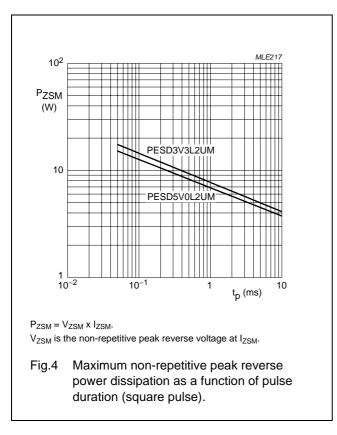
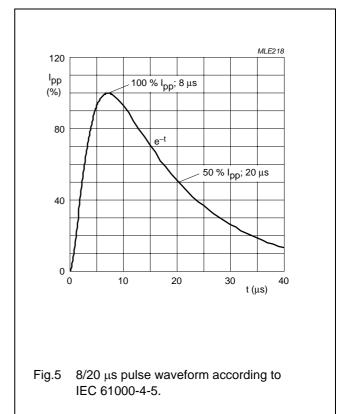


Fig.2 Non-repetitive peak reverse current as a function of pulse time (square pulse).

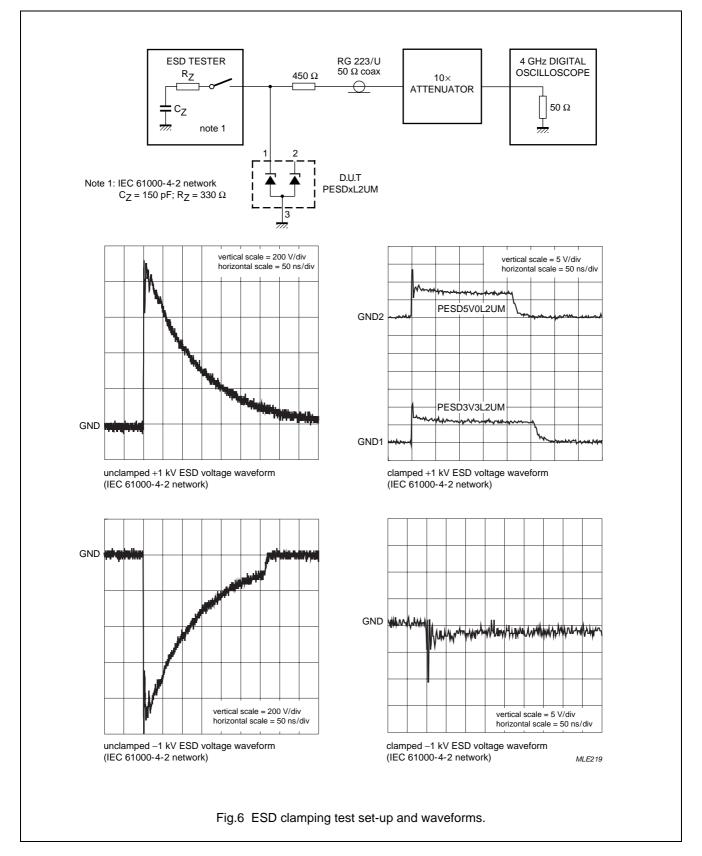






# Low capacitance double ESD protection diode

### PESDxL2UM series



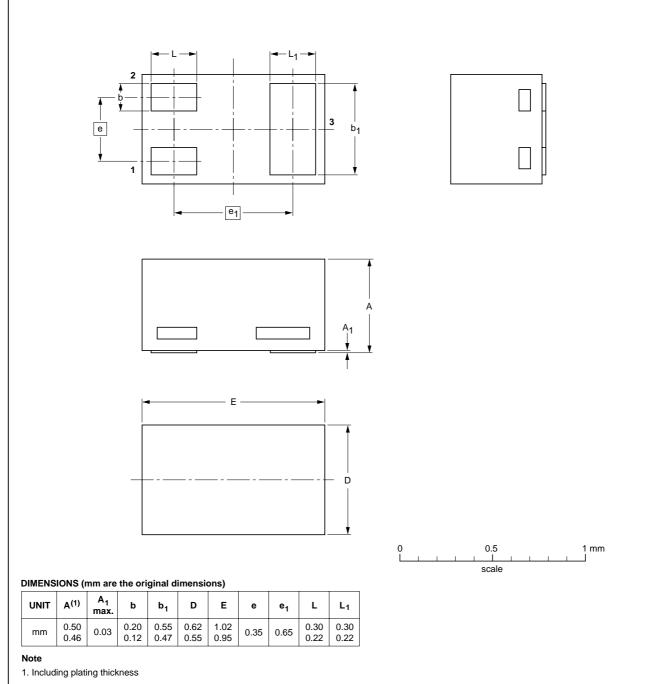
# Low capacitance double ESD protection diode

### PESDxL2UM series

#### **PACKAGE OUTLINE**

Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.5 mm

**SOT883** 



OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT883			SC-101			<del>03-02-05</del> 03-04-03

# Low capacitance double ESD protection diode

### PESDxL2UM series

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
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For additional information please visit:  $\mbox{\bf http://www.nxp.com}$ 

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