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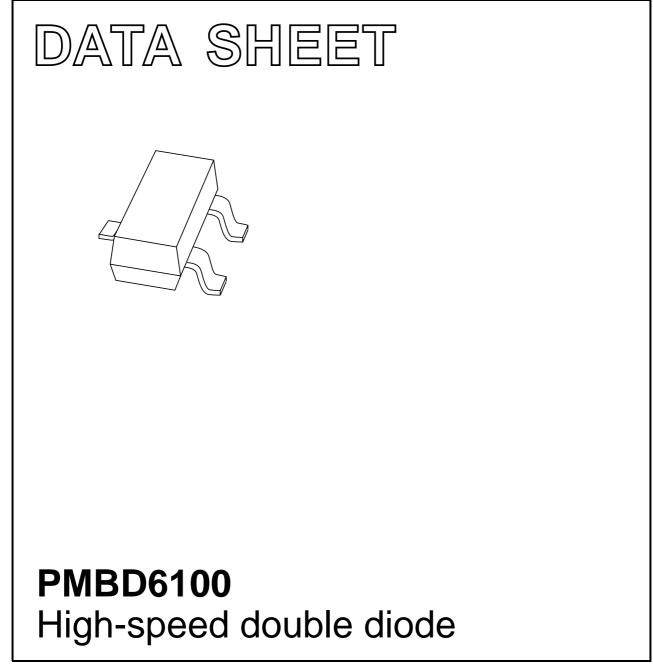
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Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1999 May 11 2003 Mar 25



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V _{RRM}	repetitive peak reverse voltage		-	85	V
V _R	continuous reverse voltage		-	70	V
l _F	continuous forward current	single diode loaded; note 1; see Fig.2	_	215	mA
		double diode loaded; note 1; see Fig.2	_	125	mA

High-speed double diode

FEATURES

Small plastic SMD package

General applicationContinuous reverse voltage:

• High switching speed: max. 4 ns

- Continuous reverse voltage: max. 70 V
- Repetitive peak reverse voltage: max. 85 V
- Repetitive peak forward current: max. 450 mA.

APPLICATIONS

• High-speed switching in surface mounted circuits.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
PMBD6100	*5B	

Note

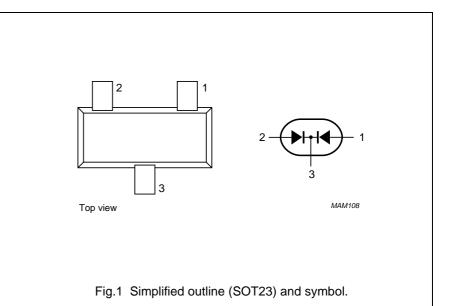
- 1. * = p : Made in Hong Kong.
- * = t : Made in Malaysia.
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DESCRIPTION

The PMBD6100 consists of two high-speed switching diodes with common cathodes, fabricated in planar technology, and encapsulated in the small SOT23 plastic SMD package.

PINNING

PIN	DESCRIPTION	
1	anode (a1)	
2	anode (a2)	
3	common cathode	



PMBD6100

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{FRM}	repetitive peak forward current		_	450	mA
I _{FSM}	non-repetitive peak forward current	square wave; T _j = 25 °C prior to surge; see Fig.4			
		t = 1 μs	-	4	А
		t = 1 ms	_	1	А
		t = 1 s	-	0.5	А
P _{tot}	total power dissipation	T _{amb} = 25 °C; note 1	-	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C

Note

1. Device mounted on an FR4 printed-circuit board.

ELECTRICAL CHARACTERISTICS

 T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V _F	forward voltage	see Fig.3			
		I _F = 1 mA	550	700	mV
		I _F = 10 mA	_	855	mV
		I _F = 50 mA	_	1	V
		I _F = 100 mA	0.85	1.1	V
I _R	reverse current	see Fig.5			
		V _R = 50 V	-	100	nA
		V _R = 50 V; T _j = 150 °C	-	50	μA
C _d	diode capacitance	$f = 1 \text{ MHz}; V_R = 0; \text{ see Fig.6}$	_	1.5	pF
t _{rr}	reverse recovery time	when switched from $I_F = 10$ mA to $I_R = 10$ mA; $R_L = 100 \Omega$; measured at $I_R = 1$ mA; see Fig.7	-	4	ns
V _{fr}	forward recovery voltage	when switched from $I_F = 10$ mA; $t_r = 20$ ns; see Fig.8	-	1.75	V

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-tp}	thermal resistance from junction to tie-point		360	K/W
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Device mounted on an FR4 printed-circuit board.

PMBD6100

GRAPHICAL DATA

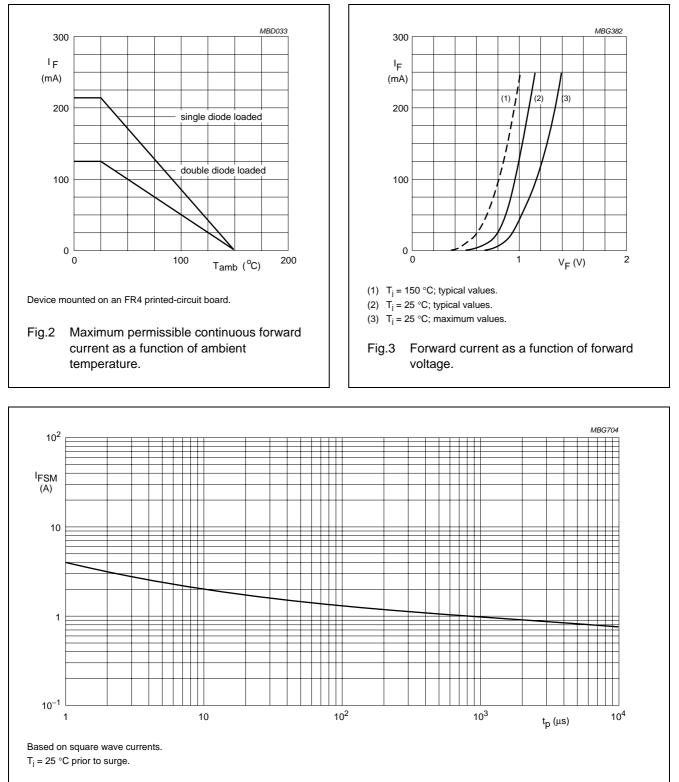
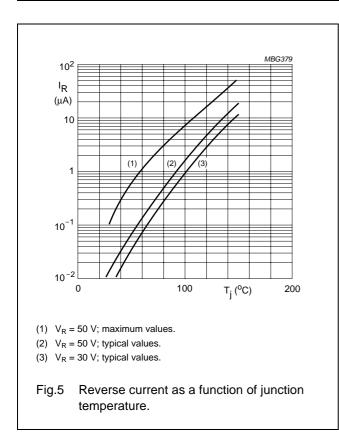
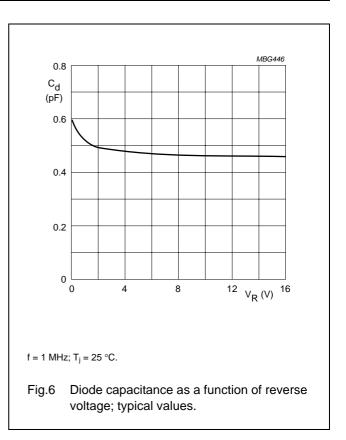
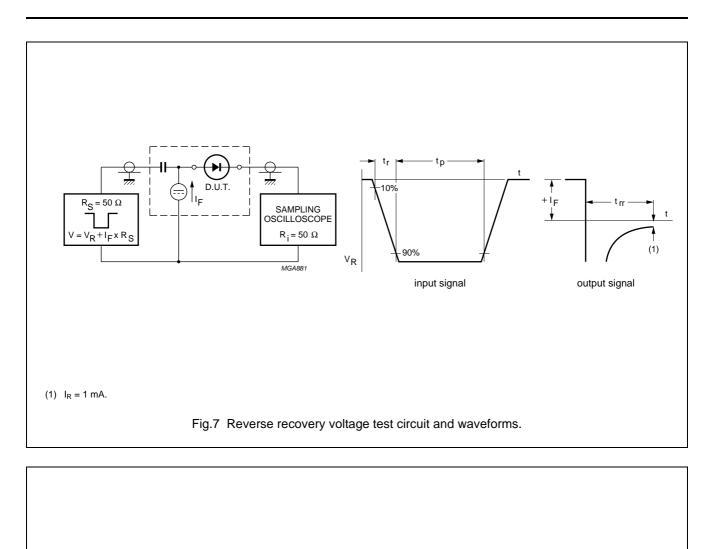
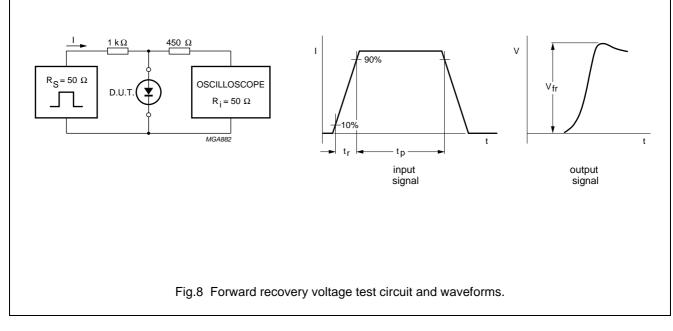


Fig.4 Maximum permissible non-repetitive peak forward current as a function of pulse duration.



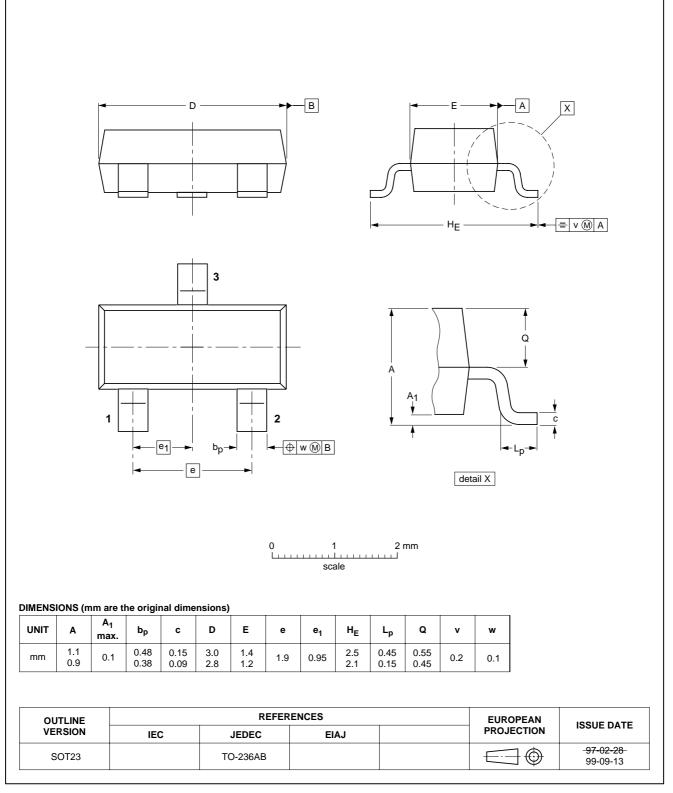






PACKAGE OUTLINE





SOT23

PMBD6100

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

DATA SHEET STATUS

Notes

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NXP Semiconductors

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Contact information

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