

Product data sheet

1. General description

PNP switching transistor in an ultra small DFN1010D-3 (SOT1215) leadless Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

NPN complement: PMBT2222AQA

2. Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 60V)
- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- AEC-Q101 qualified

3. Applications

- Switching and linear applications
- Mobile applications

4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-60	V
I _C	collector current			-	-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-800	mA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -150 mA	[1]	100	-	300	
		V _{CE} = -10 V; I _C = -500 mA	[1]	50	-	-	

[1] Pulsed test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$



5. Pinning information

Table 2.	Pinning int	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		Ċ
2	E	emitter		в
3	С	collector		
4	С	collector	Transparent top view DFN1010D-3 (SOT1215)	É sym132

6. Ordering information

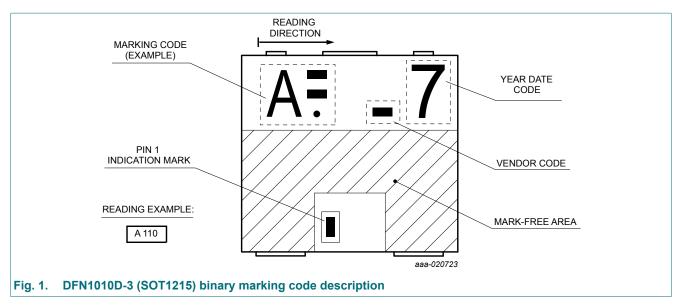
Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMBT2907AQA		plastic, leadless thermal enhanced ultra thin small outline package; 3 terminals; 0.75 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1215		

7. Marking

Table 4. Marking codes

Type number	Marking code
PMBT2907AQA	X 101



8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

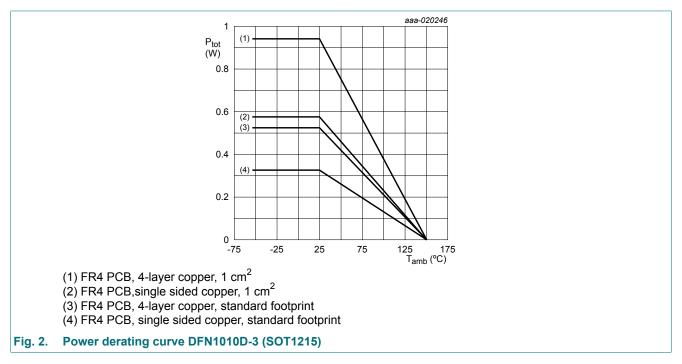
Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-60	V
V _{CEO}	collector-emitter voltage	open base		-	-60	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current	-		-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	325	mW
			[2]	-	575	mW
			[3]	-	525	mW
			[4]	-	940	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 1 cm².

[3] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.

[4] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated; mounting pad for collector 1 cm².



9. Thermal characteristics

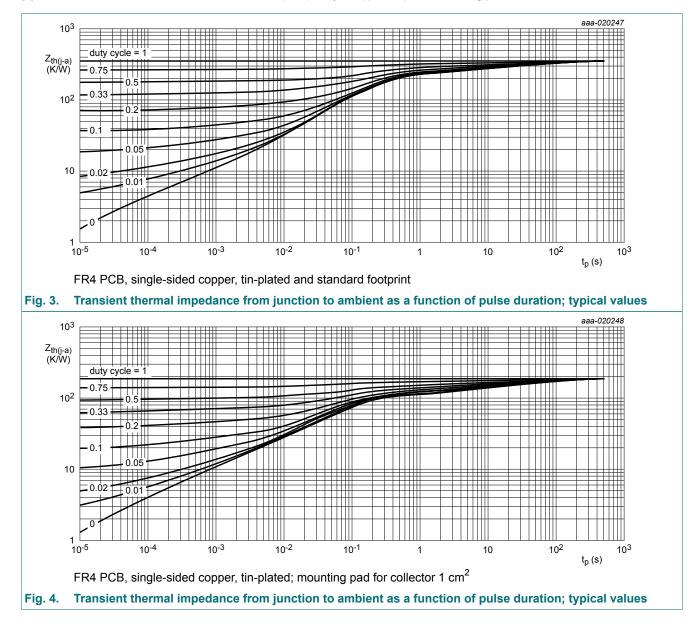
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	385	K/W
			[2]	-	-	218	K/W
			[3]	-	-	239	K/W
			[4]	-	-	133	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

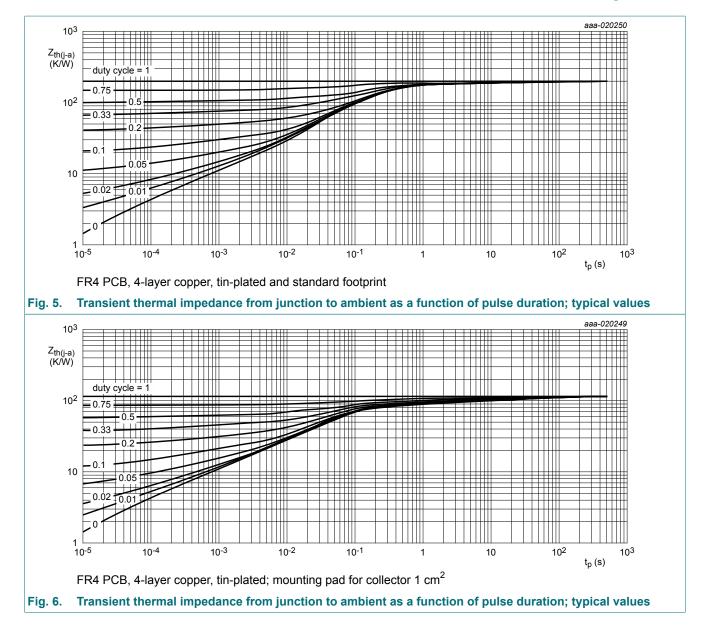
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 1 cm².

[3] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[4] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated; mounting pad for collector 1 cm².



60V, 600 mA PNP switching transistor



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10. Characteristics

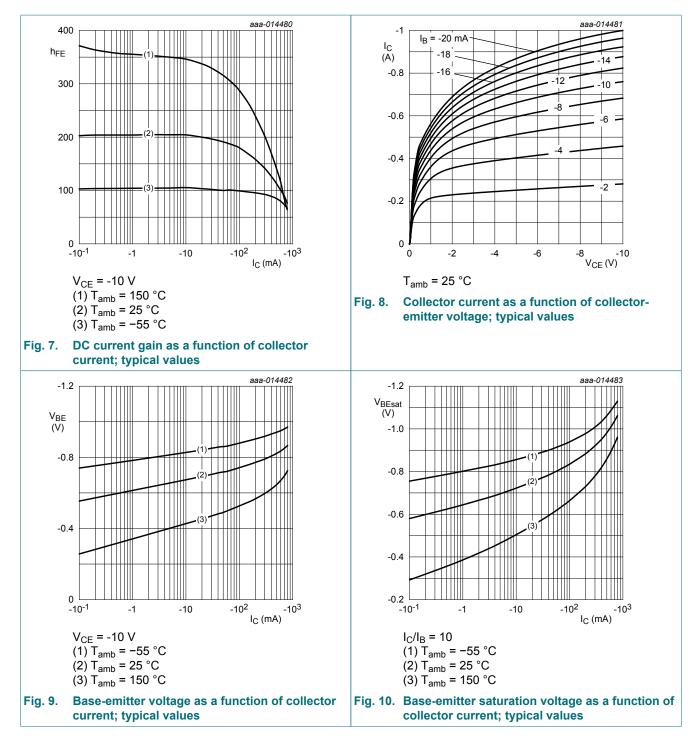
Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified

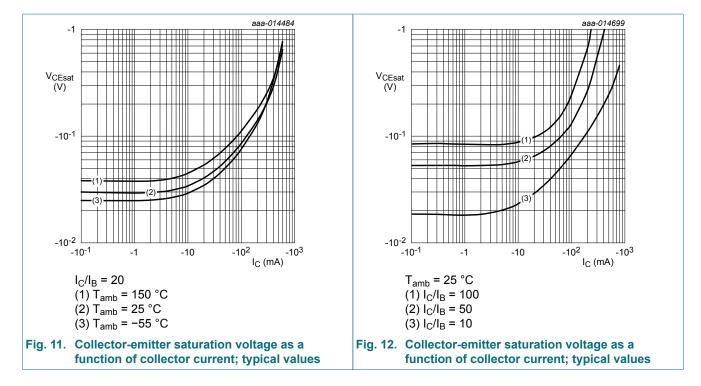
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μΑ; I _E = 0 Α		-60	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -2 mA; I _B = 0 A		-60	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _C = 0 A; I _E = -100 μA		-5	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = -50 V; I _E = 0 A		-	-	-10	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _j = 125 °C		-	-	-10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A		-	-	-50	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -100 μA		75	-	-	
		V _{CE} = -10 V; I _C = -1 mA		100	-	-	
		V _{CE} = -10 V; I _C = -10 mA		100	-	-	
		V _{CE} = -10 V; I _C = -150 mA	[1]	100	-	300	
		V _{CE} = -10 V; I _C = -500 mA	[1]	50	-	-	
V _{CEsat}	collector-emitter	I _C = -150 mA; I _B = -15 mA	[1]	-	-	-400	mV
	saturation voltage	I _C = -500 mA; I _B = -50 mA	[1]	-	-	-1.6	V
V _{BEsat}	base-emitter saturation	I _C = -150 mA; I _B = -15 mA	[1]	-	-	-1.3	V
	voltage	I _C = -500 mA; I _B = -50 mA	[1]	-	-	-2.6	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;		-	-	15	ns
t _r	rise time	I _{Boff} = 15 mA		-	-	30	ns
t _{on}	turn-on time			-	-	45	ns
t _s	storage time			-	-	300	ns
t _f	fall time			-	-	65	ns
t _{off}	turn-off time			-	-	365	ns
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz		-	-	8	pF
C _e	emitter capacitance	V _{EB} = -2 V; I _C = 0 A; i _c = 0 A; f = 1 MHz		-	-	30	pF
f _T	transition frequency	V _{CE} = -20 V; I _C = -50 mA; f = 100 MHz	[1]	-	210	-	MHz

[1] Pulsed test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$

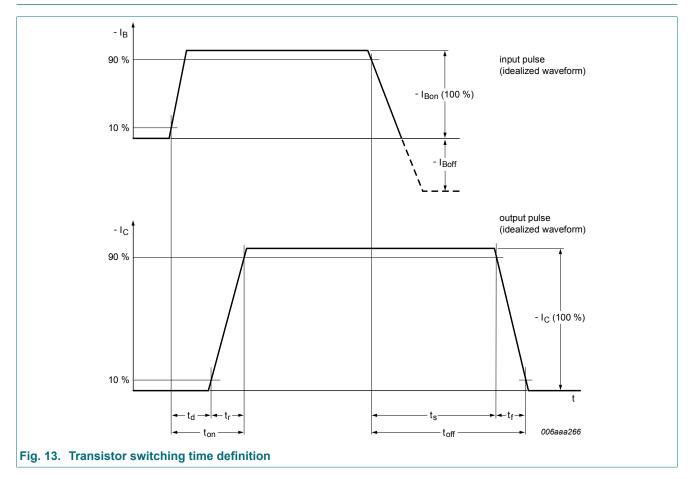
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60V, 600 mA PNP switching transistor

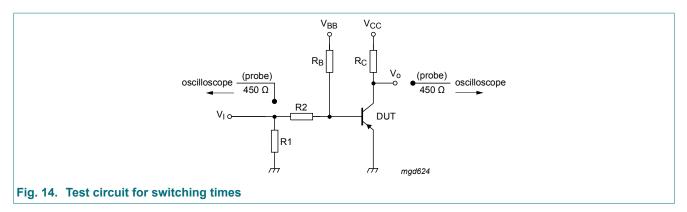


11. Test information



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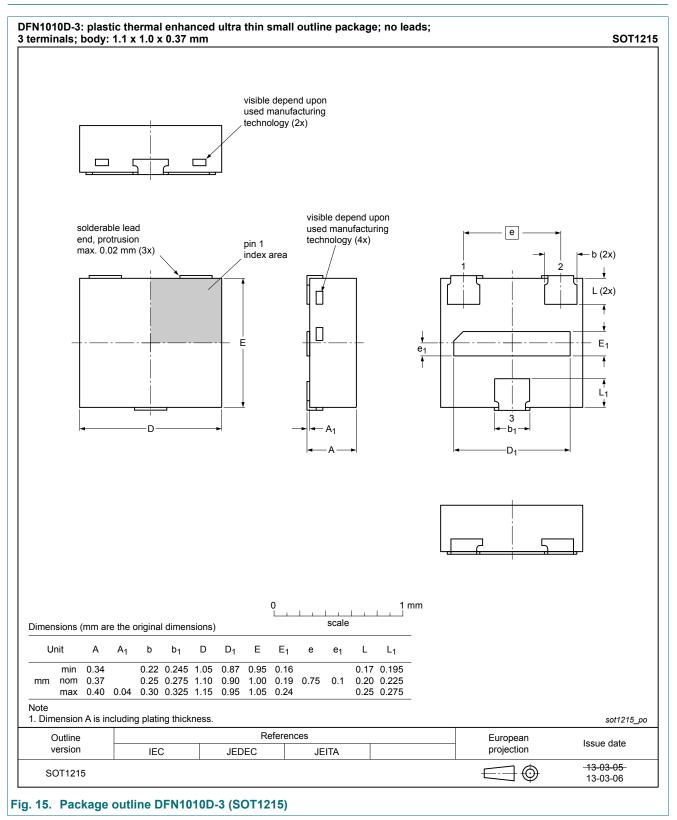
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Quality information

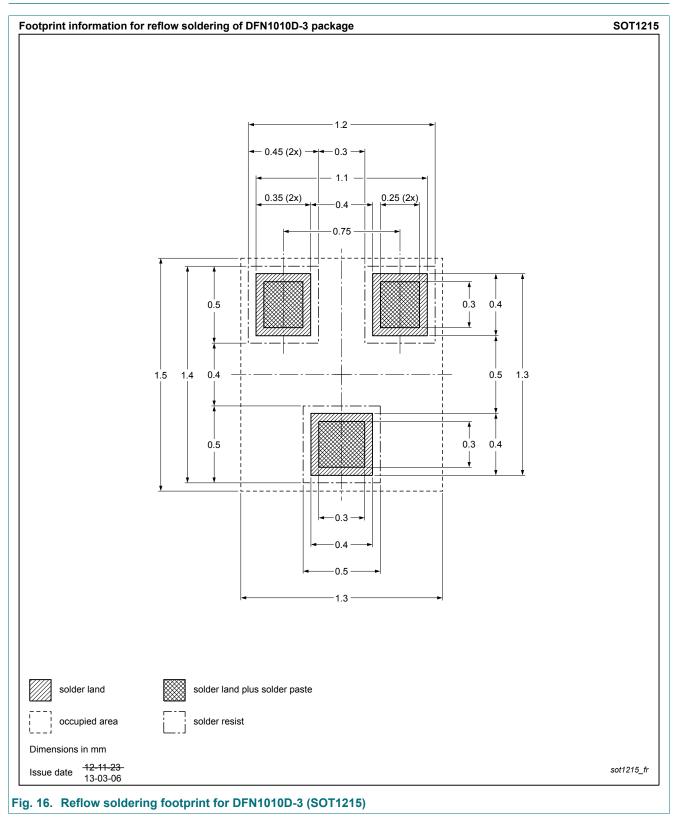
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



60V, 600 mA PNP switching transistor

13. Soldering



14. Revision history

Table 8. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PMBT2907AQA v.1	20180921	Product data sheet	-	-	

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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